

# CS609- System Programming Solved MCQS From Midterm Papers

May - 19 - 2013

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PSMD01

# MIDTERM EXAMINATION Spring 2012 CS609- System Programming

Question No: 1 (Marks: 1) - Please choose one DMA stands for
<ul> <li>▶ Direct Memory Access (Page 4)</li> <li>▶ Distinct Memory Access</li> <li>▶ Direct Module Access</li> <li>▶ Direct Memory Allocation</li> </ul>
Question No: 2 (Marks: 1) - Please choose one Standard PC operates in two modes in terms of memory which are
<ul> <li>▶ Real mode and Extended Mode</li> <li>▶ Base mode and Memory Mode</li> <li>▶ None of the given</li> <li>▶ Real mode and protected mode (Page 6)</li> </ul>
Question No: 3 (Marks: 1) - Please choose one UART stands for
<ul> <li>▶ Universal Asynchronous Receiver Transmitter (Page 107)</li> <li>▶ Universal Adjustment and Realigning Tool</li> <li>▶ Unconventional Assisted Recovery Team</li> <li>▶ None of these</li> </ul>
Question No: 4 (Marks: 1) - Please choose one Usually interrupt procedures are reentrant procedures especially those interrupt procedure compiled using C language compiler are reentrant.  True (Page 38)  False

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فداکے سواکس سے امیدمت رکو

Question No: 5 (Marks: 1) - Please choose one DTE is
<ul> <li>▶ Data terminal equipment</li> <li>▶ Data transmitting equipment</li> <li>▶ Dual terminal equipment</li> <li>▶ None of the given.</li> </ul>
Question No: 6 (Marks: 1) - Please choose one Interrupt Vector Table (IVT) in short is a bytes sized table.
► 1024 (Page 10) ► 2048 ► 3072 ► 4096
Question No: 7 (Marks: 1) - Please choose one Hardware Interrupts are
<ul> <li>▶ Preemptive</li> <li>▶ Non-Preemptive (Page 48)</li> <li>▶ Both Preemptive and Non-Preemptive</li> <li>▶ None of Given</li> </ul>
Question No: 8 (Marks: 1) - Please choose one Timer interrupt is a
<ul> <li>► Hardware Interrupt (Page 28)</li> <li>► Software Interrupt</li> <li>► Both of these</li> <li>► None of These</li> </ul>
Question No: 9 (Marks: 1) - Please choose one Int 14H can be used to set the line parameter of the UART or COM port.
<ul> <li>Service # 0 (Page 119)</li> <li>▶ Service # 1</li> <li>▶ Service # 2</li> <li>▶ None of the given options</li> </ul>
ه واطع به سه هشکا بکام این باه از 7اد. به سه کنی از بکام ده بر در بر دارد کام

دنیامیں سبسے مشکل کام اپنی اصلاح اور سبسے آسان کام دوسروں پر کلتہ چینی کرناہے

Question No: 10 (Marks: 1) - Please choose one  The keyboard makes use of interrupt number for its input operations.  ▶9 (Page 34)  ▶10  ▶11  ▶12
Question No: 11 (Marks: 1) - Please choose one In self test mode the output of the UART is routed to its input.
► True (Page 117) ► False
Question No: 12 (Marks: 1) - Please choose one In case of synchronous communication a timing signal is required to identify the start and end of a bit.
► True (Page 105) ► False
Question No: 13 (Marks: 1) - Please choose one Register can be used to divide frequency is
➤ Counter Register (Page 69) ➤ Accumulator Register ➤ None of these
Question No: 14 (Marks: 1) - Please choose one Only ports are important from programming point of view.
<ul> <li>70 and 71H (Page 141)</li> <li>▶ 71 and 72H</li> <li>▶ 70 and 72H</li> <li>▶ 72 and 73H</li> </ul>
Question No: 15 (Marks: 1) - Please choose one Which port is known as Data Port
►60H (Page 177) ►61H ►64H ►69H

بری محبت سے تنہائی بہتر ہے اور تنہائی سے نیک محبت بہتر ہے

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**Question No: 16** (Marks: 1) - Please choose one LPTs can be swapped. ►True (Page 92) **►** False **Question No: 17** (Marks: 1) - Please choose one PPI is used to perform parallel communication ►True (Page 81) **►** False **Question No: 18** (Marks: 1) - Please choose one is used to control the printer via the BIOS ►Int 16H ► Int 17H (Page 84) ►Int 18H ►Int 19H **Question No: 19** (Marks: 1) - Please choose one bit is cleared to indicate the low nibble is being sent. ▶D1 **▶**D2 **▶**D3 **▶D4** (Page 104) **Question No: 20** (Marks: 1) - Please choose one DSR stands for **▶** Data set ready (Page 111) ► Data service ready ► Data stock ready

► None of the given



# MIDTERM EXAMINATION Fall 2010 CS609- System Programming

Question No: 1 (Marks: 1) - Please choose one There are two main types of interrupts namely
<ul> <li>▶ PC based and Window based</li> <li>▶ Hardware based and Kernal based</li> <li>▶ Hardware interrupts and Software interrupts (Page 10)</li> <li>▶ None of the given</li> </ul>
Question No: 2 (Marks: 1) - Please choose one  To set the interrupt vector means is to change the double word sized interrupt vector within the IVT.
►True (Page 22) ►False
Question No: 3 (Marks: 1) - Please choose one  The service number is usually placed in the register.  AL  CL  AH (Page 26)  AX
Question No: 4 (Marks: 1) - Please choose one  The keyboard makes use of interrupt number for its input operations.  > 9 (Page 34) > 10 > 11 > 12
Question No: 5 (Marks: 1) - Please choose one The service is called the keyboard hook service.
► 15H/2FH ► 15H/4FH (Page 44) ► 15H/FFH    2   2   3   4   5   5   5   5   5   5   5   5   5

Question No: 6 (Marks: 1) - Please choose one The BIOS interrupt can be used to configure RTC.	
<ul><li>▶1AH (Page 136)</li><li>▶2AH</li><li>▶3AH</li><li>▶4AH</li></ul>	
Question No: 7 (Marks: 1) - Please choose one The interval timer can operate in modes.	
<ul> <li>▶ Five</li> <li>▶ Seven</li> <li>▶ Four</li> <li>▶ Six (Page 72)</li> </ul>	
Question No: 8       ( Marks: 1 ) - Please choose one         is Disk interrupt.         ▶ 10H         ▶ 11H         ▶ 13H       (Page 42)         ▶ 14H	
Question No: 9 (Marks: 1) - Please choose one PPI stands for  ▶ Parallel Programmable interface ▶ Peripheral Programmable interface (Page 76)  ▶ Port Programmable interface ▶ None of the given	
Question No: 10 (Marks: 1) - Please choose one Int is used to control the printer via the BIOS.  ▶ 17H (Page 84)  ▶ 18H  ▶ 20H  ▶ 21H	
Question No: 11 (Marks: 1) - Please choose one Counter register can be used to divide clock signal.  ► True (Page 69)  ► False	

Question No: 12 (Marks: 1) - Please choose one DCE stands for
<ul> <li>▶ Data communication equipment (Page 109)</li> <li>▶ Distributed Computing Environment</li> <li>▶ Data Communications Equipment</li> <li>▶ Data Carrier Equipment</li> </ul>
Question No: 13 (Marks: 1) - Please choose one is used to identify the cause of interrupt.
<ul> <li>► Interrupt ID Register (Page 116)</li> <li>► PC Register</li> <li>► AC Register</li> <li>► None of All These</li> </ul>
Question No: 14 (Marks: 1) - Please choose one The bit number of the coprocessor control word is the interrupt enable flag.
▶7 (Page 168) ▶8 ▶9 ▶6
Question No: 15 (Marks: 1) - Please choose one There are kinds of serial communication.
▶2 (Page 105) ▶3 ▶4 ▶5
Question No: 16 (Marks: 1) - Please choose one store the base address for LPT1.
► 40:00H ► 40:02H ► 40:08H (Page 92) ► 40:1AH
زندگی میں کامیابی کا بھی دازہے کہ پریشانیوں سے پریشان مت بنو

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### **Question No: 17** (Marks: 1) - Please choose one

The amount of memory above conventional memory (extended memory) can be determined using the service

TOREIGE

- ► 15H/88H (Page 162)
- ►16H/88H
- ► 17H/88H
- ►21H/88H

# Question No: 18 (Marks: 1) - Please choose one

The output on the monitor is controlled by a controller called \_\_\_\_\_ within the PC.

- ► Video controller (Page 30)
- ► Bus controller
- ▶ Ram controller
- ► None of the given

### Question No: 19 (Marks: 1) - Please choose one

The keyboard input character scan code is received at \_\_\_\_ port.

- ▶60H (Page 179)
- ▶61H
- ► 62H
- ▶63H

# Question No: 20 (Marks: 1) - Please choose one

\_\_\_\_\_ is LED control byte.

- ►0xFD
- **▶**0xED (Page 181)
- ►0xFF
- ►0xEE

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# **MIDTERM EXAMINATION**

Spring 2009

CS609- System Programming (Session - 1)

Question No: 1 (Marks: 1) - Please choose one There are two main types of interrupts, namely
<ul> <li>▶ PC based and Window based</li> <li>▶ Hardware based and Kernal based</li> <li>▶ Hardware interrupts and Software interrupts (Page 10)</li> <li>▶ None of the given</li> </ul>
Question No: 2 (Marks: 1) - Please choose one Standard PC operates in two modes in terms of memory which are
<ul> <li>▶ Real mode and Extended Mode</li> <li>▶ Base mode and Memory Mode</li> <li>▶ None of the given</li> <li>▶ Real mode and protected mode (Page 6) rep</li> </ul>
Question No: 3 (Marks: 1) - Please choose one  BPB stands for  BIOS parameter block (Page 243)  BIOS processing block  Base processing block  BIOS partition block
Question No: 4 (Marks: 1) - Please choose one The Function of I/O controller is to provide
► I/O control signals   ► Buffering   ► Error Correction and Detection   ► All of given (Page 5)
Question No: 5 (Marks: 1) - Please choose one  IVT is a table containing byte entries each of which is a far address of an interrupt service routine.  > 2  • 4 (Page 20)  • 8  • 16
عقل مند کہتاہے میں کھے نہیں جانتا جبکہ بے و قوف کہتاہے کہ میں سب کھے جانتا ہوں

Question No: 6 (Marks: 1) - Please choose one Each paragraph in keep function is bytes in size.
▶4 ▶8 ▶16 (Page 24) ▶32
Question No: 7 (Marks: 1) - Please choose one Interrupt 9 usually reads the from keyboard.  ► ASCII code
➤ Scan code (Page 34)  ➤ Both ASCII and Scan code  ➤ None of the above
Question No: 8 (Marks: 1) - Please choose one A software interrupt does not require EOI (End of interrupt).  True (Page 49)  False
Question No: 9 (Marks: 1) - Please choose one To store each character in keyboard buffer bytes are required.  2 (Page 54)  4  6  8
Question No: 10 (Marks: 1) - Please choose one Interrupt is empty; we can use its vector as a flag.
►9H ►13H ►15H ►65H (Page 65)
Question No: 11 (Marks: 1) - Please choose one Command register is an bit register
▶4 ▶8 (Page 71) ▶16 ▶32
جولو گوں کے سامنے فخر کر تاہے دہ لو گوں کی نظروں سے گر جاتا ہے

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Question No: 12 (Marks: 1) - Please choose one The interval timer can operate inmodes.	
► Three Four Five	
►Six (Page 72) rep	
Question No: 13 (Marks: 1) - Please choose one  The following command "outportb (0x61,inportb(0x61)   3);" will  ► Turn on the speaker  ► Turn off the speaker  ► Toggle the speaker  ► None of the above	
Question No: 14 (Marks: 1) - Please choose one The PPI acts as an interface between the CPU and a parallel	
►I/O device (Page 83) ►CPU ►BUS	
► None of Given	
Question No: 15 (Marks: 1) - Please choose one DTE is	
<ul> <li>Data terminal equipment (Page 109) rep</li> <li>Data transmitting equipment</li> <li>Dual terminal equipment</li> </ul>	
None of the given.	
Question No: 16 (Marks: 1) - Please choose one DSR stands for	
<ul> <li>▶ Data set ready (Page 111) rep</li> <li>▶ Data service ready</li> <li>▶ Data stock ready</li> </ul>	
None of the given  Question No: 17 (Marks: 1) - Please choose one  Language of the given	
In self test mode the output of the UART is routed to its input.	
► True (Page 117) rep  ► False	
عقل مندائے عیب خو دریکھتاہے اور ہو قوفوں کے عیب دنیاد پکھتی ہے	

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Question No: 18 (Marks: 1) - Please choose one

Interrupt \_\_\_\_\_ is used to get or set the time.

- ►0AH
- ► 1AH (Page 136)
- ►2AH
- ►3AH

Question No: 19 (Marks: 1) - Please choose one

is used to set time.

- ► 1A/02H
- ► 1A/03H (Page 138)
- ► 1A/04H
- ► 1A/05H

Question No: 20 (Marks: 1) - Please choose one

Communication between keyboard and keyboard controller is \_\_\_\_\_

- ► Asynchronous serial
- ► Synchronous serial (not sure)
- ▶ Parallel communication
- ► None of the given

MIDTERM EXAMINATION

Spring 2009

**CS609- System Programming (Session - 1)** 

Question No: 1 (Marks: 1) - Please choose one

Following is not a method of I/O

- ▶ Programmed I/O
- ► Interrupt driven I/O
- ► Hardware Based I/O (Page 4)
- ► None of given

برصورت پہر ہبر صور درائے ہے بہر ہ

Question No: 2 (Marks: 1) - Please choose one The Function of I/O controller is to provide  I/O control signals  Buffering Error Correction and Detection All of given (Page 5) rep
Question No: 3 (Marks: 1) - Please choose one Which of the following are types of ISR
<ul> <li>▶ BIOS (Basic I/O service ) ISR</li> <li>▶ DOS ISR</li> <li>▶ ISR provided by third party device drivers</li> <li>▶ All of the given (Page 13)</li> </ul>
Question No: 4 (Marks: 1) - Please choose one Interrupt service number is usually placed in register.  ► CH  ► CL  ► AH (Page 26) rep  ► AL
Question No: 5 (Marks: 1) - Please choose one NMI Stand for
<ul> <li>Non Maskable Interrupt</li> <li>Non Multitude Interrupt</li> <li>Non Maskable Instruction</li> <li>None of Given</li> </ul>
Question No: 6 (Marks: 1) - Please choose one A single interrupt controller can arbitrate among different devices.  >4 >6 >8 (Page 47) >10
Question No: 7 (Marks: 1) - Please choose one  Hardware Interrupts are  ▶ Preemptive  Non-Preemptive (Page 48) rep  ▶ Both Preemptive and Non-Preemptive  ▶ None of Given

### Question No: 8 (Marks: 1) - Please choose one

The microprocessor package has many signals for data. Below are some in Correct priority order (Higher to Lower).

- ► Reset, Hold, NMI, INTR (Page 46)
- ►NMI, INTR, Hold, Reset
- ► INTR,NMI,Reset,Hold
- ► None of the Given

#### Question No: 9 (Marks: 1) - Please choose one

The interval timer can operate in \_\_\_\_modes.

- **►** Three
- Four
- **▶** Five
- ►Six (Page 72) rep

Question No: 10 (Marks: 1) - Please choose one

PPI stands for .

- ► Parallel Programmable interface
- ► Peripheral Programmable interface (Page 76) rep
- ► Port Programmable interface
- ► None of the given

#### **Question No: 11** (Marks: 1) - Please choose one

The following command "outportb (0x61,inportb(0x61) & 0xFC);" will

- ► Turn on the speaker (Page 74)
- ► Turn off the speaker
- ► Toggle the speaker
- ► None of the given

#### Question No: 12 (Marks: 1) - Please choose one

The PPI acts as an interface between the CPU and a parallel \_\_\_\_\_

- ►I/O device (Page 83) rep
- **▶**CPU
- **BUS**
- ► None of Given



**Question No: 13** (Marks: 1) - Please choose one BIOS DO NOT support \_\_\_\_\_. ►LPT1 ►LPT2 ►LPT3 ►LPT4 (Page 91) **Question No: 14** (Marks: 1) - Please choose one \_\_ bit is cleared to indicate the low nibble is being sent. ▶D1 **▶**D2 **▶** D3 **▶D4** (Page 101) **Ouestion No: 15** (Marks: 1) - Please choose one The bit \_\_\_\_\_ of Line control register in UART, if cleared will indicate that DLL is the data register. ▶1 ▶3 ▶5 ▶7 (Page 114) Ouestion No: 16 (Marks: 1) - Please choose one Int 14H \_\_\_\_\_ can be used to set the line parameter of the UART or COM port. ► Service # 0 (Page 119) rep ➤ Service # 1 ➤ Service # 2 ➤ Service # 3 Question No: 17 (Marks: 1) - Please choose one used to determine the amount of conventional memory interfaced with the processor in kilobytes. ► INT 10 H ► INT 11 H ► INT 12 H (Page 162) ►INT 13 H

جو مخض ناکامیوں سے ڈر کر بھا گتاہے کامیابی اس سے ڈر کر بھا گتی ہے

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**Question No: 18** (Marks: 1) - Please choose one Bit number of coprocessor control word is the Interrupt Enable Flag. ▶7 (Page 168) ▶8 >9 ▶10 **Question No: 19** (Marks: 1) - Please choose one To distinguish 486 with Pentium CPUID Test is used. **► True** (Page 166) **►** False **Ouestion No: 20** (Marks: 1) - Please choose one is LED control byte.  $\triangleright 0xF3$ ► 0xED (Page 181) rep ►0xE5 ►0xFF MIDTERM EXAMINATION Spring 2009 **CS609- System Programming (Session - 1) Ouestion No: 1** (Marks: 1) - Please choose one The memory addresses of COM ports remain same for all computers **►**True ► False **Question No: 2** (Marks: 1) - Please choose one Int service 0 can be used to set the line parameter of the UART or COM port. ►14H (Page 119) ▶15H ▶13H ► None of the given option انسان د کھ نہیں دیتے بلکہ انسانوں سے وابستہ امیریں د کھ دی ہیں

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# **Question No: 3** (Marks: 1) - Please choose one In case of synchronous communication a timing signal is required to identify the start and end of a bit. **►**True (Page 105) rep **►** False Question No: 4 (Marks: 1) - Please choose one BIOS support \_\_UARTS as COM ports. **6** ▶4 (Page 113) ▶3 ▶2 (Marks: 1) - Please choose one **Ouestion No: 5** function uses the COM port number to receive a byte from the COM port using BIOS services. The ▶ recievebyte() ▶ initialize () ▶ receive() ► recievechar() (Page 125) **Question No: 6** (Marks: 1) - Please choose one DTE is ► Data terminal equipment (Page 109) rep ► Data transmitting equipment ► Dual terminal equipment ► None of above **Question No: 7** (Marks: 1) - Please choose one Int 14H\_\_\_\_\_ can be used to send a byte ➤ Service#0 ► Service#1 (Page 121) ➤ Service#2 ► None of the given option. **Question No: 8** (Marks: 1) - Please choose one Int 14H\_\_\_\_ can be used to set the line parameter of the UART or COM port ► Service#0 (Page 119) rep ➤ Service#1 ➤ Service#2 ► None of the given option البرين تجربه وه ب جس سے نفيحت ماصل مو

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	(Marks: 1) - Please choose one tion initialize the COM port whose number is passed as parameter using BIOS services.
►Initialize	com()
►Initialize	e() (Page 125)
► Recievec	har()
► None of t	these option
Question No: 10	( Marks: 1 ) - Please choose one
whenever rece	eived indicates the start of communication and whenever received indicates a
emporary pause in	the communication.
►XON & XOFF	(Page 135)
► XOFF & XON	
► XON & YOFF	
► YON & XOFF	
	( Marks: 1 ) - Please choose one ed to identify cause of interrupt.
► Interrupt	enable register
	et ID register (Page 116) rep
	Status register
► None of t	the above
	( Marks: 1 ) - Please choose one ach byte is needed to be encapsulated in start and end.
<b>►</b> Synchror	nous communication
	onous communication (Page 106)
▶Both	
► None of	given
Question No: 13	( Marks: 1 ) - Please choose one
is a devi	ce incorporated into the PC to update time even if the computer is off.
►Clock co	unter
▶ROM	
KUM	
► ROM ► Clock	

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**Question No: 14** (Marks: 1) - Please choose one

There are two type of communication synchronous and Anti Synchronous

- **►**True
- ► False (Page 105)

Question No: 15 (Marks: 1) - Please choose one

The \_\_\_\_\_ function simply enables the self test facility within the modem control register

- ►STOn()
- ► SelfTest()
- ► SelfTestOn() (Page 127)
- ► Non of these

Question No: 16 (Marks: 1) - Please choose one

DCE is \_\_\_\_\_

- ► Data communication equipment (Page 109) rep
- ► Dual communication equipment
- ► Data clearing equipment
- ► None of given

Question No: 17 (Marks: 1) - Please choose one

REGS is a Union

- **▶**True
- **►** False

Question No: 18 (Marks: 1) - Please choose one

In self test mode the output of the UART is routed to its input

- ►True (Page 117) rep
- ► False

تم اچھا کروزمانہ تم کوبر استھے ہے اس سے جھڑے کہ تم براکرواور زمانہ تم کواچھا تھے

# **CS609 System Programming**

Mid Term Examination - Spring 2006

Question No. 1 Marks: 2

If CPUID instruction is not present then the processor can be a

- **▶** 486 processor (Page 166)
- ▶ 386 processor
- ▶ 286 processor
- ► All of the above

**Question No. 2 Marks: 2** 

On the execution of IRET instruction, number of bytes popped from stack is

- ► 4 bytes
- ► 6 bytes
- ► 8 bytes Click here for detail
- ▶ 10 bytes

Question No. 3 Marks: 2

Keyboard Status Byte is located at the address

- ► 0040:0000H
- ► 0040:0013H
- ► 0040:0015H
- ► 0040:0017H (Page 29)

**Question No. 4 Marks: 2** 

If we use keep (0, 1000) in a TSR program, the memory allocated to it is

- ▶ 64000 bytes
- ➤ 32000 bytes
- ▶ 16000 bytes
- ▶ 80000 bytes

**Question No. 5 Marks: 2** 

Maximum number of interrupts in a standard PC is

- **▶** 64
- **▶** 128
- **▶ 256** (Page 10)
- **▶** 512

# CS609 – Solved Quizzes (1 & 2)

# Quiz No.1

Question: 1 of 10 (Marks: 1) - Please choose one Total No. of bytes that can be stored in Keyboard Buffer is\_\_\_\_. ▶16 ▶32 (Page 54) **▶**64 ▶128 Question: 2 of 10 (Marks: 1) - Please choose one If printer is \_\_\_\_\_ then printer sends back the ACK signal to the printer interface ▶idle (Page 97) **▶** busy ► Out of paper ► None of the given Ouestion: 3 of 10 (Marks: 1) - Please choose one The baud rate is set in accordance with the divisor value loaded within the UART internal registers base +0 and base +1. **TRUE** (Page 114) **►**FALSE **Ouestion:** 4 of 10 (Marks: 1) - Please choose one Int 14H \_\_\_\_\_ can be used to receive a byte. ➤ Service # 0 ➤ Service # 1 ► Service # 2 (Page 121) ► None of the given options Ouestion: 5 of 10 (Marks: 1) - Please choose one The BIOS interrupt 0x1AH can be used to configure real time clock **►**True (Page 136) ► False

**Question:** 6 of 10 (Marks: 1) - Please choose one

----is used to read date from RTC

- ►1A\02H
- ►1A\03H
- ► 1A\04H (Page 138)
- ►1A\05H

Question: 7 of 10 (Marks: 1) - Please choose one

The ----- function receive a byte and COM port number is passed as parameter using BIOS service

- ► Receivebyte ();
- ► Receive ();
- ► Receivechar (); (Page 125)
- ► None of the given option

**Ouestion: 8 of 10** (Marks: 1) - Please choose one

----- whenever receive indicates the start of communication ....... whenever receive indicates the end of communication

- ►XON\XOFF (Page 135)
- ► XOFF\XON
- ► XON\YOFF
- ► YON\XOFF

Question: 9 of 10 (Marks: 1) - Please choose one

----- is used to read time from RTC

- ► 1A\02H (Page 137)
- ►1A\03H
- ►1A\04H
- ►1A\05H

**Ouestion:** 10 of 10 (Marks: 1) - Please choose one

----- is used to set time from RTC

- ►1A\02H
- ►1A\03H (Page 138)
- ►1A\04H
- ► 1A\05H

انسان کے لئے بری محبت سے بڑھ کربری کوئی چیز نہیں

# Quiz No.2

```
Question: 1 of 10
                    (Marks: 1) - Please choose one
Software based flow control make use of ----- control characters
► Xon
►XOFF
▶ Both (Page 135)
► None
                  (Marks: 1) - Please choose one
Ouestion: 2 of 10
What will be the value of DL register when we are accessing C drive using undocumented service 21H/32H?
▶0
▶1
▶2
▶3
     (Page 249)
Question: 3 of 10
                    (Marks: 1) - Please choose one
Set the Interrupt vector means to change the double word sized interrupt vector within IVT.
►True
          (Page 22)
▶ False
Ouestion: 4 of 10
                  (Marks: 1) - Please choose one
If keyboard buffer is empty the head and tail points at the same location.
►True (Page 55)
► False
Question: 5 of 10 (Marks: 1) - Please choose one
Standard PC can have PPI.
▶1
▶4 (Page 84)
▶8
▶16
Question: 6 of 10 (Marks: 1) - Please choose one
By cascading two DMAs _____ bits can be transferred.
▶4
▶8
▶16 (Page 186)
▶32
```

Question: 7 of 10 (Marks: 1) - Please choose one PPI interconnection bits is cleared to indicate low nibble is being sent.
<ul> <li>D1</li> <li>D2</li> <li>D3</li> <li>D4 (Page 101)</li> </ul>
Question: 8 of 10 (Marks: 1) - Please choose one File can be viewed as collection of clusters or blocks.
<ul> <li>▶ Physically (Page 256)</li> <li>▶ Logically</li> <li>▶ Both physically and logically</li> <li>▶ None</li> </ul>
Question: 9 of 10 (Marks: 1) - Please choose one Operating system name contains bytes in boot block.
►3 ►5 ►8 (Page 257) ►11
Question: 10 of 10 (Marks: 1) - Please choose one File system used in CD's is file system
<ul> <li>Contiguous         <ul> <li>http://www.pgallert.de/english/SysAdmin/OS/file.htm</li> </ul> </li> <li>Chained         <ul> <li>Indexed</li> <li>None</li> </ul> </li> </ul>
Question: 1 of 10 (Marks: 1) - Please choose one means typematic rate will be sent in next byte.
<ul> <li>▶ 0xF3 (Page 180)</li> <li>▶ 0xF4</li> <li>▶ 0xF5</li> <li>▶ 0xF6</li> </ul>
خاموشی غصے کا جہرین علاق ہے

# Question: 2 of 10 (Marks: 1) - Please choose one To access the block within cluster using BIOS services the cluster number should be converted into \_\_\_\_\_. ► CHS **►**LBA **►LSN** (Page 258) ► None of the given Question: 3 of 10 (Marks: 1) - Please choose one Display device (Monitor) performs \_\_\_\_\_\_I/O. ► memory mapped (Page 30) ► Isolated ▶ Both of above ► None of these **Question:** 4 of 10 (Marks: 1) - Please choose one Timer interrupt occurs \_\_\_\_\_ times every second by means of hardware. ▶18.2 (Page 28) ▶16.2 ▶15.2 ▶14.2 Ouestion: 5 of 10 (Marks: 1) - Please choose one An I/O device cannot be directly connected to the busses so controller is placed between CPU and I/O. ►True (Page 83) **►** False Question: 6 of 10 (Marks: 1) - Please choose one Keyboard uses port \_\_\_\_ as status port. ► 64H (Page 177) ▶66H ▶67H ▶69H Question: 7 of 10 (Marks: 1) - Please choose one Tail of keyboard should get to get the start of buffer. ►True (Page 55) **►** False جھوٹ رزق کو کھاجاتاہے

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Question: 4 of 10 (Marks: 1) - Please choose one Control information in files is maintained using
<ul> <li>▶ BPB</li> <li>▶ DPB</li> <li>▶ FCB (Page 256)</li> <li>▶ FPB</li> </ul>
Question: 5 of 10 (Marks: 1) - Please choose one No. of bytes are used to store the character in the keyboard buffer.
► 1 ► 2 (Page 54) ► 4 ► 8
Question: 6 of 10 (Marks: 1) - Please choose one The service # is not used in any interrupt.
►01 ►02 ►03 ►FF
Question: 7 of 10 (Marks: 1) - Please choose one At IRQ 7 Interrupt # is used.
<ul> <li>▶ 0x0A</li> <li>▶ 0x0B</li> <li>▶ 0x0C</li> <li>▶ 0x0F (Page 95)</li> </ul>
Question: 8 of 10 (Marks: 1) - Please choose one  We have set the bit No. 7 of IMR(Interrupt Mask Register) to unmask the Interrupt so that interrupt can occur at line.
► 0xf ,IRQ 7  ► 0xa, IRQ 6  ► 0x8, IRQ 5  ► 0x6, IRQ 2
افضل انسان وہ ہے جو اپنی اصلاح کی کوشش کر تاہے

#### **Question:** 9 of 10 (Marks: 1) - Please choose one

If we want to produce the grave voice from speaker phone then we have to load the \_\_\_\_ divisor values at Port

- ▶ high, 0x42
- ▶low, 0x22
- ► high, 0x22
- ▶low, 0x42

# Question: 10 of 10 (Marks: 1) - Please choose one

In order to produce the sound from PC internal Speaker we have to load the\_\_\_bit divisor value at the \_\_\_port.

- ▶8, 0x21
- ▶ 16, 0x42
- ▶32, 0x22
- ▶ 64, 0x32

### **Question:** 1 of 10 (Marks: 1) - Please choose one

In counter register bit no. 3 changes its value between 0 and 1 with in \_\_\_\_clock cycles

- ▶1
- ▶2
- ▶4
- ▶ 16 (Page 69)

# Question: 2 of 10 (Marks: 1) - Please choose one

In keyboard status byte bit no. 2 and 3 are used for ctrl and alt keys respectively. which of the following condition is used to check that Ctrl + Alt keys are pressed. Where: unsigned char far \* scr = (unsigned char far \*)(0x00400017);

- $\blacktriangleright$  if (((\*scr)&12)==12)
- $\triangleright$  if (((\*scr)&8)==8)
- $\blacktriangleright$  if (((\*scr)&4)==4)
- $\triangleright$  if (((\*scr)&2)==2)

اپئیس منی اور اللہ کی سر منی میں فرق کانام غم ہے اس سے پہلے کہ متہمیں شہوت فنٹے میں ڈالے نکاح کر لو وہ لوگ مبارک ہیں جو الفاظ سے تفییحت نہیں کرتے باکہ عمل سے کرتے ہیں