



CS501- Advance Computer Architecture
Solved MCQS
From Final term Papers

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PSMD01

FINAL TERM EXAMINATION
Fall 2008
CS501- Advance Computer Architecture

Question No: 1 (Marks: 1) - Please choose one

Which one of the following is the memory organization of SRC processor?

- $2^8 * 8$ bits
- $2^{16} * 8$ bits
- **$2^{32} * 8$ bits (Page 46)**
- $2^{64} * 8$ bits

Question No: 2 (Marks: 1) - Please choose one

Type A format of SRC uses -----instructions

- **Two (Page 47)**
- three
- four
- five

Question No: 3 (Marks: 1) - Please choose one

The instruction -----will load the register R3 with the contents of the memory location M [PC+56]

- Add R3, 56
- lar R3, 56
- **ldr R3, 56 (Page 47)**
- str R3, 56

دنیا میں سب سے مشکل کام اپنی اصلاح اور سب سے آسان کام دوسروں پر نکتہ چینی کرنا ہے

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Question No: 4 (Marks: 1) - Please choose one

Which format of the instruction is called the accumulator?

- 3-address instructions
- 3-address instructions
- 2-address instructions
- **1-address instructions** (Page 32)
- 0-address instructions

Question No: 5 (Marks: 1) - Please choose one

Which one of the following are the **code size** and the **Number of memory bytes** respectively for a 2-address instruction?

- 4 bytes, 7 bytes
- **7 bytes, 16 bytes** (Page 36)
- 10 bytes, 19 bytes
- 13 bytes, 22 bytes

Question No: 6 (Marks: 1) - Please choose one

Which operator is used to name registers, or part of registers, in the Register Transfer Language?

- **:=** (Page 66)
- &
- %
- ©

Question No: 7 (Marks: 1) - Please choose one

The transmission of data in which each character is self-contained units with its own start and stop bits is -----

- **Asynchronous** [Click here for detail](#)
- Synchronous
- Parallel
- All of the given options

Question No: 8 (Marks: 1) - Please choose one

Circuitry that is used to move data is called -----

- **Bus** [click here for detail](#)
- Port
- Disk
- Memory

خدا کے سوا کسی سے امید مت رکھو

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Question No: 9 (Marks: 1) - Please choose one

Which one of the following is **NOT** a technique used when the CPU wants to exchange data with a peripheral device?

- Direct Memory Access (DMA).
- Interrupt driven I/O
- Programmed I/O
- **Virtual Memory (Page 268)**

Question No: 10 (Marks: 1) - Please choose one

Every time you press a key, an interrupt is generated. **This is an example of**

- **Hardware interrupt (Page 275)**
- Software interrupt
- Exception
- All of the given

Question No: 11 (Marks: 1) - Please choose one

The interrupts which are pre-programmed and the processor automatically finds the address of the ISR using interrupt vector table are

- Maskable
- Non-maskable
- Non-vectored
- **Vectored (Page 277)**

Question No: 12 (Marks: 1) - Please choose one

Which is the last instruction of the ISR that is to be executed when the ISR terminates?

- **IRET (Page 278)**
- IRQ
- INT
- NMI

Question No: 13 (Marks: 1) - Please choose one

If NMI and INTR both interrupts occur simultaneously, then which one has the precedence over the other

- **NMI (Page 279)**
- INTR
- IRET
- All of the given

بری صحبت سے تنہائی بہتر ہے اور تنہائی سے نیک صحبت بہتر ہے

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Question No: 14 (Marks: 1) - Please choose one

Identify the following type of serial communication error condition:

The prior character that was received was not still read by the CPU and is over written by a new received character.

- Framing error
- Parity error
- **Overrun error** (Page 240)
- Under-run error

Question No: 15 (Marks: 1) - Please choose one

-----the device usually means reading its status register every so often until the device's status changes to indicate that it has completed the request.

- Executing
- Interrupting
- Masking
- **Polling** [click here for detail](#)

Question No: 16 (Marks: 1) - Please choose one

Which I/O technique will be used by a sound card that may need to access data stored in the computer's RAM?

- Programmed I/O
- Interrupt driven I/O
- **Direct memory access(DMA)** [Click here for detail](#)
- Polling

Question No: 17 (Marks: 1) - Please choose one

For increased and better performance we use _____ which are usually made of glass.

- Coaxial Cables
- Twisted Pair Cables
- **Fiber Optic Cables** (Page 390)
- Shielded Twisted Pair Cables

Question No: 18 (Marks: 1) - Please choose one

In _____ if we find some call party busy we can have provision of call waiting.

- **Delay System** (Page 381)
- Loss System
- Single Server Model
- None of the given

اللہ کا خوف سب سے بڑی دانائی ہے

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Question No: 19 (Marks: 1) - Please choose one

In ____ technique memory is divided into segments of variable sizes depending upon the requirements.

- Paging
- **Segmentation** (Page 365)
- Fragmentation
- None of the given

Question No: 20 (Marks: 1) - Please choose one

For a request of data if the requested data is not present in the cache, it is called a _____

- **Cache Miss** (Page 358)
- Spatial Locality
- Temporal Locality
- Cache Hit

Question No: 21 (Marks: 1) - Please choose one

An entire _____ memory can be erased in one or a few seconds which is much faster than EPROM.

- PROM
- Cache
- EEPROM
- **Flash Memory** (Page 356)

Question No: 22 (Marks: 1) - Please choose one

_____ chips have quartz windows and by applying ultraviolet light data can be erased from them.

- PROM
- Flash Memory
- **EPROM** (Page 356)
- EEPROM

Question No: 23 (Marks: 1) - Please choose one

The _____ signal coming from the CPU tells the memory that some interaction is required between the CPU and memory.

- **REQUEST** (Page 350)
- COMPLETE
- None of the given

دنیا کی سب سے بڑی فتح نفس پر قابو رکھنا ہے

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Question No: 24 (Marks: 1) - Please choose one

_____ is a combination of arithmetic, logic and shifter unit along with some multiplexers and control unit.

- Barrel Rotator
- Control Unit
- Flip Flop
- **ALU (Page 347)**

Question No: 25 (Marks: 1) - Please choose one

In Multiple Interrupt Line, a number of interrupt lines are provided between the _____ modules.

- **CPU and the I/O (Page 283)**
- CPU and Memory
- Memory and I/O
- None of the given

Question No: 26 (Marks: 1) - Please choose one

The data movement instructions _____ data within the machine and to or from input/output devices.

- Store
- Load
- Move
- **None of given (Page 141)**

Question No: 27 (Marks: 1) - Please choose one

CRC has ----- overhead as compared to Hamming code.

- Equal
- Greater
- **Lesser (Page 329)**
- None of the given

Question No: 28 (Marks: 1) - Please choose one

The _____ is w-bit wide and contains a data word, directly connected to the data bus which is b-bit wide memory address register (MAR) .

- Instruction Register(IR)
- memory address register (MAR)
- **memory Buffer Register(MBR) (Page 350)**
- Program counter (PC)

ایماندار کو غصہ دیر سے آتا ہے اور جلدی دور ہو جاتا ہے

Question No: 29 (Marks: 1) - Please choose one

In _____ technique, a particular block of data from main memory can be placed in only one location into the cache memory .

- Set Associative Mapping
- **Direct Mapping (Page 360)**
- Associative Mapping
- Block Placement

Question No: 30 (Marks: 1) - Please choose one

_____ indicate the availability of page in main memory.

- Access Control Bits
- Used Bits
- **Presence Bits** [click here for detail](#)
- None of the given

**FINAL TERM EXAMINATION
FALL 2006
CS501 - ADVANCE COMPUTER ARCHITECTURE**

Question No: 1 (Marks: 1) - Please choose one

The _____ RTN describes the overall effect of instructions on the programmer visible registers.

- ▶ **Abstract** [click here for detail](#)
- ▶ Concrete
- ▶ Absolute
- ▶ Basic

Question No: 2 (Marks: 1) - Please choose one

The instruction set is of _____ importance in governing the structure and function of the pipeline.

- ▶ Least
- ▶ **Primary** [click here for detail](#)
- ▶ Secondary
- ▶ No

زندگی میں کامیابی کا پہلا راز ہے کہ پریشانیوں سے پریشان مت بنو

Question No: 3 (Marks: 1) - Please choose one

_____ is the most general and least useful performance metrics for RISC machines.

- ▶ **MIPS** [click here for detail](#)
- ▶ Instruction Count
- ▶ Number of registers
- ▶ Clock Speed

Question No: 4 (Marks: 1) - Please choose one

A _____ provides four functions: Select, DataIn, DataOut and Read/Write.

- ▶ ALU
- ▶ Bus
- ▶ Register
- ▶ **Memory Cell (Page 351)**

Question No: 5 (Marks: 1) - Please choose one

We can classify or partition the SRC instructions by their overall _____ behavior.

- ▶ **Register transfer** [click here for detail](#)
- ▶ Memory transfer
- ▶ Execution
- ▶ Logical

Question No: 6 (Marks: 1) - Please choose one

The _____ RTN describes detailed register transfer steps in the data path that produce the overall effect.

- ▶ Abstract
- ▶ **Concrete** [click here for detail](#)
- ▶ Absolute
- ▶ Basic

Question No: 7 (Marks: 1) - Please choose one

All members of the MC68000 family are _____ processors.

- ▶ **32-bit** [click here for detail](#)
- ▶ 16-bit
- ▶ 64-bit
- ▶ 8-bit

جھوٹ انسان اور ایمان دونوں کا دشمن ہے

Question No: 8 (Marks: 1) - Please choose one

_____ Operations refers to a processor that can issue more than one instruction simultaneously.

- ▶ Macro
- ▶ Micro
- ▶ Scalar
- ▶ **Superscalar** [click here for detail](#)

Question No: 9 (Marks: 1) - Please choose one

Exceptions which are _____ occur in response to events that are paced by the internal processor clock.

- ▶ Asynchronous
- ▶ **Synchronous** [click here for detail](#)
- ▶ Internal
- ▶ External

Question No: 10 (Marks: 1) - Please choose one

In the hazard detection by hardware, resolved by pipeline stalls, if the instructions are in the adjoining stages, then the hazard must be detected in stage _____.

- ▶ 4
- ▶ 2
- ▶ **3** [click here for detail](#)
- ▶ 1

**FINAL TERM EXAMINATION
SPRING 2006
CS501 - ADVANCE COMPUTER ARCHITECTURE**

Question No: 1 (Marks: 3) - Please choose one

16k x4 static RAM Chip is arranged in the form of four _____ cells.

- ▶ 16x512
- ▶ 32x512
- ▶ 256x512
- ▶ **64x256** (Page 352)

عقل مند کہتا ہے میں کچھ نہیں جانتا جبکہ بے وقوف کہتا ہے کہ میں سب کچھ جانتا ہوں

Question No: 2 (Marks: 3) - Please choose one

In a DRAM cell, the storage capacitor will discharge in around _____

▶ **4 -15 ms** (Page 354)

▶ 2 - 10 ms

▶ 5-20 ms

▶ 10-25 ms

Question No: 3 (Marks: 3) - Please choose one

1-bit sign, 8-bit exponent, 23-bit fraction and a bias of 127 is used for _____ Binary Floating Point Representation

▶ Double precision

▶ **Single Precision** (Page 348)

▶ All of above

▶ Half Precision

Question No: 4 (Marks: 3) - Please choose one

The average rotational latency if the disk rotated at 20,000rpm is _____

▶ 0.5 ms

▶ 3.5 ms

▶ 2.5 ms

▶ **1.5 ms** (Page 324)

Question No: 5 (Marks: 3) - Please choose one

A hard disk with 5 platters has 1024 tracks per platter, 512 sectors per track and 512 bytes/sector. What is the total capacity of the disk?

▶ 1.5 GB

▶ **1 GB** (Page 324)

▶ 2 GB

▶ 3 GB

خود کو تمہیں سے بڑھ کر کوئی اچھا مشورہ نہیں دے سکتا

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CS501 – Final Term Quizzes

Quiz No.3 (Fall 2012)

Question # 1 of 10 (Total Marks: 1) Select correct option:

Where does the processor store the address of the first instruction of the ISR?

Interrupt vector (Page 277)

- Interrupt request
- Interrupt handler
- All of the given options

Question # 2 of 10 (Total Marks: 1) Select correct option:

In _____, a separate address space of the CPU is reserved for I/O operations.

Isolated I/O (Page 236)

- Memory Mapped I/O
- All of above
- None of above

Question # 3 of 10 (Total Marks: 1) Select correct option:

----- is the time needed by the CPU to recognize (not service) an interrupt request.

Interrupt Latency (Page 279)

- Response Deadline
- Timer delay
- Throughput

Question # 4 of 10 (Total Marks: 1) Select correct option:

_____ is a technique in which some of the CPU's address lines forming an input to the address decoder are ignored.

- Microprogramming
- Instruction pre-fetching
- Pipelining

Partial decoding (Page 255)

جو شخص ناکامیوں سے ڈر کر بھاگتا ہے کامیابی اس سے ڈر کر بھاگتی ہے

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Question # 5 of 10 (Total Marks: 1) Select correct option:

How can you define an interrupt?

A process where an external device can speedup the working of the microprocessor

A process where memory can speed up programs execution speed

A process where an external device can get the attention of the microprocessor [click here for detail](#)

A process where input devices can takeover the working of the microprocessor

Question # 6 of 10 (Total Marks: 1) Select correct option:

An interface that can be used to connect the microcomputer bus to _____ is called an I/O Port.

Flip Flops

Memory

Peripheral devices (Page 234)

Multiplexers

Question # 7 of 10 (Total Marks: 1) Select correct option:

A software routine performed when an interrupt is received by the computer is called as -----

Interrupt

Interrupt handler [click here for detail](#)

Exception

Trap

Question # 8 of 10 (Total Marks: 1) Select correct option:

Which one of the following methods for resolving the priority makes use of individual bits of a priority encoder?

Daisy-Chaining Priority

Asynchronous Priority

Parallel Priority (Page 281)

Semi-synchronous Priority

جو لوگوں کے سامنے فخر کرتا ہے وہ لوگوں کی نظروں سے گر جاتا ہے

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Quiz No.3 (Fall 2012)

Question # 1 of 10 (Total Marks: 1) Select correct option:

In which one of the following methods for resolving the priority, the device with the highest priority is placed in the first position, followed by lower-priority devices up to the device with the lowest priority, which is placed last in the series?

Asynchronous

Daisy-Chaining Priority [Click here for detail](#)

Parallel

Semi-synchronous

Question # 2 of 10 (Total Marks: 1) Select correct option:

Identify the type of serial communication error condition in which A 0 is received instead of a stop bit (which is always a 1)?

Framing error (Page 240)

Parity error

Overrun error

Under-run error

Question # 3 of 10 (Total Marks: 1) Select correct option:

Identify the following type of serial communication error condition in which no character is available at the beginning of an interval.

Framing error

Parity error

Overrun error

Under-run error (Page 240)

Question # 4 of 10 (Total Marks: 1) Select correct option:

_____ is an electrical pathway through which the processor communicates with the internal and external devices attached to the computer.

Computer Bus [click here for detail](#)

Hazard

Memory

Disk

عقل مند اپنے عیب خود دیکھتا ہے اور بھو تو فوں کے عیب دنیا دیکھتی ہے

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Question # 5 of 10 (Total Marks: 1) Select correct option:

Connection to a CPU that provides a data path between the CPU and external devices, such as a keyboard, display, or reader is called-----

processor

program

Buses [click here for detail](#)

memory address

Question # 6 of 10 (Total Marks: 1) Select correct option:

VLIW stands for -----

Very Lengthy Interaction Word

Very Length Instruction Width

Very Long Instruction Word (Page 219)

none of given options

Question # 7 of 10 (Total Marks: 1) Select correct option:

A -----is a wiring scheme in which, for example, device A is wired to device B, device B is wired to device C, device C is wired to device D etc.

Daisy chain [click here for detail](#)

DMA

Interrupt driven I/O

Polling

Question # 8 of 10 (Total Marks: 1) Select correct option:

An ----- is the memory address of an interrupt handler.

Interrupt vector [click here for detail](#)

Interrupt service routine

Exception

Mask

Question # 9 of 10 (Total Marks: 1) Select correct option:

The conversion of numbers from a representation in one base to another is known as _____

Radix Conversion (Page 333)

Number Representation

Decimal representation

Hexadecimal Representation

بد صورت چہرہ بد صورت دماغ سے بہتر ہے

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Question # 10 of 10 (Total Marks: 1) Select correct option:

If an interrupt is set by the timer component or by the peripheral device then how would you categorize it?

Hardware [click here for detail](#)

Software

Exception

All of the given options

Quiz No.3 (Fall 2012)

Question # 1 of 10 (Total Marks: 1) Select correct option: 1

In which one of the following interrupts the device have to supply the address of the subroutine to the

Microprocessor

Maskable

Non-maskable [click here for detail](#)

Non-vectorod

Vectored

Question # 2 of 10 (Total Marks: 1) Select correct option:

----- interrupts are usually associated with the software

hardware

software [click here for detail](#)

machine

internal

Question # 3 of 10 (Total Marks: 1) Select correct option:

When the address of the subroutine is already known to the Microprocessor then it is called as ----- interrupt.

Maskable

Non-maskable

Non-vectorod

Vectored [click here for detail](#)

Question # 4 of 10 (Total Marks: 1) Select correct option:

How Interrupt driven I/O is better than polling because?

Interrupt driver I/O is easy to design

Interrupt driver I/O is enhanced version of polling.

Interrupt driver I/O does not waste time on checking which device is available. (Page 274)

Interrupt driven I/O is easy to program.

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CS501 – Quiz No.4 (13 Feb 2013)

Question # 1 of 10 (Total Marks: 1) Select correct option:

In Single-Precision Binary Floating Point Representation the exponent is _____

- 8 bits (Page 348)
- 11 bits
- 1 bit
- 23 bits

Question # 2 of 10 (Total Marks: 1) Select correct option:

The _____ is m-bits wide and contains memory address generated by the CPU directly connected to the m-bit wide address bus Booth Recording

- memory address register (MAR) (Page 350)
- memory Buffer Register(MBR)
- Program counter (PC)
- Instruction Register(IR)

Question # 3 of 10 (Total Marks: 1) Select correct option:

A combination of parallel and sequential hardware used to build a multiplier is known as _____

- Parallel Array Multiplier
- Booth Recording
- Series Parallel Multiplier (Page 342)
- None of the given

Question # 4 of 10 (Total Marks: 1) Select correct option:

The register file is a collection of _____ bit wide registers used for data transfer between memory and the CPU .

- 8
- 16
- 32 (Page 350)
- 64

Question # 5 of 10 (Total Marks: 1) Select correct option:

The _____ of an m digit number x is xc^{m-1}

- Radix Compliment
- Diminished Radix Compliment (Page 337)
- Signed Magnitude Form
- Biased Representation

Question # 6 of 10 (Total Marks: 1) Select correct option:

Shifting of the radix point towards left or right is called _____

Shifting

Logical Shift

Right Shift

Scaling (Page 335)

Question # 7 of 10 (Total Marks: 1) Select correct option:

In _____ adder circuit we feed carry out from the previous stage to the next stage and so on.

Ripple Carry Adder (Page 341)

Carry Look Ahead Adder

Complement Adder

2's Complement Adder

Question # 8 of 10 (Total Marks: 1) Select correct option:

_____ are computed by the ALU and stored in processor status register.

Condition codes (Page 334)

Conditional Branches

Fraction Division

None of the given

Question # 9 of 10 (Total Marks: 1) Select correct option:

A _____ signal decides whether the input word should be shifted or bypassed.

Control Read

Shift/bypass (Page 346)

Control Write

None of the given

Question # 10 of 10 (Total Marks: 1) Select correct option:

Along with information bits we add up another bit which is called the _____ bit.

CRC

Hamming

Error Detection

Parity (Page 328)

بہترین تجربہ وہ ہے جس سے نصیحت حاصل ہو

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Question # 1 of 10 (Total Marks: 1) Select correct option:

In ____ recording ,bits are encoded in pairs so there are only ' n/2' additions instead of 'n'.

Booth Recording

Bit Pair Recording (Page 343)

Integer division

None of the given

Question # 2 of 10 (Total Marks: 1) Select correct option:

When ____ signal is high, this would correspond to a read operation equivalent to having an input data to the CPU and output from the memory REQUEST .

R/W (Page 350)

COMPLETE

REQUEST

None of the given

Question # 3 of 10 (Total Marks: 1) Select correct option:

Given an m-digit base b number x, the _____ of x is $xc = (bm - x) \bmod bm$

Radix Compliment (Page 337)

Diminished Radix Compliment

Signed Magnitude Form

Biased Representation

Question # 4 of 10 (Total Marks: 1) Select correct option:

For _____ of an error we just need to know that there exists an error.

Detection (Page 328)

Correction

Both Correction and Detection

None of the given

Question # 5 of 10 (Total Marks: 1) Select correct option:

In Double-Precision Binary Floating Representation the function is ____

23 bits

52 bits (Page 348)

1 bits

1 bit

تم اچھا کرو زمانہ تم کو برا سمجھے یہ اس سے بہتر ہے کہ تم برا کرو اور زمانہ تم کو اچھا سمجھے

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Question # 6 of 10 (Total Marks: 1) Select correct option:

_____ is the simplest form for representing a signed number

Based representation

Diminished Redex Complement Form

Sign Magnitude Form (Page 336)

None of the given

Question # 7 of 10 (Total Marks: 1) Select correct option:

In computers, floating-point representation uses _____ to encode significand, exponent and their sign in a single word

Decimal Numbers

Binary Numbers (Page 347)

Octal Numbers

Hexa decimal Numbers

افضل انسان وہ ہے جو اپنی اصلاح کی کوشش کرتا ہے

اپنی مرضی اور اللہ کی مرضی میں فرق کا نام غم ہے

اطمینان قلب چاہتے ہو تو حسد سے دور رہو

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Some More New MCQ'S (Updated)

Question # 1 of 10 (Total Marks: 1) Select correct option:

Which one of the following registers store a previously calculated value or a value loaded from the main memory?

- ▶ **Accumulator** [click here for detail](#)
- ▶ Address Mask
- ▶ Instruction Register
- ▶ Program Counter

Question # 2 of 10 (Total Marks: 1) Select correct option:

Which one of the following portions of an instruction represents the operation to be performed?

- ▶ Address
- ▶ Instruction code
- ▶ **Opcode** (Page 33)
- ▶ Operand

Question # 3 of 10 (Total Marks: 1) Select correct option:

_____ control signal enable the input to the PC for receiving a value that is currently on the internal processor bus.

- ▶ **LPC** (Page 172)
- ▶ INC4
- ▶ LC
- ▶ Cout

Question # 4 of 10 (Total Marks: 1) Select correct option:

What is the instruction length of the FALCON-E processor?

- ▶ 8 bits
- ▶ 16 bits
- ▶ **32 bits** (Page 134)
- ▶ 64 bits

وہ لوگ مبارک ہیں جو الفاظ سے نصیحت نہیں کرتے بلکہ عمل سے کرتے ہیں

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Question # 5 of 10 (Total Marks: 1) Select correct option:

Which type of instructions enables mathematical computations?

▶ **Arithmetic (Page 92)**

- ▶ Control
- ▶ Data transfer
- ▶ None of the given

Question # 6 of 10 (Total Marks: 1) Select correct option:

What is the instruction length of the SRC and Falcon E processor?

- ▶ 8 bits
- ▶ 16 bits
- ▶ **32 bits (Page 134)**
- ▶ 64 bits

Question # 7 of 10 (Total Marks: 1) Select correct option:

An instruction that specifies one operand in memory and one operand in a register would be known as a _____ address instruction.

- ▶ 2-1/2
- ▶ **1-1/2 (Page 37)**
- ▶ 0
- ▶ 2

Question # 8 of 10 (Total Marks: 1) Select correct option:

In floating point representations _____ is also called mantissa.

- ▶ Sign
- ▶ Base
- ▶ **Significant (Page 347)**
- ▶ Exponent

Question # 9 of 10 (Total Marks: 1) Select correct option:

What should be the behavior of interrupts during critical sections?

- ▶ **Must remain disable (Page 197)**
- ▶ Must remain Enable
- ▶ Can be either enable or disable
- ▶ only important interrupts be enable

خدا کے سوا کسی سے امید مت رکھو

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Question # 10 of 10 (Total Marks: 1) Select correct option:

Which one of the following is a binary cell capable of storing one bit of information?

- ▶ Decoder
- ▶ **Flip-flop (Page 76)**
- ▶ Multiplexer
- ▶ Diplexer

Question # 1 of 10 (Total Marks: 1) Select correct option:

Which type of instructions load data from memory into registers, or store data from registers into memory and transfer data between different kinds of special-purpose registers?

- ▶ Arithmetic
- ▶ Control
- ▶ **Data transfer (Page 88)**
- ▶ Floating point

Question # 2 of 10 (Total Marks: 1) Select correct option:

What does the RTL expression [M(1234)] mean?

- ▶ **The contents of memory whose address is 1234.** [Click here for detail](#)
- ▶ The contents of data register 1234
- ▶ The effective address of register 1234
- ▶ The address of memory whose address is 1234.

Question # 3 of 10 (Total Marks: 1) Select correct option:

Which one of the following languages presents a simple, human-oriented language to specify the operations, register communication and timing of the steps that take place within a CPU to carry out higher level (user programmable) instructions?

- ▶ Assembly Language
- ▶ OOP(Object Oriented Language)
- ▶ **RTL (Register Transfer Language)** [Click here for detail](#)
- ▶ UML(Unified Modeling language)

Question # 4 of 10 (Total Marks: 1) Select correct option:

Which one of the following instructions is used to load register from memory using a relative address?

- ▶ la
- ▶ lar
- ▶ **ldr (Page 145)**
- ▶ str

Question # 5 of 10 (Total Marks: 1) Select correct option:

Taking control of the system bus for a few bus cycles is known as _____.

- ▶ Bus Stealing
- ▶ **Cycle Stealing** (Page 317)
- ▶ Cycle Transferring
- ▶ None of given

Question # 6 of 10 (Total Marks: 1) Select correct option:

In-----address mode, the actual data is stored in the instruction.

- ▶ Direct
- ▶ Indirect
- ▶ **Immediate** [click here for detail](#)
- ▶ Relative

Question # 7 of 10 (Total Marks: 1) Select correct option:

Keyboard Interrupt (INT 9) is an example of ----- interrupt.

- ▶ **Hardware** [click here for detail](#)
- ▶ Software

Question # 8 of 10 (Total Marks: 1) Select correct option:

A user program has to delete a file. The user program will be executing in the user mode. When it makes the specific system call to delete the file, an interrupt will be generated, this will cause the processor to halt its current activity and switch to supervisor mode. Once in supervisor mode, the operating system will delete the file and then control will return to the user program. This is an example of

- ▶ Hardware interrupt
- ▶ **Software interrupt** (Page 275)
- ▶ Exception
- ▶ All of the given

Question # 9 of 10 (Total Marks: 1) Select correct option:

By which file extension does the FALCON-A Assembler loads a FALCON-A assembly file?

- ▶ **.asmfa** (Page 8)
- ▶ .org
- ▶ .exe
- ▶ .src

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Question # 10 of 10 (Total Marks: 1) Select correct option:

All -----interrupts have priority over all -----interrupts

▶ **internal, external** (Page 279)

▶ external, internal

Question # 1 of 10 (Total Marks: 1) Select correct option:

The ----- can also be used anywhere in the source file to force code at a particular address in the memory.

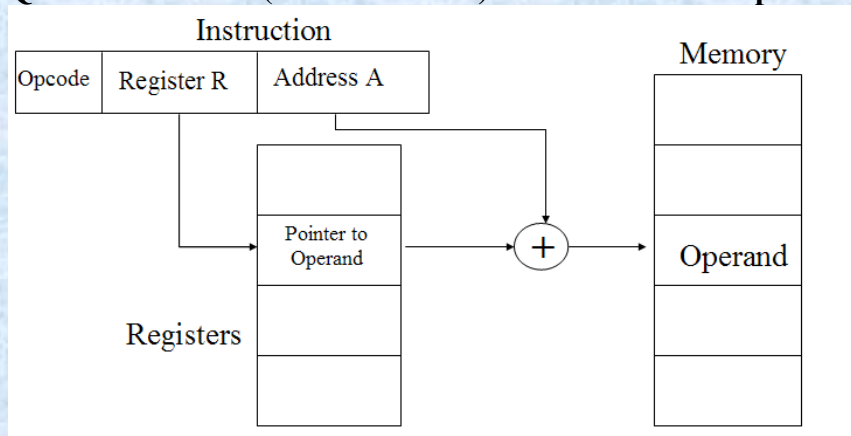
▶ .end directive

▶ .start directive

▶ .label directive

▶ **.org directive** (Page 298)

Question # 2 of 10 (Total Marks: 1) Select correct option:



In this figure, the constant value specified by the immediate field is added to the register value, and the resultant is the index of memory location that is referred i.e. Effective Address = $A + (\text{content of } R)$.

Identify the addressing mode.

▶ **Displacement** (Page 139)

▶ Immediate

▶ Indexed

▶ Relative

Question # 3 of 10 (Total Marks: 1) Select correct option:

In which one of the following addressing modes, the operand does not specify an address but it is the actual data to be used.

▶ Direct

▶ Indirect

▶ **Immediate** [click here for detail](#)

▶ Relative

Question # 4 of 10 (Total Marks: 1) Select correct option:

When is the “Divide error interrupt” generated?

- ▶ When an attempt is made to divide by decimal number
- ▶ When an attempt is made to multiply by zero
- ▶ **When an attempt is made to divide by zero (Page 197)**
- ▶ When negative number is stored in a register

Question # 5 of 10 (Total Marks: 1) Select correct option:

Which one of the following is a term used to describe a storage systems' resilience to disk failure through the use of multiple disks and by the use of data distribution and correction techniques?

- ▶ Interrupt handling
- ▶ Programmed I/O
- ▶ Polling
- ▶ **RAID** [click here for detail](#)

Question # 6 of 10 (Total Marks: 1) Select correct option:

_____ is the time for first bit of the message to arrive at the receiver including delays.

- ▶ Transmission Time
- ▶ Latency
- ▶ Transport Latency
- ▶ **Time of Flight (Page 388)**

Question # 7 of 10 (Total Marks: 1) Select correct option:

Falcon-A Simulator loads a FALCON-A binary file with a _____ extension and presents its contents into different areas of the simulator.

- ▶ .bin
- ▶ **.binfa (Page 5)**
- ▶ .fa
- ▶ None of the given

Question # 8 of 10 (Total Marks: 1) Select correct option:

In machines where instructions can be executed in parallel or out of order, two additional hazards can occur: WAW and -----

- ▶ None fo the given
- ▶ **WAR**
- ▶ RAW
- ▶ RAR

فرقہ بندی ہی ہماری قوم کا زوال کا باعث ہے

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Question # 9 of 10 (Total Marks: 1) Select correct option:

For _____ of an error we just need to know that there exists an error.

- ▶ None of the given
- ▶ Correction
- ▶ **Detection (Page 328)**
- ▶ Both Correction and Detection

Question # 10 of 10 (Total Marks: 1) Select correct option:

Identify the type of serial communication error condition in which 0 is received instead of a stop bit (which is always a 1)?

- ▶ **Framing error (Page 240)**
- ▶ Parity error
- ▶ Overrun error
- ▶ Under-run error

Question # 1 of 10 (Total Marks: 1) Select correct option:

_____ is/are defined as the number of instructions processed per second

- ▶ **Throughput (Page 203)**
- ▶ Latency Time to process 1 request.
- ▶ Throughput and Latency
- ▶ None of the given

Question # 2 of 10 (Total Marks: 1) Select correct option:

Raid Level ____ is not a true member of the RAID family.

- ▶ **0 (Page 330)**
- ▶ 2
- ▶ 3
- ▶ 4

Question # 3 of 10 (Total Marks: 1) Select correct option:

Which one of the following is an address (binary bit pattern) issued by CPU?

- ▶ Memory
- ▶ **Effective (Page 39)**
- ▶ Base
- ▶ Next instruction

بے بسی نصف موت ہے

Question # 4 of 10 (Total Marks: 1) Select correct option:

Which one the following interrupts is initiated with an INT instruction?

- ▶ Hardware
- ▶ **Software** [click here for detail](#)
- ▶ Both hardware and Software
- ▶ None of the given

Question # 5 of 10 (Total Marks: 1) Select correct option:

An -----is a program that takes basic computer instructions and converts them into a pattern of bits that the computer's processor can use to perform its basic operations.

- ▶ **Assembler** [click here for detail](#)
- ▶ Debugger
- ▶ Editor
- ▶ Console

پہلے وقتوں میں تعلیم کم تھی اور علم زیادہ، آج کل علم کم ہے اور تعلیم زیادہ

بہادر آدمی ایک مرتبہ جبکہ بزدل آدمی کئی مرتبہ مرتا ہے

اچھائی کرنے کے لئے ہمیشہ کسی بہانے کی تلاش میں رہو

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