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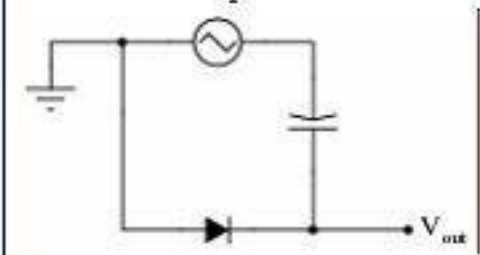
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Draw the output waveform shape for this circuit, assuming an ideal diode (no forward voltage drop and no reverse leakage):



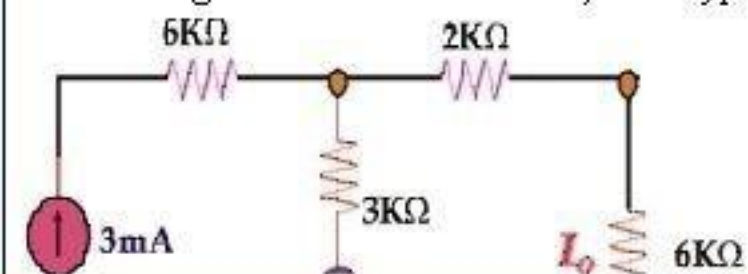
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Considering the **Norton's theorem**, what type of changes you will do in the circuit to find Norton's Resistance  $R_N$ . Draw the circuit only.

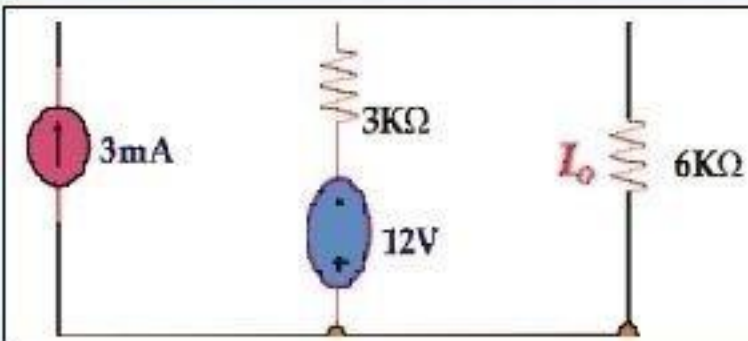


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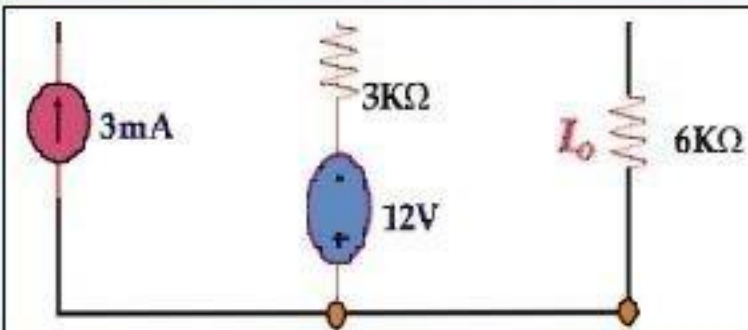


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State Kirchoff's voltage law (KVL).

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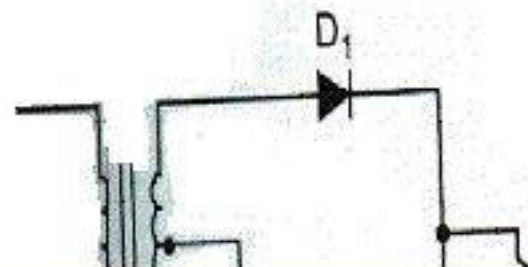
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What type of changes you will do in the circuit to make it Negative Full wave rectifier?

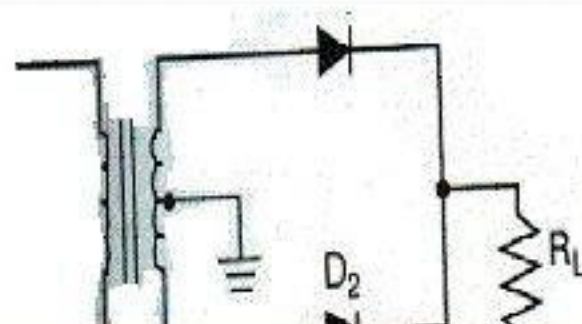


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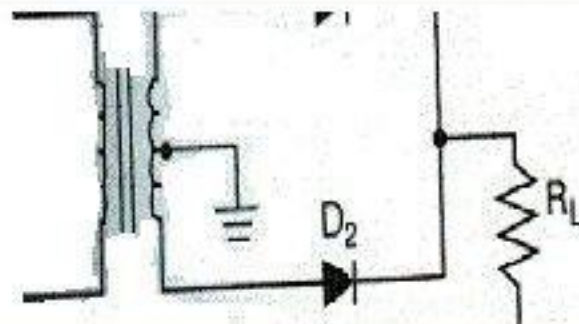
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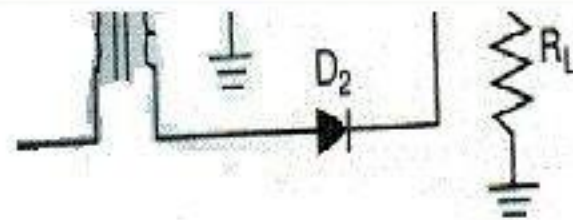


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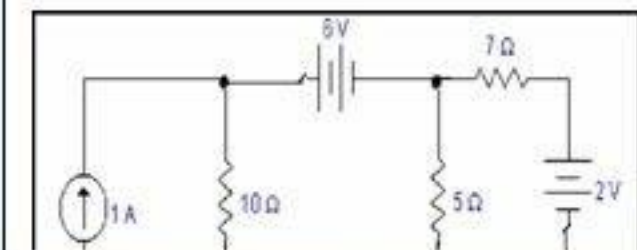
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Label the given circuit for node, reference node and super node.

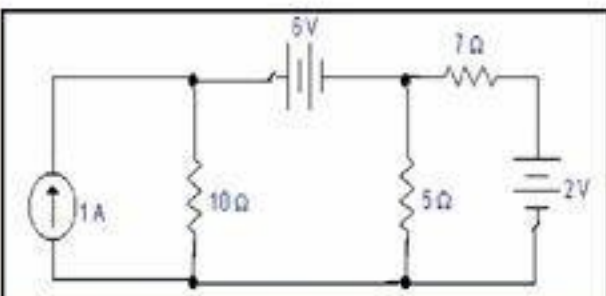


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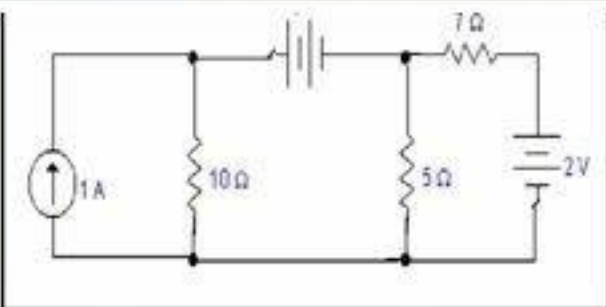


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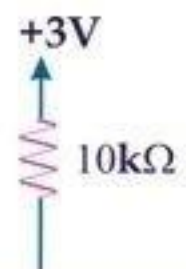
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For the circuit shown in the figure below using ideal diode, find the value of the indicated voltage and current.



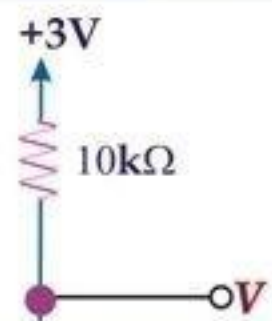
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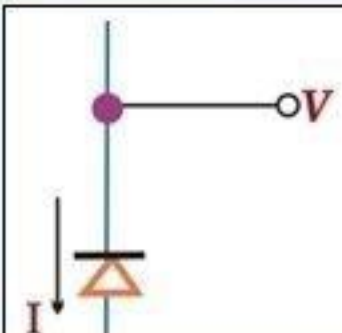


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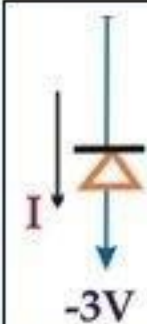


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Consider a diode with  $n = 2$  biased at  $1\text{mA}$ . Find the change in current as a result of changing the voltage by  $-20\text{mV}$  using Small Signal Model. Where diode small signal conductance  $V_T$  is  $25\text{mho}$

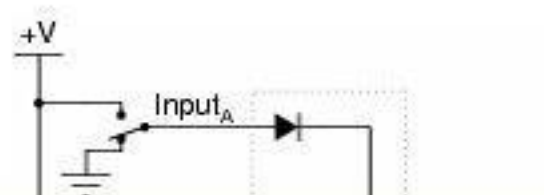
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Crude logic gates circuits may be constructed out of nothing but diodes and resistors. Take for example this logic gate circuit:

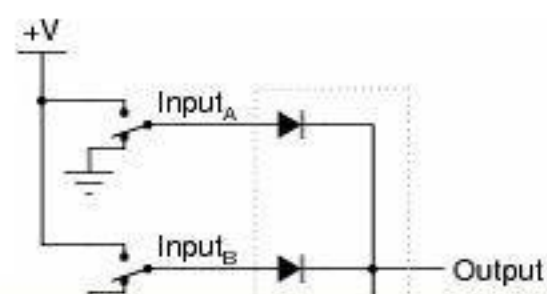


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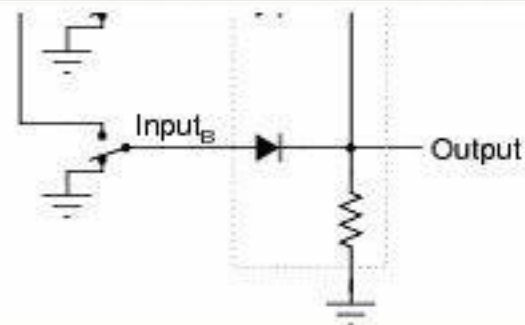
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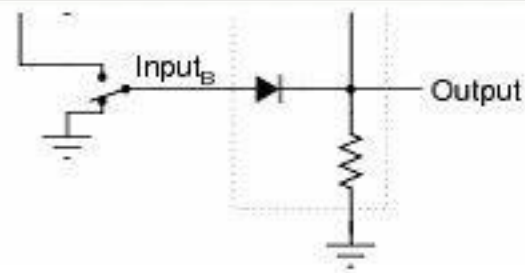


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Identify what type of logic function is represented by this gate circuit

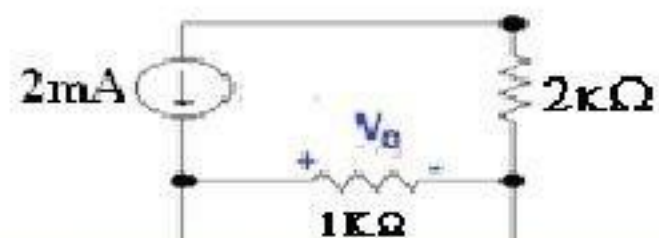
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Label the circuit properly. Use any technique to find out the  $V_o$  for the given circuit.

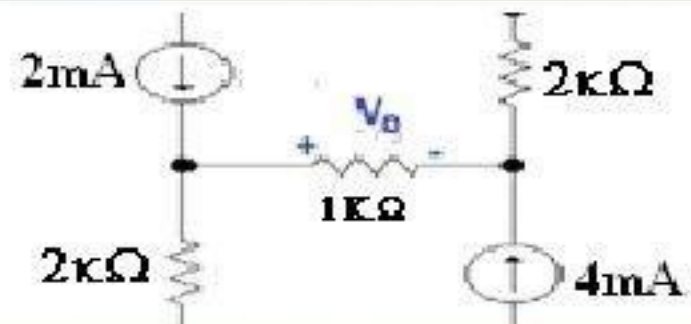


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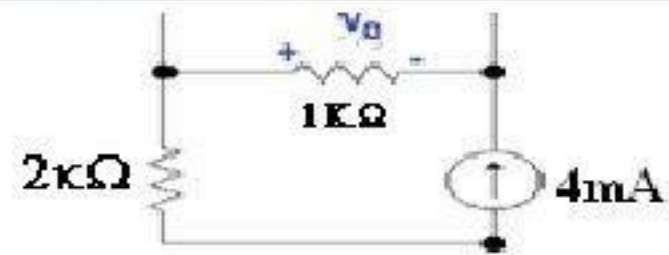


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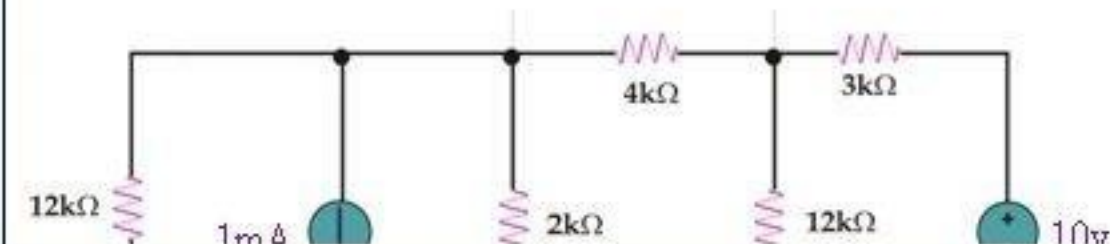
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Using the **Source transformation method**, how will you convert **10v** voltage source into current source and **1mA** into voltage source in the following circuit? Draw diagrams of converted circuit.



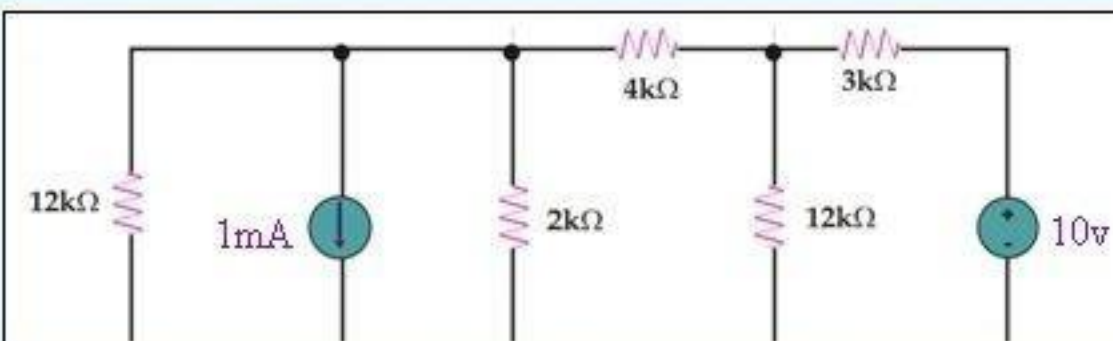
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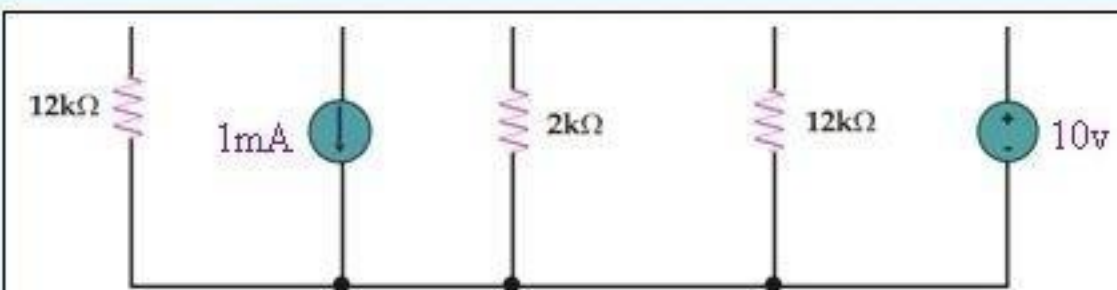


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Given below are two figures (a) and (b) having Diode, which diode is forward biased or reversed biased? tell reason.

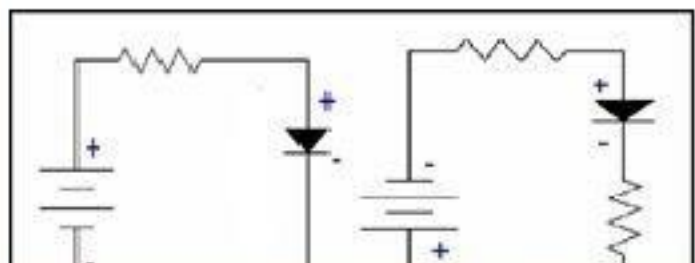


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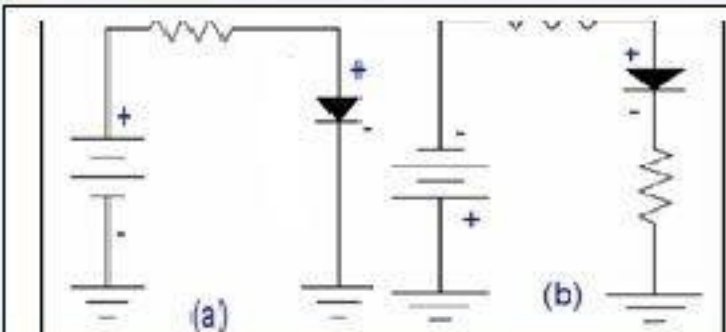


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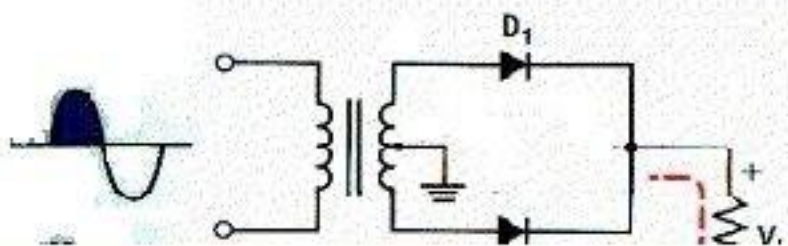
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Describe the basic circuit operation of Full wave rectifier for both input signals, shown at left side of given fig.



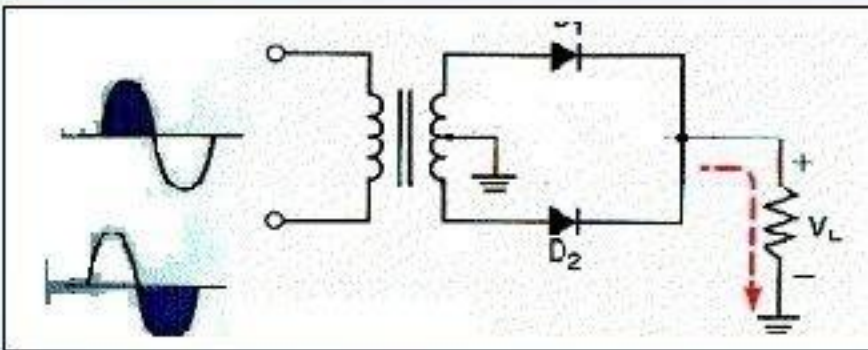
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Write a brief description about LEDs.

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What will happen when  
(I) two like charges come close  
(II) two opposite charges come close

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Mention the P- type and n -type for Diode symbol given below.



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Write the formula to calculate PIV for half wave rectifier.

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What is the relationship between the polarity of the voltage applied to the PNP transistor and that applied to the NPN transistor?

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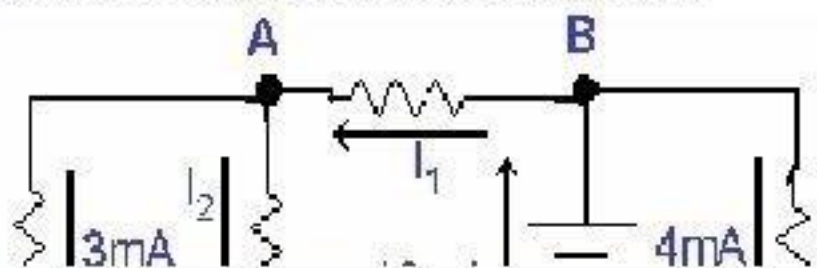
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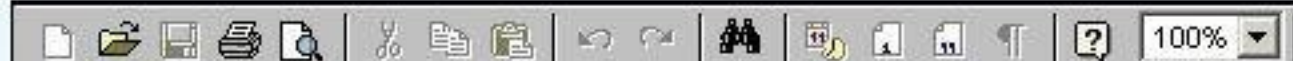


Write KCL equation for node A and node B.



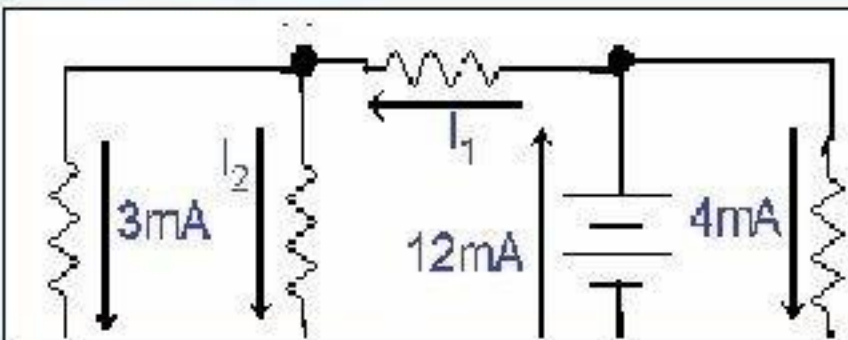
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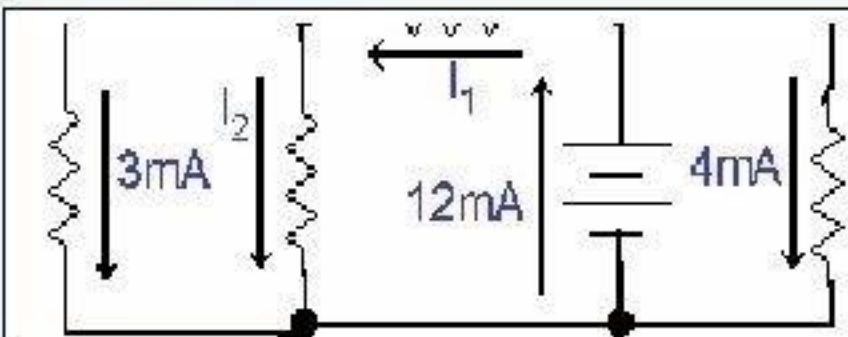
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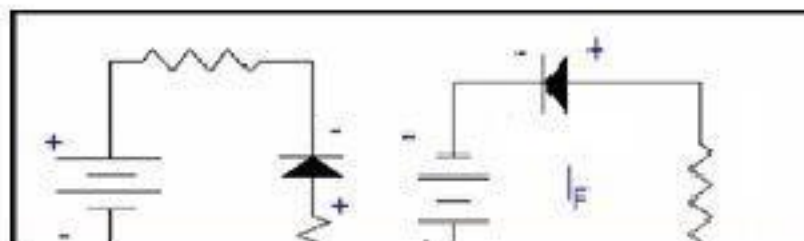
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Given below are two figures (a) and (b) having Diode, In which diode current will flow and will not flow? Give its reason.

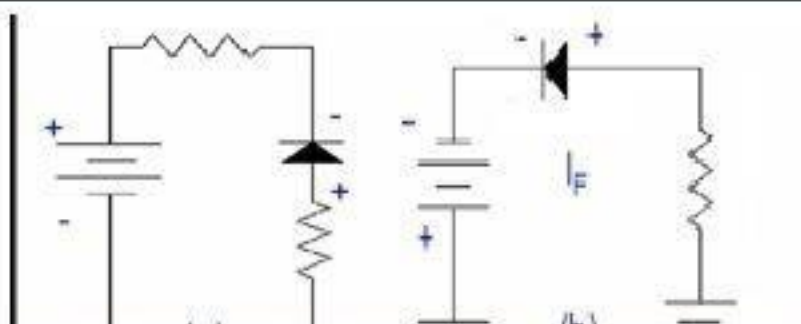


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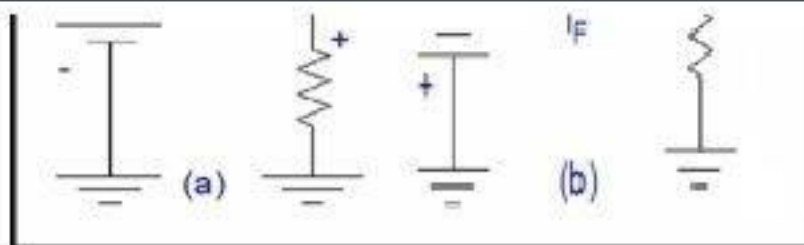


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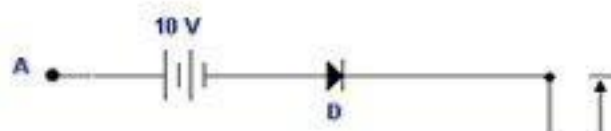
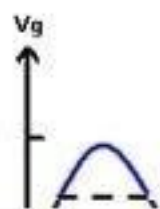
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Tell the name of circuit in fig (b) and how it response for input signal of fig. (a)?



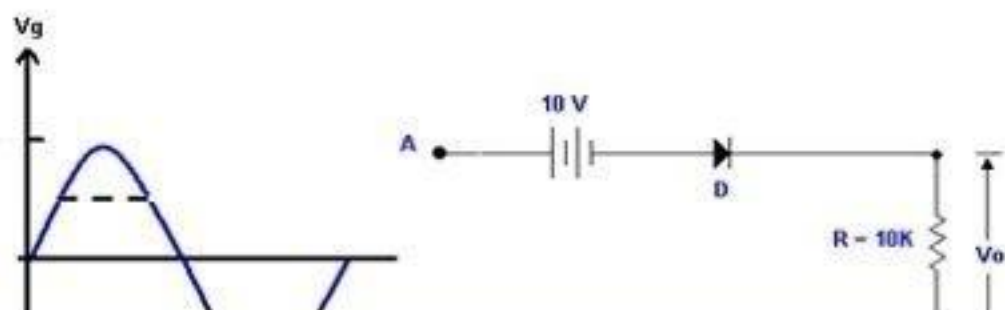
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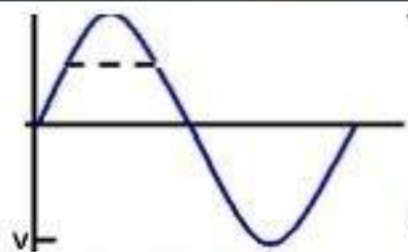


Fig (a)

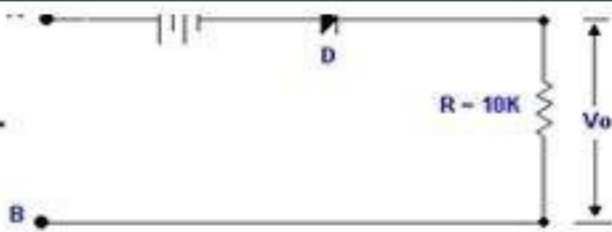


Fig (b)

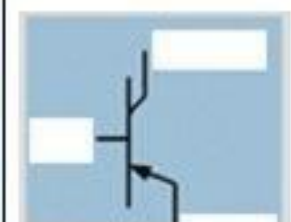
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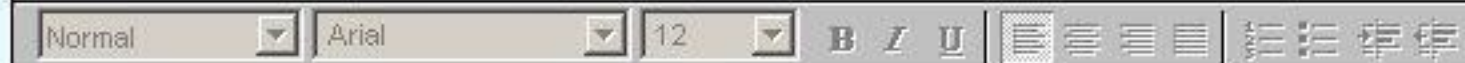
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Label the given diagram for transistor symbol and write its type as well.



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For the circuit shown, find  $I_E$  &  $R_C$



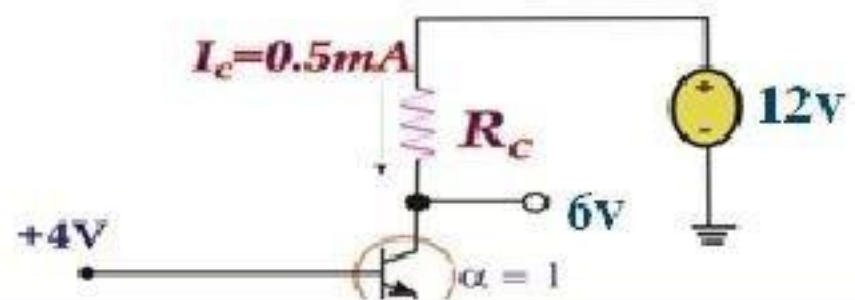
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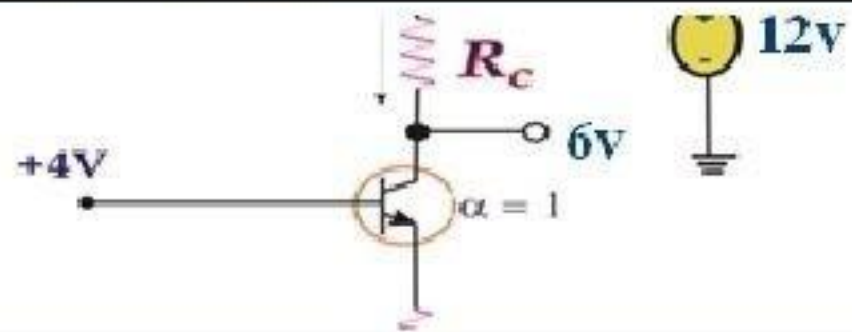
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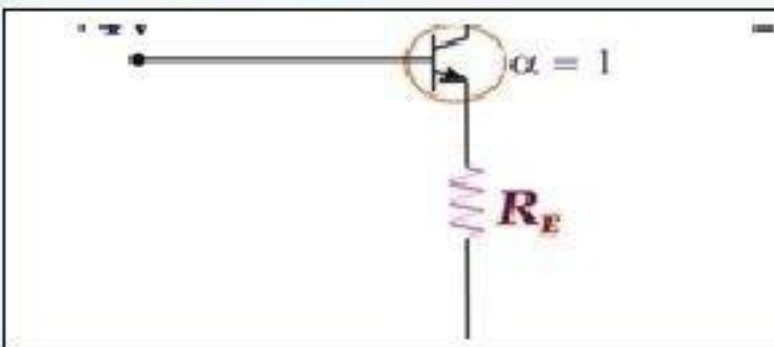
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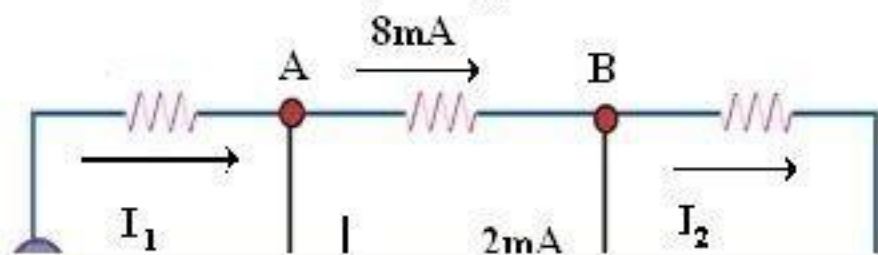
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Calculate the values of  $I_1$  and  $I_2$ .



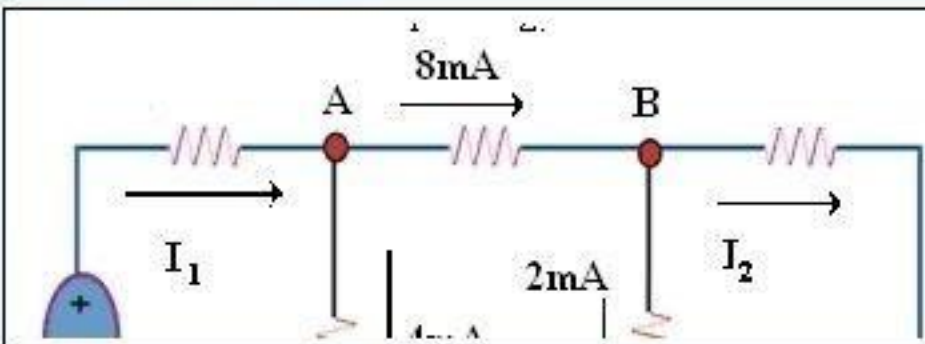
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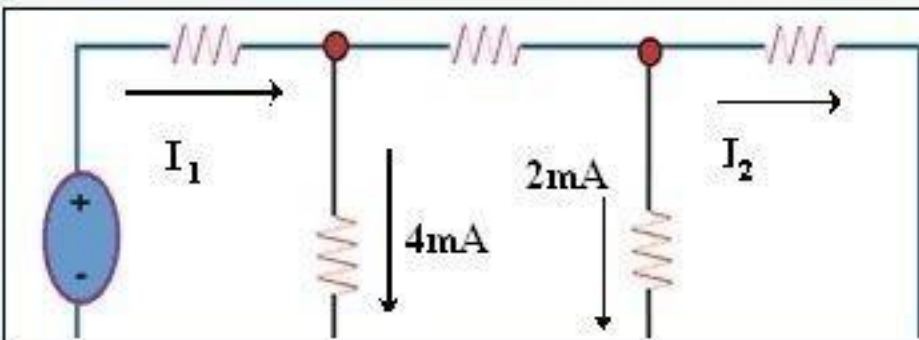
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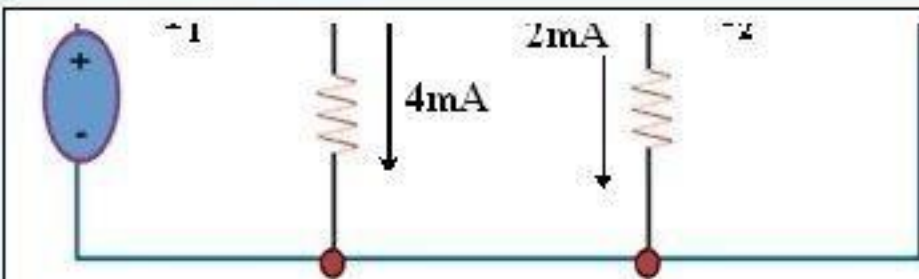
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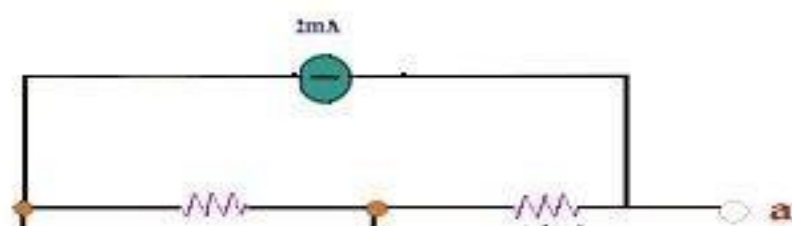
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Using the **Thevenin's Theorem**, find Thevenin's Resistance  $R_{th}$  to the left of a,b . Draw the circuit.



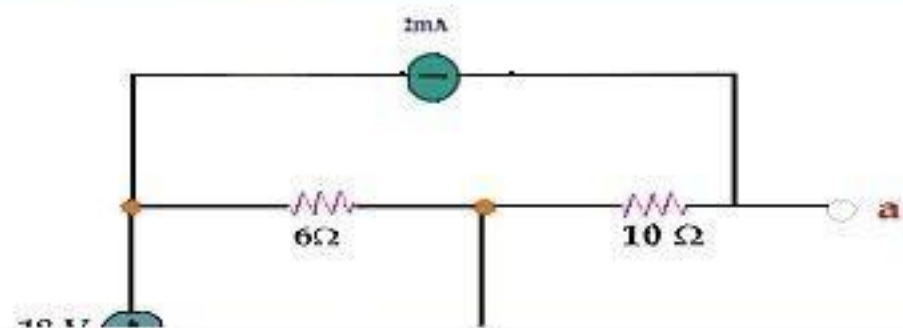
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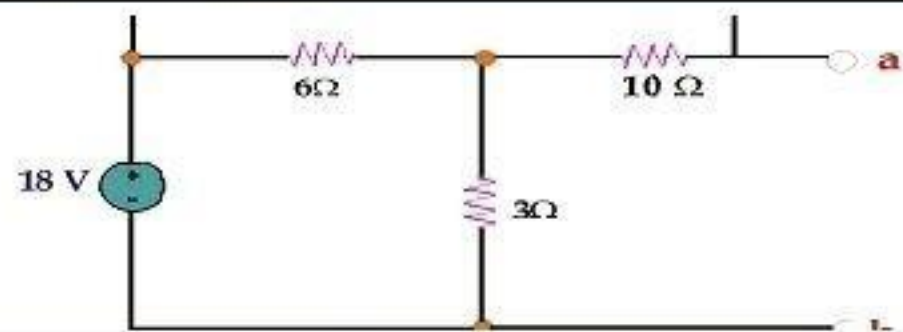
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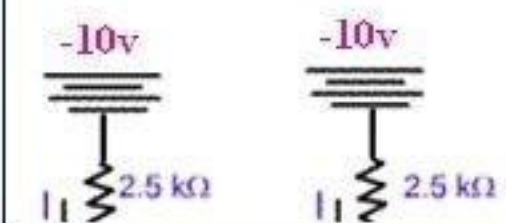
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Find the value of current  $I$  and voltage  $V$  in the circuits shown in figure (a) and (b). (Assuming the diodes to be ideal).

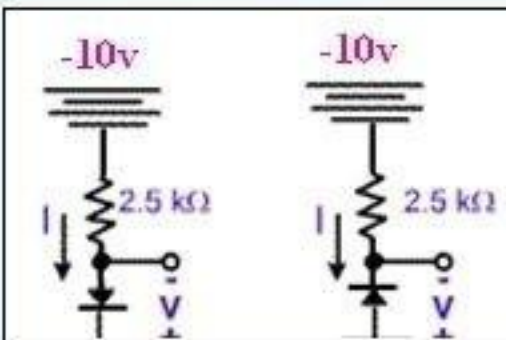


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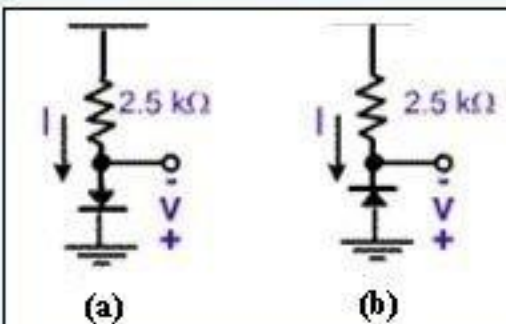
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Find the value of the diode small signal resistance  $r_d$  at bias current of 0.1mA and 10mA. Assume  $n=1$

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