Question No : 1 of 52   Marks: 1 (Budgeted Time 1 Min)

Which one of the following is the memory organization of **EAGLE processor?**

Answer (Please select your correct option)

VuAnswers.com

○ $2^8 * 8$ bits

○ $2^{16} * 8$ bits   *page 12o*

○ $2^{32} * 8$ bits

○ $2^{64} * 8$ bits

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Question No : 2 of 52   Marks: 1 (Budgeted Time 1 Min)

-------------is a collection of binary digits or bits that the computer reads and interprets.

Answer (Please select your correct option)

VuAnswers.com

○ assembly language

○ higher level language

○ English language

○ machine language   *page 12*

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Question No : 3 of 52   Marks: 1 (Budgeted Time 1 Min)

In which one of the following addressing modes, the value to be stored in memory is obtained by directly retrieving it from another memory location?

Answer (Please select your correct option)

VuAnswers.com

○ Direct Addressing Mode

○ Immediate addressing mode

○ Indirect Addressing Mode

○ Register (Direct) Addressing Mode

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Question No : 4 of 52 · Marks: 1 (Budgeted Time 1 Min)

The external interface of FALCON-A consists of a _____ address bus and a _____ data bus.

Answer (Please select your correct option)

- ○ 8-bit , 8-bit
- ○ 16-bit , 16-bit   **176**
- ○ 16-bit , 24-bit
- ○ 16-bit , 32-bit

Question No : 5 of 52 · Marks: 1 (Budgeted Time 1 Min)

Execution time of a program with respect to the processor is calculated as:

Answer (Please select your correct option)

- ○ Execution Time = IC x CPI x MIPS
- ○ Execution Time = IC x CPI x T   **ans**
- ○ Execution Time = CPI x T x MFLOPS
- ○ Execution Time = IC x T

Question No : 6 of 52 · Marks: 1 (Budgeted Time 1 Min)

In which of the following instructions the data move between a register in the processor and a memory location (or another register) and are also called data movement?

Answer (Please select your correct option)

- ○ Arithmetic
- ○ Load/store   **ans**
- ○ Control
- ○ Logic

The _____ instruction is completed once memory access has been made and the memory location has been written to.

Answer (Please select your correct option)

VuAnswers.com

○ Store   p 208

○ Branch

○ Load

○ Control

Which type of instructions enables mathematical computations?

Answer (Please select your correct option)

VuAnswers.com

○ Arithmetic   page 92

○ Control

○ Data transfer

○ Miscellaneous

_____ control signal allows the bus to read from the selected register.

Answer (Please select your correct option)

VuAnswers.com

○ RBE

○ RCE

○ LCON

○ R2BUS   page 193

For More Visit **VU Answer**

For any of the instructions that are a part of the instruction set of the SRC, there are certain _____ required; which may be used to select the appropriate function for the ALU to b performed, to select the appropriate registers, or the appropriate memory location.

Answer (Please select your correct option)

VuAnswers.com

○ Registers

○ Control signals   p 171   ans

○ Memory

○ DMA controllers

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Every I/O port has a unique identifier associated with it, which is called its ------------------

Answer (Please select your correct option)

VuAnswers.com

○ Address   ans

○ Access point

○ Interval Identifier

○ Device driver

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_____ is a technique in which some of the CPU's address lines forming an input to the address decoder are ignored.

Answer (Please select your correct option)

VuAnswers.com

○ Partial decoding   ans

○ Full encoding

○ Partial multiplexing

○ Half encoding

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For More Visit **VU Answer**

Partial decoding is an attractive choice in _____.

Answer (Please select your correct option)

○ Small systems
         ans

○ Large systems

○ Medium systems

○ All of the above

Consider Falcon A, with 16 address lines, the total address space is _____ Kbytes.

Answer (Please select your correct option)

○ $2 \wedge 16$
         ans

○ $2 \wedge 10$

○ $2 \wedge 6$

○ $2 \wedge 8$

In which one of the following methods, does the CPU poll to identify the interrupting module and branch to an interrupt service routine on detecting an interrupt?

Answer (Please select your correct option)

○ Multiple interrupt lines

○ Software Poll
         ans

○ Daisy Chain

○ Parallel Priority

Maskable Interrupts are applied to the _____ pin of the processor.

Answer (Please select your correct option)

○ INTR

    ans

○ NMI

○ IRET

○ INT

Non-maskable Interrupts are detected using the _____ pin of the processor.

Answer (Please select your correct option)

○ INTR

○ NMI

    ans

○ IRET

○ INT

In Multiple Interrupt Lines approach, a number of interrupt lines are provided between the _____ modules.

Answer (Please select your correct option)

○ External and Internal

○ CPU and I/O

    ans

○ CPU and Memory

○ Memory and I/O

Question No : 19 of 52   Marks: 1 (Budgeted Time 1 Min)

In FALCON-A assembler and simulator (FALSIM), variables are defined by using the _____ directive.

Answer (Please select your correct option)

○ .bin

○ .equ

○ .iret

○ .end

Question No : 20 of 52   Marks: 1 (Budgeted Time 1 Min)

_____ allows a peripheral device to read from and/or write to memory without intervention by the CPU.

Answer (Please select your correct option)

○ Programmed I/O

○ Interrupt driven I/O

○ Direct memory access   ans

○ Polling

Question No : 21 of 52   Marks: 1 (Budgeted Time 1 Min)

Taking control of the system bus for a few bus cycles is known as _____.

Answer (Please select your correct option)

○ Bus Scheduling

○ Cycle Stealing   ans

○ Cycle Transferring

○ CPU Scheduling

Question No : 22 of 52 Marks: 1 (Budgeted Time 1 Min)

A component connected to the system bus and having control of it during a particular bus cycle is called _____.

Answer (Please select your correct option)

○ Address decoder

○ BIOS

○ Master component
  ans

○ Slave component

Question No : 23 of 52 Marks: 1 (Budgeted Time 1 Min)

When it is required to read data from a particular location of the disk, the head moves towards the selected track and this process is called_____.

Answer (Please select your correct option)

○ Fragmentation

○ Defragmentation

○ Seek
  ans

○ Encoding

Question No : 24 of 52 Marks: 1 (Budgeted Time 1 Min)

CRC has _____overhead as compared to Hamming code.

Answer (Please select your correct option)

○ Equal

○ Greater

○ Lesser
  ans

○ Absolutely no

The conversion of numbers from a representation in one base to another is known as_____

Answer (Please select your correct option)

○ Radix Conversion **ans**

○ Number Representation

○ Decimal Representation

○ Hexadecimal Representation

In _____, bits are encoded in pairs so there are only ' n/2' additions instead of 'n'.

Answer (Please select your correct option)

○ Booth Recoding

○ Bit-Pair Recoding **ans**

○ Fraction Division

○ Integer Division

_____ are computed by the ALU and stored in processor status register.

Answer (Please select your correct option)

○ Condition Codes **ans**

○ Control Signals

○ Flip Flops

○ Multiplexers

In computers, floating-point representation uses _____ to encode significand, exponent and their sign in a single word.

Answer (Please select your correct option)

○ Decimal Numbers

○ Binary Numbers   **ans**

○ Octal Numbers

○ Hexadecimal Numbers

In floating point representations _____ is also called mantissa.

Answer (Please select your correct option)

○ Sign

○ Base

○ Significand   **ans**   p 347

○ Exponent

In Single-Precision Binary Floating Point Representation the size of exponent is _____.

Answer (Please select your correct option)

○ 8-bits   **ans**

○ 11-bits

○ 1-bits

○ 23-bits

_____ is nonvolatile i.e. it retains the information in it when power is removed from it.

Answer (Please select your correct option)

○ RAM

○ DRAM

○ ROM **ans**

○ SRAM

Based on the statistical results, the cache block which has been least used in the recent past, is replaced with a new block. This technique is called _____.

Answer (Please select your correct option)

○ Always Replacement

○ Random Replacement

○ LFU (Least Frequently Used) **ans**

○ Write Allocate

_____ acts as a cache between main memory and secondary memory.

Answer (Please select your correct option)

○ Read Only Memory

○ Flash Memory

○ Virtual Memory **ans**

○ Magnetic Tape

Marks: 1 (Budgeted Time 1 Min)

Randomly replacing any older page to bring in the desired page is known as _____.

Answer (Please select your correct option)

○ Always Replacement

○ LFU (Least Frequently Used)

○ Random Replacement
ans

○ Fragmentation

Marks: 1 (Budgeted Time 1 Min)

_____ are formed by concatenating the page number with the word number.

Answer (Please select your correct option)

○ Memory chips

○ Protocols

○ Hazards

○ Virtual addresses
ans

Marks: 1 (Budgeted Time 1 Min)

In _____ technique memory is divided into segments of variable sizes depending upon the requirements.

Answer (Please select your correct option)

○ Multiplexing

○ Segmentation
ans

○ Hamming code

○ Partial decoding

Question No : 37 of 52　　　　　　　　　　　　　　　　　　　　Marks: **1** (Budgeted Time **1 Min**)

_____ depends upon the average number of calls and the service time taken by a particular server.

Answer (Please select your correct option)

○ Throughput　　　**ans**

○ Latency

○ Poisson Distribution

○ Response Time

Question No : 38 of 52　　　　　　　　　　　　　　　　　　　　Marks: **1** (Budgeted Time **1 Min**)

_____ is also called traffic intensity and its value must be between 0 and 1.

Answer (Please select your correct option)

○ Little's Law

○ Poisson Distribution

○ Server Utilization　　　**ans**

○ SPEC

The time for the message to pass through the network, except the time of flight is called _____.

Answer (Please select your correct option)

VuAnswers.com

○ Transmission Time   **ans**

○ Latency

○ Transport Latency

○ Bandwidth

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A set of rules followed by different components in a network is called _____.

Answer (Please select your correct option)

VuAnswers.com

○ Host

○ Connectivity

○ Resource Sharing

○ Protocol   **ans**

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