	pers By Waqar (File 1)
ion No : 1 of 52	Marks: 1 (Budgeted Time
t does the instruction "Idr R3, 58" of SRC do?	
er (Please select your correct option)	VuAnswers.com
it will load the register R3 with the contents of the memory location M [PC+58]	correct
It will load the register R3 with the relative address itself (PC+58).	<u>correct</u>
t will store the register R3 contents to the memory location M [PC+58]	
No operation	
	Made by: Waqar Side
ion No : 2 of 52	Marks: 1 (Budgeted Time
NET TO A TANK TO A TANK TO A TANK TO A	VuAnswers.com
t will load the register R3 with the contents of the memory location M [PC+36]	
er (Please select your correct option) it will load the register R3 with the contents of the memory location M [PC+36] It will load the register R3 with the relative address itself (PC+36). It will store the register R3 contents to the memory location M [PC+36]	VuAnswers.com
it will load the register R3 with the contents of the memory location M [PC+36] It will load the register R3 with the relative address itself (PC+36).	
it will load the register R3 with the contents of the memory location M [PC+36] It will load the register R3 with the relative address itself (PC+36).	correct
it will load the register R3 with the contents of the memory location M [PC+36] It will load the register R3 with the relative address itself (PC+36). It will store the register R3 contents to the memory location M [PC+36]	
it will load the register R3 with the contents of the memory location M [PC+36] It will load the register R3 with the relative address itself (PC+36). It will store the register R3 contents to the memory location M [PC+36] No operation	wned Made by: Wagar Sidd
t will load the register R3 with the contents of the memory location M [PC+36] t will load the register R3 with the relative address itself (PC+36). t will store the register R3 contents to the memory location M [PC+36] No operation	wned Made by: Wagar Sidd
it will load the register R3 with the contents of the memory location M [PC+36] It will load the register R3 with the relative address itself (PC+36). It will store the register R3 contents to the memory location M [PC+36] No operation ion No : 3 of 52 What is the instruction length of the SRC processor?	correct Marks: 1 (Budgeted Time
it will load the register R3 with the contents of the memory location M [PC+36] It will load the register R3 with the relative address itself (PC+36). It will store the register R3 contents to the memory location M [PC+36] No operation	wned Made by: Wagar Sidd
it will load the register R3 with the contents of the memory location M [PC+36] It will load the register R3 with the relative address itself (PC+36). It will store the register R3 contents to the memory location M [PC+36] No operation Ion No : 3 of 52 Vhat is the instruction length of the SRC processor? In Please select your correct option) 8 bits	correct Marks: 1 (Budgeted Time
it will load the register R3 with the contents of the memory location M [PC+36] It will load the register R3 with the relative address itself (PC+36). It will store the register R3 contents to the memory location M [PC+36] No operation It will store the instruction length of the SRC processor? It is the instruction length of the SRC processor? It is the instruction length of the SRC processor?	correct Marks: 1 (Budgeted Time
t will load the register R3 with the contents of the memory location M [PC+36] t will load the register R3 with the relative address itself (PC+36). t will store the register R3 contents to the memory location M [PC+36] No operation too No : 3 of 52 What is the instruction length of the SRC processor? ar (Please select your correct option) 8 bits	correct Marks: 1 (Budgeted Time

tion No : 4 of 52		Marks: 1 (Budgeted Time 1 Min)
at does the word 'D' in the 'D-flip-Flop' stands	s for?	manner (exagence i sino i min)
wer (Please select your correct option)		VuAnswers.com
Data	correct	
Digital	187 199 J	
Digital		
Dynamic		
Double		Made by: Waqar Siddhu
stion No : 5 of 52		Marks: 1 (Budgeted Time 1 Min)
nost every commercial computer has its own particular	language	
wer (Please select your correct option)		VuAnswers.com
assembly language	correct	
English language		
Higher level language		
201		
36L		Made by: Magar Siddhu
3GL stion No : 6 of 52		Made by: Wagar Siddhu Marks: 1 (Budgeted Time 1 Min)
stion No : 6 of 52	on is the " type of operation " that is to be perf	Marks: 1 (Budgeted Time 1 Min)
stion No : 6 of 52	on is the " type of operation " that is to be perf	Marks: 1 (Budgeted Time 1 Min)
stion No : 6 of 52	on is the " type of operation " that is to be perf	Marks: 1 (Budgeted Time 1 Min)
estion No : 6 of 52 Which field of the machine language instruction wer (Please select your correct option)	on is the " type of operation " that is to be perf	Marks: 1 (Budgeted Time 1 Min)
estion No : 6 of 52 Which field of the machine language instruction	on is the " type of operation " that is to be perfo	Marks: 1 (Budgeted Time 1 Min)
estion No : 6 of 52 Which field of the machine language instruction wer (Please select your correct option)		Marks: 1 (Budgeted Time 1 Min)
estion No : 6 of 52 Which field of the machine language instruction wer (Please select your correct option) Op-code (or the operation code)		Marks: 1 (Budgeted Time 1 Min)
estion No : 6 of 52 Which field of the machine language instruction wer (Please select your correct option) Op-code (or the operation code)		Marks: 1 (Budgeted Time 1 Min)
Instion No : 6 of 52 Which field of the machine language instruction Wer (Please select your correct option) Op-code (or the operation code) CPU registers Memory cells		Marks: 1 (Budgeted Time 1 Min)
estion No : 6 of 52 Which field of the machine language instruction wer (Please select your correct option) Op-code (or the operation code) CPU registers		Marks: 1 (Budgeted Time 1 Min) ormed? VuAnswers.com
stion No : 6 of 52 Which field of the machine language instruction ver (Please select your correct option) Op-code (or the operation code) CPU registers Memory cells		Marks: 1 (Budgeted Time 1 Min) ormed? VuAnswers.com
Instion No : 6 of 52 Which field of the machine language instruction Wer (Please select your correct option) Op-code (or the operation code) CPU registers Memory cells		Marks: 1 (Budgeted Time 1 Min)
stion No : 6 of 52 Which field of the machine language instruction wer (Please select your correct option) Op-code (or the operation code) CPU registers Memory cells		Marks: 1 (Budgeted Time 1 Min) ormed? VuAnswers.com
Instion No : 6 of 52 Which field of the machine language instruction Wer (Please select your correct option) Op-code (or the operation code) CPU registers Memory cells		Marks: 1 (Budgeted Time 1 Min) ormed? VuAnswers.com
Instion No : 6 of 52 Which field of the machine language instruction Wer (Please select your correct option) Op-code (or the operation code) CPU registers Memory cells		Marks: 1 (Budgeted Time 1 Min) ormed? VuAnswers.com
estion No : 6 of 52 Which field of the machine language instruction wer (Please select your correct option) Op-code (or the operation code) CPU registers Memory cells VO locations		Marks: 1 (Budgeted Time 1 Min) ormed? VUAnswers.com

	tion No : 7 of 52 Marks: 1 (Budgeted Time 1 Min)
	is/are defined as the time required processing a single instruction.
sv	ver (Please select your correct option) VuAnswers.com
0	Latency and Throughput
	Latency
	correct
c	Throughput
c	Made by: Wagar Siddhu
Je	stion No : 8 of 52 Marks: 1 (Budgeted Time 1 Min)
a p	ipelining is increased by overlapping the instruction execution
nsv	wer (Please select your correct option) VuAnswers.com
0	Latency
	Throughput
C	correct
0	Execution time
3	
C	Clock speed Mode by: Jalogor Siddh
	Clock speed Made by: Wagar Siddhu stion No : 9 of 52
ue: Vh	stion No : 9 of 52 Marks: 1 (Budgeted Time 1 Min) ich of the following register(s) takes input from the ALSU as the address of the memory location to be accessed and transfers the memory contents on that location onto the
ue: Vh	stion No : 9 of 52 Marks: 1 (Budgeted Time 1 Min)
ue: Wh	stion No : 9 of 52 Marks: 1 (Budgeted Time 1 Min) ich of the following register(s) takes input from the ALSU as the address of the memory location to be accessed and transfers the memory contents on that location onto the
ve: Vh	stion No : 9 of 52 Marks: 1 (Budgeted Time 1 Min) ich of the following register(s) takes input from the ALSU as the address of the memory location to be accessed and transfers the memory contents on that location onto the
ve Vh ne	stion No : 9 of 52 Sector Control of the following register(s) takes input from the ALSU as the address of the memory location to be accessed and transfers the memory contents on that location onto the mory sub-system?
Vh	Stion No : 9 of 52 Marks: 1 (Budgeted Time 1 Min) ich of the following register(s) takes input from the ALSU as the address of the memory location to be accessed and transfers the memory contents on that location onto the mory sub-system? Wer (Please select your correct option) VuAnswers.com Instruction Register
vh ne	stion No : 9 of 52 Marks: 1 (Budgeted Time 1 Min) ich of the following register(s) takes input from the ALSU as the address of the memory location to be accessed and transfers the memory contents on that location onto the mory sub-system? wer (Please select your correct option) VuAnswers.com
	Stion No : 9 of 52 Marks: 1 (Budgeted Time 1 Min) ich of the following register(s) takes input from the ALSU as the address of the memory location to be accessed and transfers the memory contents on that location onto the mory sub-system? VuAnswers.com Instruction Register Memory address register
Wh	Stion No : 9 of 52 Marks: 1 (Budgeted Time 1 Min) ich of the following register(s) takes input from the ALSU as the address of the memory location to be accessed and transfers the memory contents on that location onto the mory sub-system? VuAnswers.com Instruction Register Memory address register
	Stion No : 9 of 52 Marks: 1 (Budgeted Time 1 Min) ich of the following register(s) takes input from the ALSU as the address of the memory location to be accessed and transfers the memory contents on that location onto the mory sub-system? VuAnswers.com Instruction Register Memory address register

	Marks: 1 (Budgeted Time 1 Min)
which one of the following addressing modes, data is the	part of the instruction itself, and so there is no need of address calculation?
r (Please select your correct option)	VuAnswers.com
Direct Addressing Mode	Vu/115WC13.0011
	<u>correct</u>
nmediate addressing mode	
ndirect Addressing Mode	
Register (Direct) Addressing Mode	Made by: Waqar Siddhu
on No : 11 of 52	Marks: 1 (Budgeted Time 1 Min)
ı one of the following type of error occurs when a 0 is received in	stead of a stop bit (which is always a 1)?
r (Please select your correct option)	VuAnswers.com
raming error	
	correct
arity error	
Over-run error	
T. 1	
naer-run error	Made by: Waqar Siddhu
	Marks: 1 (Budgeted Time 1 Min)
on No : 12 of 52	Marks: 1 (Budgeted Time 1 Min)
ion No : 12 of 52 is a technique in which some of the CPU's address lines er (Please select your correct option)	Marks: 1 (Budgeted Time 1 Min)
ion No : 12 of 52 is a technique in which some of the CPU's address lines er (Please select your correct option)	Marks: 1 (Budgeted Time 1 Min)
ion No : 12 of 52 is a technique in which some of the CPU's address lines ar (Please select your correct option) Partial decoding	Marks: 1 (Budgeted Time 1 Min) : forming an input to the address decoder are ignored. VuAnswers.com
ion No : 12 of 52 is a technique in which some of the CPU's address lines er (Please select your correct option) Partial decoding Full encoding	Marks: 1 (Budgeted Time 1 Min) : forming an input to the address decoder are ignored. VuAnswers.com
Under-run error tion No : 12 of 52	forming an input to the address decoder are ignored. VuAnswers.com correct
tion No : 12 of 52 is a technique in which some of the CPU's address lines er (Please select your correct option) Partial decoding Full encoding Partial multiplexing	Marks: 1 (Budgeted Time 1 Min) : forming an input to the address decoder are ignored. VuAnswers.com

	ion No : 13 of 52	Marks: 1 (Budgeted Time 1 Min)
5	ider Falcon A, with 16 address lines, the total address space isKbytes.	•
/	er (Please select your correct option)	VuAnswers.com
	2^16	t .
		-
	2^10	
ſ	2^6	
	2^8	Mada hu Jalaaan Giddh
1	ion No : 14 of 52	Made by: Wagar Siddhu Marks: 1 (Budgeted Time 1 Min)
_	can exchange data with a peripheral device using technique.	marks. T Daugesea Trine 1 mini
10/	er (Please select your correct option)	VuAnswers.com
Г	Memory Contention	Vu/inswers.com
	Direct Memory Access	correct
	Pre-fetching	
	Pipelining	
	• **	Made by: Waqar Siddhu
est	ion No : 15 of 52	Marks: 1 (Budgeted Time 1 Min)
ú	h one of the following is $\operatorname{\mathbf{NOT}}$ a technique used when the CPU wants to exchange data with	ith peripheral device?
w	er (Please select your correct option)	VuAnswers.com
-	Direct Memory Access	
100	Laterate Alexa UO	
	Interrupt driven I/O	
	Interrupt driven I/O Programmed I/O	
		Made by: Waqar Siddhu

ion No : 16 of 52	Marks: 1 (Budgeted Time 1 Min)
h one is the last instruction of the ISR that is to be executed when the ISR terminates?	
er (Please select your correct option)	VuAnswers.com
IRET	VUAIISWEIS.COITI
<u>correct</u>	
RQ	
INT	
NMI	
	Made by: Waqar Siddhu
ion No : 17 of 52	Marks: 1 (Budgeted Time 1 Min)
is the time needed by the CPU to recognize (not service) an interrupt request.	
er (Please select your correct option)	VuAnswers.com
Interrupt Latency	Vu/inswers.com
correct	
Response Deadline	
Fimer delay	
Phroughput	
	Made by: Waqar Siddhu
ion No : 18 of 52	Marks: 1 (Budgeted Time 1 Min)
ultiple Interrupt Lines approach, a number of interrupt lines are provided between the	modules.
er (Please select your correct option)	VuAnswers.com
External and Internal	
CPU and I/O	
<u>toenco</u>	
CPU and Memory	Mada has Jalanna Giddha
CPU and Memory	Made by: Waqar Siddhu
CPU and Memory	Made by: Waqar Siddhu
CPU and Memory	Made by: Waqar Siddhu
CPU and Memory	Made by: Waqar Siddhu
CPU and Memory	Made by: Waqar Siddhu
CPU and Memory Memory and I/O	Made by: Waqar Siddhu
CPU and Memory	Made by: Waqar Siddhu

er (Please select your corre	ct option)	VuAnswers.com
.bin		
.equ	correct	
iret		
.end		Made by: Waqar Siddhu
tion No : 20 of 52		Marks: 1 (Budgeted Time 1 Min)
irect memory access (DMA),	a is needed to control the total activity and to synchro	
er (Please select your corre	ct option)	VuAnswers.com
DMA memory unit		
DMA controller		
DIMA controller	correc	<u>t</u>
Control software		<u> </u>
		* Made by: Waqar Siddhu
Control software Programmed I/O tion No : 21 of 52		Made by: Wagar Siddhu Marks: 1 (Budgeted Time 1 Min)
Control software Programmed I/O tion No : 21 of 52	correct	Made by: Wagar Siddhu Marks: 1 (Budgeted Time 1 Min)
Control software Programmed I/O tion No : 21 of 52 allows a periphe	ral device to read from and/or write to memory without intervent	Made by: Wagar Siddhu Marks: 1 (Budgeted Time 1 Min) ion by the CPU.
Control software Programmed I/O tion No : 21 of 52	ral device to read from and/or write to memory without intervent	Made by: Wagar Siddhu Marks: 1 (Budgeted Time 1 Min)
Control software Programmed I/O tion No : 21 of 52 allows a periphe er (Please select your corre	ral device to read from and/or write to memory without intervent	Made by: Wagar Siddhu Marks: 1 (Budgeted Time 1 Min) ion by the CPU.
Control software Programmed I/O tion No : 21 of 52 allows a periphe allows a periphe er (Please select your corre Programmed I/O	ral device to read from and/or write to memory without intervent	Made by: Wagar Siddhu Marks: 1 (Budgeted Time 1 Min) ion by the CPU. VuAnswers.com
Control software Programmed I/O tion No : 21 of 52 allows a periphe er (Please select your corre Programmed I/O Interrupt driven I/O	ral device to read from and/or write to memory without intervent	Made by: Wagar Siddbu Marks: 1 (Budgeted Time 1 Min) ion by the CPU. VuAnswers.com
Control software Programmed I/O ion No : 21 of 52 allows a periphe ar (Please select your corre Programmed I/O Interrupt driven I/O Direct memory access	ral device to read from and/or write to memory without intervent	Made by: Wagar Siddhu Marks: 1 (Budgeted Time 1 Min) ion by the CPU. VuAnswers.com
Control software Programmed I/O on No : 21 of 52 allows a periphe r (Please select your corre Programmed I/O Interrupt driven I/O Direct memory access	ral device to read from and/or write to memory without intervent ct option)	Made by: Wagar Siddh Marks: 1 (Budgeted Time 1 Mir ion by the CPU. VUAnswers.com

tion No : 22 of 52	Marks: 1 (Budgeted Time 1 Min)
mponent connected to the system bus and having control of it during a particular bus cycle is called	20
er (Please select your correct option)	VuAnswers.com
Address decoder	
BIOS	
bios	
Master component	
<u>correct</u>	
Slave component	Made by: Waqar Siddhu
tion No : 23 of 52	Marks: 1 (Budgeted Time 1 Min)
ard Disk sector has the parts.	
er (Please select your correct option)	VuAnswers.com
Header only	
-	
Data section and a trailer	
Data section only	
Header, data section and a trailer	Made by: Waqar Siddhu
<u>correct</u>	
ion No : 24 of 52	
tion No : 24 of 52 C hasoverhead as compared to Hamming code.	Marks: 1 (Budgeted Time 1 Min)
hasoverhead as compared to Hamming code.	
hasoverhead as compared to Hamming code.	Marks: 1 (Budgeted Time 1 Min)
hasoverhead as compared to Hamming code. er (Please select your correct option) Equal	Marks: 1 (Budgeted Time 1 Min)
hasoverhead as compared to Hamming code. er (Please select your correct option) Equal	Marks: 1 (Budgeted Time 1 Min)
hasoverhead as compared to Hamming code. ar (Please select your correct option) Equal Greater	Marks: 1 (Budgeted Time 1 Min)
hasoverhead as compared to Hamming code. er (Please select your correct option) Equal Greater	Marks: 1 (Budgeted Time 1 Min)
hasoverhead as compared to Hamming code. er (Please select your correct option) Equal Greater Lesser Correct	Marks: 1 (Budgeted Time 1 Min)
hasoverhead as compared to Hamming code. r (Please select your correct option) Equal Greater Lesser COTEC	Marks: 1 (Budgeted Time 1 Min)
hasoverhead as compared to Hamming code. ar (Please select your correct option) Equal Greater Lesser COTEC	Marks: 1 (Budgeted Time 1 Min)
hasoverhead as compared to Hamming code. er (Please select your correct option) Equal Greater Lesser Correct	Marks: 1 (Budgeted Time 1 Min)
hasoverhead as compared to Hamming code. er (Please select your correct option) Equal Greater Lesser Correct	Marks: 1 (Budgeted Time 1 Min)
hasoverhead as compared to Hamming code. er (Please select your correct option) Equal Greater Lesser	Marks: 1 (Budgeted Time 1 Min)
hasoverhead as compared to Hamming code. er (Please select your correct option) Equal Greater Lesser Correct	Marks: 1 (Budgeted Time 1 Min)

tic	on No : 25 of 52	Marks: 1 (Budgeted Time 1 Min)
	are computed by the ALU and stored in processor status register.	
3	r (Please select your correct option)	VuAnswers.com
C	Condition Codes	
	Control Signals	
	onie os Sigurais	
F	The Flops	
N	Multiplexers	Ma de Les Seleses Ciddle
	on No : 26 of 52	Made by: Waqar Siddhu
•	on No : 26 of 52	Marks: 1 (Budgeted Time 1 Min)
	r (Please select your correct option)	VuAnswers.com
	Jnderflow	VUAIISWEIS.COITI
	Dverflow	
	<u>correct</u>	
R	Rounding Off	
N	Vormalization	
1		Made by: Waqar Siddhu
0	on No : 27 of 52	Marks: 1 (Budgeted Time 1 Min)
100	gle-Precision Binary Floating Point Representation the size of exponent is	
	r (Please select your correct option)	VuAnswers.com
3	-bits	
	11-bits	
l	-bits	
2	13-bits	
		Made by: Waqar Siddhu

•	tion No : 28 of 52	Marks: 1 (Budgeted Time 1 Min)
	is m-bits wide and contains memory address generated by the CPU directly connected	to the m-bit wide address bus.
N	ver (Please select your correct option) Memory address register (MAR)	VuAnswers.com
	corre	ct
10.0	Program counter register	
1000		
	Accumulator register	
2000	Instruction register	
1000		Made by: Waqar Siddhu
es	stion No : 29 of 52	Marks: 1 (Budgeted Time 1 Min)
	is a place for safe storage and provides the fastest possible storage after the registers.	
SW	ver (Please select your correct option)	VuAnswers.com
2	Hard Disk	
100-	Cache	
	correct	
32	Compact Disk	
Sec.		
141	Floppy Disk	Made by: Waqar Siddhu
es	stion No : 30 of 52	Marks: 1 (Budgeted Time 1 Min)
r	a request for data, if the data is available in the cache it results in a	
51	ver (Please select your correct option)	VuAnswers.com
100	Cache Miss	
11.61	Constant Transformed	
	Spatial Locality	
	Temporal Locality	
100		
100		
100 miles	Cache Hit	Made by: Waqar Siddhi

stion No : 31 of 52 write to complete in Write through, the CP	U has to wait. This wait state is called	Marks: 1 (Budgeted Time 1 Min)
ver(Please select your correct option)		VuAnswers.com
Write Through		
Write Back		
Write Allocate		
Write Stall	correct	Made by: Waqar Siddhu
stion No : 32 of 52		Marks: 1 (Budgeted Time 1 Min)
contains permanent pattern of data that	cannot be changed.	
_		
ver (Please select your correct option)		VuAnswers.com
RAM		
Hard Disk		
Cache		
ROM	Vie. 800 - 1986	
tion No : 33 of 52	correct	Made by: Waqar Siddhu Marks: 1 (Budgeted Time 1 Min)
	te the availability of page in main memory.	mana, i landeren i men
ver (Please select your correct option)		VuAnswers.com
Access Control Bits		
Used Bits		
Presence Bits		
	correct	
Redundant Bits		Made by: Waqar Siddhu
Redundant Bits		

_	stion No : 34 of 52	Marks: 1 (Budgeted Time 1 Min)
_	technique memory is divided into segments of variable sizes depending upon the re	equirements.
ารพ	ver (Please select your correct option)	VuAnswers.com
0	Multiplexing	
1610	Segmentation	
0		
0	Hamming code	
0		
c	Partial decoding	Made by: Waqar Siddhu
ues	stion No : 35 of 52	Marks: 1 (Budgeted Time 1 Min)
	depends upon the average number of calls and the service time taken by a particular serv	
nsw	ver (Please select your correct option)	VuAnswers.com
	Throughput	
0		
C	2	correct
11.00	Latency	correct
10.00		<u>correct</u>
с	Latency	<u>correct</u>
c	Latency	
с с	Latency Poisson Distribution Response Time	Made by: Waqar Siddhu
0 0	Latency Poisson Distribution	
0 0	Latency Poisson Distribution Response Time stion No : 36 of 52	Made by: Waqar Siddhu
0 0	Latency Poisson Distribution Response Time stion No : 36 of 52	Made by: Waqar Siddhu
	Latency Poisson Distribution Response Time stion No : 36 of 52	Made by: Waqar Siddhu Marks: 1 (Budgeted Time 1 Min)
C	Latency Poisson Distribution Response Time stion No : 36 of 52	Made by: Waqar Siddhu
C C Ues	Latency Poisson Distribution Response Time stion No : 36 of 52	Made by: Waqar Siddhu Marks: 1 (Budgeted Time 1 Min)
C D D D D D D D D D D D D D D D D D D D	Latency Poisson Distribution Response Time stion No : 36 of 52	Made by: Waqar Siddhu Marks: 1 (Budgeted Time 1 Min)
C Ues	Latency Poisson Distribution Response Time tion No : 36 of 52	Made by: Waqar Siddhu Marks: 1 (Budgeted Time 1 Min)
	Latency Poisson Distribution Response Time stion No : 36 of 52	Made by: Magar Siddhu Marks: 1 (Budgeted Time 1 Min) VuAnswers.com
C C	Latency Poisson Distribution Response Time stion No : 36 of 52	Made by: Magar Siddhu Marks: 1 (Budgeted Time 1 Min) VuAnswers.com

on No : 37 of 52	Marks: 1 (Budgeted Time 1 Min)
is the maximum rate at which data can be transmitted through networks.	
er (Please select your correct option)	VuAnswers.com
Fransmission Time	
Transie Contraction of Contraction o	
Latency	
Transport Latency	
Bandwidth	Made has \$ closers 6:ddhr
on No : 38 of 52	Made by: Waqar Siddhu
on No : 38 of 32 me for the message to pass through the network, except the time of flight is called	Marks: 1 (Budgeted Time 1 Min)
	-
	\ /
r (Please select your correct option) Transmission Time	VuAnswers.com
	correct
Latency	
Fransport Latency	
Bandwidth	Made by: Waqar Siddhu
on No : 39 of 52	Marks: 1 (Budgeted Time 1 Min)
_ refers to the interconnection of machines in a building or a campus.	
er (Please select your correct option)	VuAnswers.com
SAN	
LAN	
correct	
WAN	
MAN	
	Made by: Waqar Siddhu

n No : 40 of 52 Mar	ks: 1 (Budgeted Time 1 Min
C (Scalable Processor Architecture) is an example ofarchitecture.	
	~
(Please select your correct option) VUAnswers.co	
se conset	
Made by: Maqu	ar Siddh
	ks: 2 (Budgeted Time 4 Min
(Please <u>click here</u> to Add Answer)	m
E ● A 3 D E 4 0 0 4 5 0 1 1 1 2 1 100% ■ ▼ And ▼ 12 ▼ B I U E E E E E E E E E E E E E E E E E E	
E 会 人 と 能 い い 林 ら こ ま て 2 100% マ And マ セ マ B エ U 目 手 手 目 日 手 作 In distributed computing, all elements which are interconnected operate under oneoperating system. To a user, it appears as a virtual uni- processor system.	
E 会 人 と 能 い い 林 ら こ ま て 2 100% マ And マ セ マ B エ U 目 手 手 目 日 手 作 In distributed computing, all elements which are interconnected operate under oneoperating system. To a user, it appears as a virtual uni- processor system.	ar Siddh
Area vitual uni- processor system. No : 42 of 52	er Siddh ks: 2 (Budgeted Time 4 Min
And DIE EIEEEEEEEEE In distributed computing, all elements which are interconnected operate under oneoperating system. To a user, it appears as a virtual uniprocessor system. More 42 of 52 No : 42 of 52 Mar No : 42 of 52 Mar Mar Myode bag: More 42 of 52 Mar Mar More 54 of 52 Mar Mar Mar Mar More 54 of 52 Mar Mar Mar More 54 of 52 Mar Mar Mar More 54 of 52 Mar Mar Mar Mar More 54 Mar Mar Mar More 54 Mar Mar More 54 Mar Mar	er Siddh ks: 2 (Budgeted Time 4 Min
Image: Second Action Seco	er Siddh ks: 2 (Budgeted Time 4 Min

on No : 43 of 52	Marks: 2 (Budgeted Time
rentiate between selector channel and multiplexer channel.	
wer(Please <u>click here</u> to Add Answer)	VuAnswers.com
annal ▼ Arial ▼ 12 ▼ B Z U 目標目目 日日 存年	
Selector ChannelIt is the DMA controller that can do block transfe but only one at a time. Multiplexer ChannelIt is the DMA controller that can do block tra devices at once.	
estion No : 44 of 52	Marks: 2 (Budgeted Time 4
swer (Please <u>click here</u> to Add Answer)	VuAnswers.com
〕 📽 🗟 💁 🐧 🕺 陶 値 🖉 ♀ ♀ 🗰 👘 🖬 🖷 ¶ 😰 100% ▼ kornal 🔹 🗶 Arial 🔹 👔 12 💌 B / U 藍合言目 語日 提供	
 PROM The PROM stands for Programmable Read only Memory. It is also n written into onlyonce. For PROM, the writing process is performed e PROMs provide flexibility and convenience. EPROM Erasable Programmable Read-only Memory or EPROM chips have applying ultravioletlight erase the data can be erased from the EPROM 	electrically in the field. quartz windows and by M. Data can be restored
in an EPROM after erasure. EPROMsare more expensive than PRO used for prototyping or small-quantity, special purposework.	
	Made by: Waqar Sidd
estion No : 45 of 52	Marks: 3 (Budgeted Time 6
ow can you define an instruction set? Name the essential elements of computer instructions.	
swer (Please <u>click here</u> to Add Answer)	VuAnswers.com
▶ 2 2 2 3 3 10 10 10 2 10 10 10 10 10 10 10 10 10 10 10 10 10	
INSTRUCTION SET An instruction set is a collection of all possible machine la areunderstood and can be executed by a processor. ESSENTIAL ELEMENTS OF COMPUTER INSTRUCTIONS: There are t an instruction; the type of operation to be performed, the place to find the so place to store the result(s) and the source of the next instruction to be executed	four essential elements of purce operand(s) , the
	Made by: Waqar Sidd
For More Visit	

That is the relationship between hard disk Platters, Tracks and Sectors?	Marks: 3 (Budgeted Time 6 M
swer (Please <u>click here</u> to Add Answer)	VuAnswers.com
	VuAnswers.com
▶ 📽 🗃 💽 🕺 為 🖻 簡 🗠 🔌 👪 🖬 🕻 🕯 ¶ 🗍 🕄 🗍 🗍 😨 🚺 🚺 🖬 🖉 🖉 🖉 🚺 🖬 🖉 🖉 🚺 🛃 🖉 🖉 🖉 🖉 🖉 🖉 🖉 🖉 🖉 🖉 🖉 🖉 🖉	
A hard disk is the most frequently used peripheral device. It consists of a set of platters. Each platter is divided into tracks. The track is subdivided into sectors. To identify each sector, we need to have an address. So, before a ctual data, there is a header and this header consisting of few bytes like 10 bytes. Along with header there is a trailer. Every sector has three parts: a header, data section and a trailer.	
	Made by: Waqar Siddl
uestion No : 47 of 52	Marks: 3 (Budgeted Time 6 M
and out the Sign, Significand and Exponent from the following floating point number: $0.7 imes10^{-4}$	
nswer (Please <u>click here</u> to Add Answer)	VuAnswers.com
► # # # X & @ > ~ # 5, I @ ¶ 7 100% ▼	VuAnswers.com
Armal 文 Arial マ 12 ア B ズ U 単変 目 生活 使 使 Sign = 0 Significand= 0.7 Exponent - 4 Base = 10= fixed for given type of representation	Made by: Wagar Siddl Marks: 3 (Budgeted Time 6 M
Armal 文 Arial マ 12 ア B ズ U 単変 目 生活 使 使 Sign = 0 Significand= 0.7 Exponent - 4 Base = 10= fixed for given type of representation	Made by: Waqar Siddh
Image: Sign = 0 Significand= 0.7 Exponent= 4 Base = 10= fixed for given type of representation	Made by: Waqar Siddh
○ ○ ●<	Made by: Magar Siddl Marks: 3 (Budgeted Time 6 M
Image: Sign = 0 Significand= 0.7 Exponent=-4 Base = 10= fixed for given type of representation Image: Significand= 0.7 Exponent=-4 Base = 10= fixed for given type of representation	Made by: Magar Siddl Marks: 3 (Budgeted Time 6 M
Image: Sign = 0 Significand= 0.7 Exponent = 4 Base = 10= fixed for given type of representation	Made by: Magar Siddl Marks: 3 (Budgeted Time 6 M

For N	Aore	Visit	VU	Answer
-------	------	-------	----	--------

				Marks: 5 (Budgeted Time 10 Min)
rite the Structural RTL	description for "un-conditional jump" instruction i.e. jun	np [ra+c2].		
			1.6.8	
swer (Please <u>click her</u>			VuAnswe	ers.com
l 📽 🖬 🎒 💽 🕺	B C C T C 100% ▼ ▼ 12 ■ B T U E<	日作作		
Step	RTL	Step	RTL	
T0-T2	Instruction Fetch	T0-T2	Instruction fetch	
Т3	(ra=0): A← PC, (ra≠0): A ← R[ra];	T3	n<40> ← IR<40>;	
T4	$C \leftarrow A + c2(sign extend);$	T4	C ← (Nα0) © R[rb]<15N>;	
T5	PC ← C;		R[ra] ← C:	
			(IL)QUE DA: 1	Vaqar Siddhu
	ers has 1024 tracks per platter, 512 sectors per track a	nd 512 bytes/sector		
estion No : 50 of 52		nd 512 bytes/sector	. What is the total capacity of the disk?	Marks: 5 (Budgeted Time 10 Min
estion No : 50 of 52 hard disk with 10 platte swer (Please <u>click her</u>	re to Add Answer)	nd 512 bytes/sector		Marks: 5 (Budgeted Time 10 Min)
estion No : 50 of 52 hard disk with 10 platte swer (Please <u>click her</u>	<u>re</u> to Add Answer) 같: (같: (. What is the total capacity of the disk?	Marks: 5 (Budgeted Time 10 Min
hard disk with 10 platte wer (Please <u>click her</u>) Cornel Artal	ne to Add Answer) Β Ε ∽ ~ ▲ Β,	nd 512 bytes/sector	. What is the total capacity of the disk?	Marks: 5 (Budgeted Time 10 Min
estion No : 50 of 52 hard disk with 10 platte swer (Please <u>click her</u>	ne to Add Answer) Β Ε ∽ ~ ▲ Β,		. What is the total capacity of the disk?	Marks: 5 (Budgeted Time 10 Min
estion No : 50 of 52 hard disk with 10 platte swer (Please <u>click her</u> estimated by the state tormal I Artal	ne to Add Answer)		. What is the total capacity of the disk?	Marks: 5 (Budgeted Time 10 Min)
hard disk with 10 platte wer (Please click her Place C	ne to Add Answer)		. What is the total capacity of the disk?	Marks: 5 (Budgeted Time 10 Min
estion No : 50 of 52 hard disk with 10 platte wer (Please click her is a line of the sectors of	Image: Contract of the second seco	百存伊	. What is the total capacity of the disk?	Marks: 5 (Budgeted Time 10 Min)
estion No : 50 of 52 hard disk with 10 platte sever (Please click her is a line of the sector secto	te to Add Answer)	百存伊	: What is the total capacity of the disk? VuAnswe	Marks: 5 (Budgeted Time 10 Min

on No : 52 of 52 ose an I/O system with a single disk gets (on average) 100 I/O requests/second and the average time for a disk to service an I r (Please click here to Add Answer) Image: Im	D=000010 100000 d=000101 Dif(-) q=00100 D=000101 000000 d=000101 Dif(+) q=001001 Dif(+) q=001001
The property CLUPDY VUES 65 Sim., Fell. into the : St of S2 and the reper involved in integer division algorithm to divide 45% by 5%: and the reper involved in integer division algorithm to divide 45% by 5%: and the reper involved in integer division algorithm to divide 45% by 5%: and the reper involved in integer division algorithm to divide 45% by 5%: and the reper involved in integer division algorithm to divide 45% by 5%: and the reper involved in integer division algorithm to divide 45% by 5%: and the reper involved in integer division algorithm to divide 45% by 5%: and the reper involved in integer division algorithm to divide 45% by 5%: and the reper involved in integer division algorithm to divide 45% by 5%: and the reper involved in integer division algorithm to divide 45% by 5%: and the reper involved in integer division algorithm to divide 45% by 5%: and the reper involved in integer division algorithm to divide 45% by 5%: and the reper involved in integer division algorithm to divide 45% by 5%: and the reper involved integer invo	D=000010 100000 d=000101 100000 d=000101 Dif(-) q=00100 D=000101 000000 d=000101 Dif(+) q=001001
annalous hydrem (LIRMY) VITES 6.5 ::: Fel tion No : 51 of 52	D=000010 100000 d=000101 100000 d=000101 Dif(-) q=00100 D=000101 000000 d=000101 Dif(+) q=001001
integer division algorithm to divide 45% by 5%. er (Please click here to Add Answer) Image:	D=000010 100000 d=000101 100000 d=000101 Dif(-) q=00100 D=000101 000000 d=000101 Dif(+) q=001001
Show all the steps involved in integer division algorithm to divide 45 to 55 to 55 to 52 to 52 to 52 to 52 to 55 t	D=000010 100000 d=000101 Dif(-) g=00100 D=000101 000101 Dif(+) g=001001
er (Please click here to Add Answer)	D=000010 100000 d=000101 Dif(-) q=00100 D=000101 000000 d=000101 Dif(+) q=001001 Cade by: Wars: 5 (Budgeted Tin O request is 6ms. What is the utilization of the I/O system
er (Please click here to Add Answer)	d=000101 Dif(-) q=00100 D=000101 000000 d=000101 Dif(+) q=001001
er (Please click here to Add Answer)	d=000101 Dif(-) q=00100 D=000101 000000 d=000101 Dif(+) q=001001
Image: Construction of the second	d=000101 Dif(-) q=00100 D=000101 000000 d=000101 Dif(+) q=001001
Image: Contract of the second seco	d=000101 Dif(-) q=00100 D=000101 000000 d=000101 Dif(+) q=001001
$\frac{1}{10000000000000000000000000000000000$	d=000101 Dif(-) q=00100 D=000101 000000 d=000101 Dif(+) q=001001
$\frac{1}{10000000000000000000000000000000000$	d=000101 Dif(-) q=00100 D=000101 000000 d=000101 Dif(+) q=001001
$\frac{1}{\frac{1}{1}} = \frac{1}{1} $	d=000101 Dif(-) q=00100 D=000101 000000 d=000101 Dif(+) q=001001
$\frac{1}{\frac{1}{1}} = \frac{1}{1} $	d=000101 Dif(-) q=00100 D=000101 000000 d=000101 Dif(+) q=001001
$\frac{d=000101}{\text{Diff}(\cdot)} q=0$ $\frac{d=000101}{\text{Diff}(\cdot)} q=001$ $\frac{d=00101}{\text{Diff}(\cdot)} q=001$ $\frac{d=001}{\text{Diff}(\cdot)} q=001$	d=000101 Dif(-) q=00100 D=000101 000000 d=000101 Dif(+) q=001001
Dif(-) $q=0$ d=000101 110100 d=000101 Dif(-) $q=00$ Dif(-) $q=0010$ d=000101 Dif(-) $q=0010$ d=000101 q=0010 d=000101 q=0010 q=000 q=010 q=010 q=0010 q=000 q=010 q=010 q=0010 q=000 q=00 q=0.6 sec	Dif(-) q=00100 D=000101 000000 d=000101 Dif(+) q=001001 Code by: Warks: 5 (Budgeted Tin O request is 6ms. What is the utilization of the I/O system
D=000010 110100 d=000101 Dif(-) g=00 Dif(-) g=00 Dif(-) g=0010 f COOOD Dif(-) g=0010 Dif(-) g=0010 f COOOD Dif(-) g=0010 f COOOD Dif(-) g=0010 f COOOD Dif(-) g=0010 f COOOD Dif(-) g=0010 f COOOD Dif(-) g=0010 f COOOD Dif(-) g=0010 f COOOD Dif(-) g=0010 f COOOD CO	D=000101 000000 d=000101 Dif(+) q=001001
Dif(-) q=00 Time 7.37 PM Hence remainder = (000000);2 =0 Quotient = (001001;2 = 9;10 x n No : 52 of 52 se an I/O system with a single disk gets (on average) 100 I/O requests/second and the average time for a disk to service an I (Please click here to Add Answer) Please click here to Add Answer) Time for an I/O request = 6ms =0.005sec Server utilization = 100 x 0.006 = 0.6 sec	Dif(+) q=001001
Imme 7:37 PM Hence remainder = (000000), 2 = 0 Quotient = (001001), 2 = 9, 10 Imme 100 system with a single disk gets (on average) 100 I/O requests/second and the average time for a disk to service an I Immo 1: 52 of 52 se an I/O system with a single disk gets (on average) 100 I/O requests/second and the average time for a disk to service an I Immo 1: 52 of 52 se an I/O system with a single disk gets (on average) 100 I/O requests/second and the average time for a disk to service an I Immo 1: 0 system with a single disk gets (on average) 100 I/O requests/second and the average time for a disk to service an I Immo 1: 0 system with a single disk gets (on average) 100 I/O requests/second and the average time for a disk to service an I Immo 1: 0 system with a single disk gets (on average) 100 I/O requests/second and the average time for a disk to service an I Immo 1: 0 system with a single disk gets (on average) 100 I/O requests/second and the average time for a disk to service an I Immo 1: 0 system	Cade by: Waqar Si Marks: 5 (Budgeted Tin O request is 6ms. What is the utilization of the I/O system
Hence remainder = (000000 ₁₂ = 9 Quotient = (001001 ₁₂ = 9 ₁₀ * in No : 52 of 52 se an I/O system with a single disk gets (on average) 100 I/O requests/second and the average time for a disk to service an I (Please click here to Add Answer) (Please click here to Add Answer) (Please click here to Add Answer) Time for an I/O request = 6ms =0.006scc Server utilization = 100 x 0.006 = 0.6 sec	Jade by: Waqar Si Marks: 5 (Budgeted Tin O request is 6ms. What is the utilization of the 1/0 system
Hence remainder = (000000 ₁₂ = ⁹ Quotient = (001001 ₁₂ = ⁹ ₁₀ n No : 52 of 52 se an I/O system with a single disk gets (on average) 100 I/O requests/second and the average time for a disk to service an I (Please click here to Add Answer) (Please click here to Add Answer) Please click here to Add Answer) Time for an I/O request = 6ms =0.006sec Server utilization = 100 x 0.006 = 0.6 sec	Jade by: Waqar Si Marks: 5 (Budgeted Tin O request is 6ms. What is the utilization of the 1/0 system
Hence remainder = (000000 ₁₂ = ⁹ Quotient = (001001 ₁₂ = ⁹ ₁₀ n No : 52 of 52 se an I/O system with a single disk gets (on average) 100 I/O requests/second and the average time for a disk to service an I (Please click here to Add Answer) (Please click here to Add Answer) Please click here to Add Answer) Time for an I/O request = 6ms =0.006sec Server utilization = 100 x 0.006 = 0.6 sec	Jade by: Waqar Si Marks: 5 (Budgeted Tin O request is 6ms. What is the utilization of the 1/0 system
Implete Valuent = (001001,2 - 9,10 x m No : 52 of 52 se an I/O system with a single disk gets (on average) 100 I/O requests/second and the average time for a disk to service an I (Please click here to Add Answer) Implete Implete <t< td=""><td>Marks: 5 (Budgeted Tin O request is 6ms. What is the utilization of the I/O system</td></t<>	Marks: 5 (Budgeted Tin O request is 6ms. What is the utilization of the I/O system
n No : 52 of 52 se an I/O system with a single disk gets (on average) 100 I/O requests/second and the average time for a disk to service an I (Please click here to Add Answer) Click here to Add Answer) Click here to Add Answer () Click here t	Marks: 5 (Budgeted Tin O request is 6ms. What is the utilization of the I/O system
se an I/O system with a single disk gets (on average) 100 I/O requests/second and the average time for a disk to service an I (Please click here to Add Answer) (Please cl	O request is 6ms. What is the utilization of the I/O system
(Please <u>click here</u> to Add Answer)	
=0.006sec Server utilization = 100 x 0.006 = 0.6 sec	
= 0.6 sec	
= 0.6 sec	
Ma	
Ma	
Ma	
Ma	
	le by: Waqar Sic