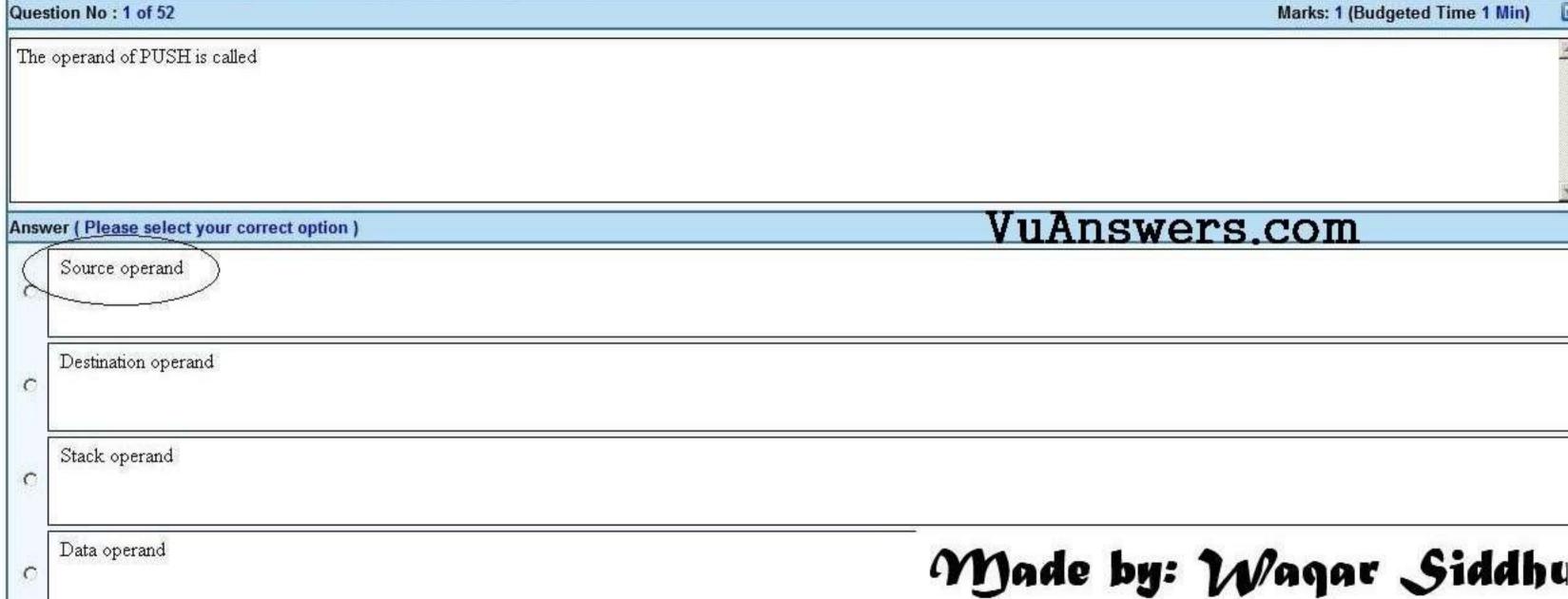


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The clear screen operation initializes whole block of video memory to	
Answer (Please select your correct option)	VuAns
0417	Y UAIIS
c 0741	
. 0714	
с 0174	Made k





Which of the following IRQs is connected to serial port COM 2?	
Answer (Please select your correct option)	
c IRQ 0	VuAns
c IRQ 1	
c IRQ 2	
• IRQ 3	Made b





Which of the following IRQs is connected to serial port COM 1?	
Answer (Please select your correct option)	VuAns
C IRQ 5	
c IRQ 6 IRQ 7 C	
	Made b





Wh	ich of the following IRQs is used for sound card or network card?	
Ansv	ver (Please select your correct option)	
0	IRQ 4	VuAns
•	IRQ 5	
С	IRQ 6	
0	IRQ 7	Made b





The	e programmable interval timer (PIT) has input frequency of	
Ansv	wer (Please select your correct option)	
•	1.193MHZ	VuAns
0	2.193MHZ	
С	3.193MHZ	
0	4.193MHZ	Made b





By	executing which of the following instruction, the value in AX register becomes the half of pre	viously stored value ?
Ansv	wer (Please select your correct option)	
0	Sh1 AX, 1	VuAns
	Shr AX, 1	
С	NOT AX	
0	AND AX, 0xFFFF	Made b





8088 processor divides interrupts into classes.	
Answer (Please select your correct option)	Tuna
λ^2	VuAns
c 3	
c 4	
° [5	Made k





In .	A4FB:4872 Segment:offset pair the physical address is (both segment and offset are in hexadecimal):	
Ansv	wer (Please select your correct option)	
0	A4FB0	VuAns
C	04872	
4	A9822	
0	A4872	Made b





When	When the instruction "CMP AX,BX" executes and after execution ZF is set, it indicates that		
Answe	er (Please select your correct option)		
0	AX is greater	VuAns	
0	BX is greater		
•	Both AX and BX are equal		
0	ZF has no relation with CMP	Made b	





Tradi	itionally all the mathematical and logical operations are performed in	
Answe	er (Please select your correct option)	
c	Code Segment register	VuAns
c	Data Segment register	
J.	Accumulator register	
0	Stack segment register	Made b





Stack is a data structure which behaves as first in last out manner 59		
Answer	r (Please select your correct option)	
C	First in first out manner	VuAns
•	Last in first out manner	
c	All in no out manner	
c	Last in last out manner	Made I





Seg	ment wraparound occurs when during the effective address calculation	is generated.	
Алем	ver (Please select your correct option)		
	carry		VuAns
C	error		
С	zero		
0	parity		Made b





The bits	s of register work independently. If combined all these bit	s then they are meaningless.
Answer (Ple	ease select your correct option)	
c	Accumulator (AX)	VuAns
с	Instruction Pointer (IP)	
c C	Flag	
c	Base	Made k





IBN	IPC has separate memory address space and peripheral address space. Memory address space is selected when	instruction is given
Ansv	ver (Please select your correct option)	
С	MOV	VuAns
с	DEC	
6	M	
С	ADD	Made k

n to the processor.



INT 10 is used for	services.	
Answer (Please select yo	ur correct option)	VuAns
C DOS video		
CRAM		
C Disk		Made k





are not used in mathematical operations.	
Answer (Please select your correct option)	
Carry, Interrupt and Trap flag Direction, Interrupt and Trap flag	VuAns
Direction, Overflow and Trap flag	
C Direction, Interrupt and Sign flag	Made k





VESA stands for	
Answer (Please select your correct option)	
C Variable Electronics Standards Association	VuAns
Video Electronics Standards Association	
Video Energy Standards Association	
C Vega Energy Standards Association	Made b





In Computer Architecture, the Protected mode is different from	
Answer (Please select your correct option) Programming mode	VuAns
Real time mode not sure	
C Visualization mode	
C Memory mode	Made b





In Assembly language programming, how many prevalent Calling Convention	ions do exist ?
Answer (Please select your correct option)	VuAns
2	
с <mark>3</mark>	
с <mark>4</mark>	Made b





In	Computer Architecture, RISC stands for	
	wer (Please select your correct option) Reversed Instruction Set Computer	VuAn
0	Reversed Instruction System Computer	
	Reduced Instruction Set Computer	
e	Reduced Instruction System Computer	Made





Computer Architecture, CISC stands for	
wer (Please select your correct option)	
Complex Instruction Set Computer	VuAns
Complex Instruction System Computer	
Correct Instruction Set Computer	
Correct Instruction System Computer	Made I
	Complex Instruction System Computer Correct Instruction Set Computer





Which byte of the DOS input buffer holds the number of characters ac	tually read on return?
Answer (Please select your correct option) First Byte	VuAns
Second Byte	
C Third Byte	
C Last Byte	Made k





In multi-tasking environment, which is called scheduler for saving an	d restoring the registers?
Answer (Please select your correct option)	
C INT 10	VuAns
C INT 09	
INT 08	
C INT 07	Made k





The	thread registration code initializes the PCB and adds it to the linked list. so that which will give it a turn?	
Answ	ver (Please select your correct option) scheduler	VuAns
0	linker	
С	debugger	
0	assembler	Made b





In multi-tasking programming, all threads are	
Answer (Please select your correct option) Independent to each other	VuAns
C Dependent on each other	
C Not executed	
C Not scheduled	Made b





"INT 10 Video - Scroll Up Window" is a type of video services us	ed in which mode?
Answer (Please select your correct option) Graphics	VuAns
C Text	
د Audio	
c Numeric	Made k





BIOS sees the disks as	
Answer (Please select your correct option)	
C logical storage device	VuAns
raw storage device	
in the form of sectors only	
c in the form of tracks only	Made b





Which of the following gives more logical view of the storage medium	17
Answer (Please select your correct option) DOS	VuAns
C RAM	
CROM	
CBIOS	Made b





In d	device attribute word, which of the following bit decides whether it	is a character device or a block device ?
Ansv	wer (Please select your correct option)	
с	Bit 12	VuAns
с	Bit 13	
с	Bit 14	
5	Bit 15	Made k





The serial port connection is a connector	
Answer (Please select your correct option)	
9 pin DB-9	VuAns
c 8 pin DB-9	
c 3 pin DB-9	
c 9 pin DB-5	Made k





In 9 pin DB-9, which pin number is assigned to TD (Transmitted I	Data) ?
Answer (Please select your correct option)	VuAns
6	V UAIIS
c 2	
c 4	Made b





In 9 pin DB-9, RI (Ring Indicator) is assigned on pin number	
Answer (Please select your correct option)	VuAns
c 7	VUAIIS
c 8	
9	
с ⁶	Made k





8088 is a	
Answer (Please select your correct option) 16 bit processor	VuAns
C 32 bit processor	
c 64 bit processor	
c 128 bit processor	Made b





The table index (TI) is set to to access the GDT (Global Descriptor Table).	
Answer (Please select your correct option)	VuAns
c -1	
c -2	Made k





In 68K processors there is a program counter (PC) that holds the address of currently executing instruction.		
Answ	ver (Please select your correct option)	
c	8 bit	VuAns
C	16 bit	
2	32 bit	
C	64 bit	Made b





In 68K processors there is a 32 bit that holds the address of cu	rently executing instruction.
Answer (Please select your correct option) Program counter	VuAns
C Stack pointer	
C Register	
c Stack	Made b





An 8 x 16 font is stored in	bytes.	
Answer (Please select your correct o	ption)	VuAns
с 4 с		
c 8		
16		Made k





Whic	h of the following interrupts is Non Maskable Interrupt (NMI).	
Answe	er (Please select your correct option)	
•	INT 2	VuAns
0	INT 3	
c	INT 0	
0	INT 1	Made b





number of pin() of a processor is/are used by the external hardware to generate an interrupt.
Answer (Please select	
o ¹	VuAn
c 2	
с ³	
c 4	Made





Question No : 1 of 52	
After the execution of "PUSH AX" statement	
Answer (Please select your correct option)	VuAn
C AX register will reside on the stack	
A copy of AX will go on the stack	
The value of AX disappear after moving on stack	
C Stack will send an acceptance message	Made







Question No : 2 of 52	
Which of the following IRQs is connected to serial port COM 1?	
Answer (Please select your correct option)	VuAns
IRQ 4	
c IRQ 5	
c IRQ 6	
c IRQ 7	Made







Que	Question No : 3 of 52	
An	End of Interrupt (EOI) signal is sent by	
Ans	wer (Please select your correct option)	VuAns
•	Handler	
o	Processor	
0	IRQ	
16	PIC	Made







Que	estion No : 4 of 52	
The	e programmable interval timer (PIT) has input frequency of	
Ansv	wer (Please select your correct option)	VuAn
•	1.193MHZ	
С	2.193MHZ	
o	3.193MHZ	
c	4.193MHZ	Made







Que	stion No : 5 of 52	
Wh	nich of the following interrupt is of highest priority interrupt?	
Ansv	wer (Please select your correct option)	VuAn
C	Key board interrupt	
•	Timer interrupt	
o	INT 2	
с	INT 3	Made







Que	uestion No : 6 of 52	
The	e actual physical address represented by the segment-offset pair "AB01:FFFF" will be:	
Ansv	wer (Please select your correct option)	VuAns
C	1AB00	
C	001AB	
•	BB00F	
с	F00BB	Made







Question No : 7 of 52		
	ose DH contains 10001010 and CL contains 3, what is the value of DH after the following instru DH, CL	action:
Answei	r (Please select your correct option)	VuAns
	01010000	
0	10001010	
0	00010100	
c	01010111	Made I







Question No : 8 of 52	Question No : 8 of 52	
When two devices in the system want to use the same IRQ line i	then what will happen?	
Answer (Please select your correct option)	VuAn	
C An IRQ Collision		
An IRQ Conflict		
C An IRQ Crash		
C An IRQ Blockage	Made	







Que	uestion No : 9 of 52	
Afte	er the execution of STOSW, the CX will be	
Ansv	wer (Please select your correct option)	VuAns
•	Decremented by 1	
c	Decremented by 2	
c	Incremented by 1	
с	Incremented by 2	Made







Que	estion No : 10 of 52	
W	hich of the following is true ?	
Ans	wer (Please select your correct option)	VuAn
•	In general the memory cell cannot be wider than data bus Control bus is unidirectional	
0	Memory can use the address bus to refer to certain register in processor	
с	There is no relation between memory and data bus	Mada







Que	uestion No : 11 of 52		
Tra	aditionally all the mathematical and logical operations are performed in		
Ansv	wer (Please select your correct option)	VuAn	
c	Code Segment register		
с	Data Segment register		
•	Accumulator register		
с	Stack segment register	Made	







Questic	Question No : 12 of 52	
Relatir	ng to the stack in 8088 architecture which of the following statement is true ?	
Answei	r (Please select your correct option)	VuAns
c	Single word can not be pushed on the stack	
c	Single word can not be popped from the stack	
•	Single byte can not be popped from the stack	
c	Single quad can be popped from the stack	







Questi	Question No : 13 of 52		
The s	second byte of the word designated for one screen location holds		
Answe	er (Please select your correct option)	VuAn	
c	The dimensions of the screen		
c	Character position on the screen		
•	Character color on the screen		
c	ASCII code of the character	Made	







Question No : 14 of 52		
INT 10 is used for se	ervices.	
Answer (Please select your correc	t option)	VuAns
BIOS video		
C DOS video		
CRAM		
C		Made







Que	Question No : 15 of 52		
In graphics mode a location in video memory corresponds to a		on the screen.	
Ansv	wer (Please select your correct option)		VuAns
C	line		
•	dot		
o	circle		
с	rectangle		Made







Question No : 16 of 52		
The value of AH register in the write Graphics pixel service is		
Answer (Please select your correct option)	VuAns	
• OCh		
C 0Bh		
c 1Ch		
c 2Ch	Made	







Que	uestion No : 17 of 52	
The	e space where the current state of a task is stored is called	
Ans	wer (Please select your correct option)	VuAns
c	control block	
¢	Process control block	
o	stack	
c	hard disk	Made







Que	uestion No : 18 of 52		
INT	C21 service 01H is used to read character from standard input with echo. It returns the result in	register.	
Ansv	ver (Please select your correct option)		VuAns
a	al		
C	61		
0	cl		
С	bh	Ĺ	Made I







Que	Question No : 19 of 52	
VE	SA VBE 2.0 is a standard for	
Ansv	wer (Please select your correct option)	VuAns
o	Ultra resolution mode	
•	High resolution mode	
c	Medium resolution mode	
c	Low resolution mode	Made







Que	uestion No : 20 of 52	
VE	SA stands for	
Ansv	wer (Please select your correct option)	VuAn
o	Variable Electronics Standards Association	
•	Video Electronics Standards Association	
o	Video Energy Standards Association	
c	Vega Energy Standards Association	Made







Question No : 21 of 52	
VBE stands for, as proposed by VESA (an organization).	
Answer (Please select your correct option)	VuAn
Video BIOS Extensions	
C Video BIOS Emergence	
C Video Binary Extensions	
C Video Blocked Extensions	Made







Que	Question No : 22 of 52		
ID'I	TR is a register.		
Ansv	wer (Please select your correct option)	VuAns	
C	16 bit		
с	20 bit		
o	32 bit		
	48 bit	Made I	







Que	uestion No : 23 of 52	
In	Computer Architecture, the Protected mode is different from	
Ans	wer (Please select your correct option)	VuAns
c	Programming mode	
	Real time mode	
0	Visualization mode	
с	Memory mode	Made







Que	lestion No : 24 of 52	
Wh	nich byte of the DOS input buffer holds the number of characters actually read on return?	
Ans	wer (Please select your correct option)	VuAns
c	First Byte	
	Second Byte	
0	Third Byte	
С	Last Byte	Made







Que	uestion No : 25 of 52		
Wh	nat is the size of a Sector of a standard 1.44MB Floppy disk having 80 tracks and 18 Sectors/track?		
Ansv	wer(Please select your correct option)	VuAns	
C	128 Bytes		
С	256 Bytes		
	512 Bytes		
c	1024 Bytes	Made	







Que	uestion No : 26 of 52	
In n	nulti-tasking environment, which is called scheduler for saving and restoring the registers?	
Ansv	wer (Please select your correct option)	VuAns
c	INT 10	
с	09 TVI	
•	80 TM	
С	INT 07	Made







Que	uestion No : 27 of 52	
Thr	reads can have function calls, parameters and which type of variables?	
Ansv	wer (Please select your correct option)	VuAna
0	global	
•	local	
o	legal	
с	illegal	Made







Que	Question No : 28 of 52		
Technically LAPX88 stands for "		Advanced Processor Extensions 88."	
Ansv	wer (Please select your correct opt	ion)	VuAns
•	Intel		
c	Integrated		
c	Intra		
с	Internal		Made







Que	uestion No : 29 of 52	
InI	Left-Shift-Operation, the left most bit	
Ansv	wer (Please select your correct option)	VuAns
c	will be dropped	
	will go into CF	
0	will be moved to the right-most position	
с	will always be 1	Made







Que	uestion No : 30 of 52	
The serial port connection is a connector		
Ans	wer (Please select your correct option)	VuAns
	9 pin DB-9	
c	8 pin DB-9	
o	3 pin DB-9	
c	9 pin DB-5	Made







Que	uestion No : 31 of 52		
In S	9 pin DB-9, which pin number is assigned to CTS (Clear To Send) ?		
Ansv	wer (Please select your correct option)	VuAn	
c	6		
с	7		
•	8		
с	9	Made	







Que	Question No : 32 of 52		
In S	9 pin DB-9, which pin number is assigned to CD (Carrier Detect) ?		
Ans	wer (Please select your correct option)	VuAna	
•	1		
с	2		
0	3		
c	4	Made	







Que	uestion No : 33 of 52		
Wł	hich of the following BIOS INT provides serial port services		
Ansv	wer (Please select your correct option)	VuAns	
•	INT14		
c	INT11		
o	INT12		
c	INT13	Made	







Que	Question No : 34 of 52		
8088 is a			
Ans	wer (Please select your correct option)	VuAns	
	16 bit processor		
с	32 bit processor		
o	64 bit processor		
с	128 bit processor	Made	







Que	Question No : 35 of 52		
A 3	32 bit address register can access up to of memory, so memory access has increased a lot.		
Ansv	wer (Please select your correct option)	VuAns	
c	2GB		
•	4GB		
c	6GB		
с	8GB	Made	







Question No : 36 of 52		
The table index (TI) is set to to access the GDT (Global Descriptor T	able).	
Answer (Please select your correct option)	VuAn	
o ¹		
• 0		
c -1		
c -2	Made	







Que	uestion No : 37 of 52		
In 6	58K processors there is a program counter (PC) that holds the address of currently executing instruction.		
Ansv	wer (Please select your correct option)	VuAn	
C	8 bit		
c	16 bit		
	32 bit		
С	64 bit	Nade	







Que	Question No : 38 of 52		
In	68K processors there is a 32 bit that holds the address of currently executing instruction.		
Ans	wer (Please select your correct option)	VuAns	
•	Program counter		
с	Stack pointer		
o	Register		
с	Stack	Made	







Question No : 39 of 52 Programmable Interrupt Controller (PIC) has	
One input signals and eight output signals	
One input signal and one output signal	
Eight input signals and one output signals	
Eight input signals and eight output signals	Made





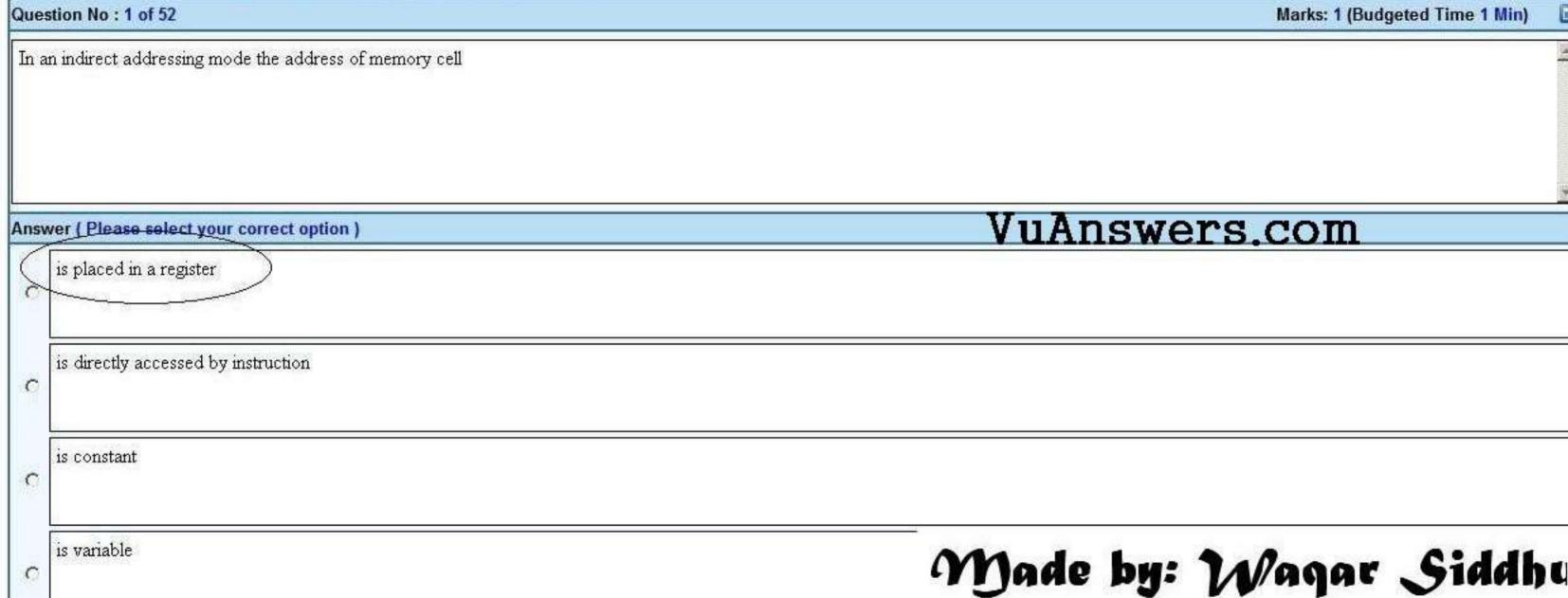


Que	uestion No : 40 of 52		
Wh	ich of the following interrupt is used in debugging with the help of trap flag?		
Ansv	wer (Please select your correct option)	VuAna	
c	INT 0		
	INT 1		
o	INT 2		
с	INT 3	Made	









In an indirect addressing mode the address of memory cell		
Ansv	ver (Please select your correct option)	
0	is placed in a register	VuAns
С	is directly accessed by instruction	
с	is constant	
0	is variable	Made I

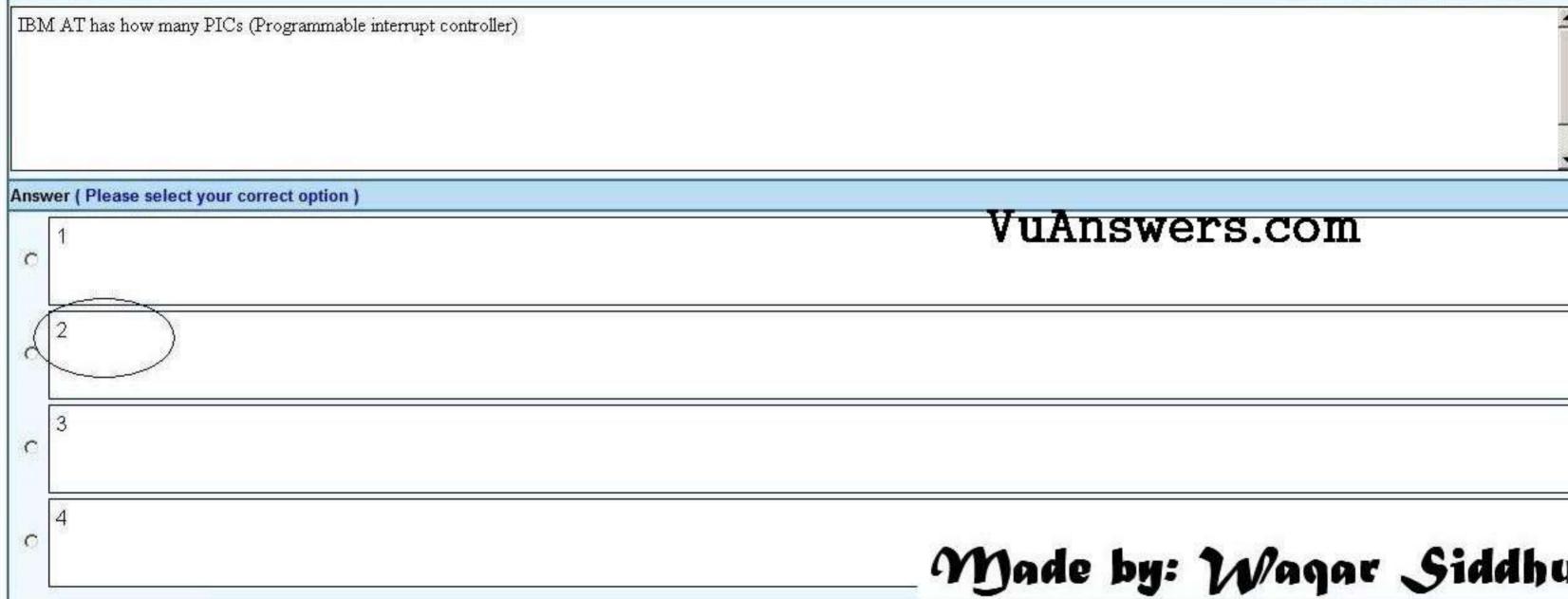




In STOSB instruction, when DF is clear, SI is	
Answer / D'	VuAns
Incremented by 2	
C Decremented by 1	
C Decremented by 2	Made b









Processor informs the memory that it wants to read data, using :	
Answer (Please select your correct option)	VuAns
C Data bus	
Control bus	
C It informs without using any bus	Made k





Memory cell width refers to	
Answer (Please select your correct option)	VuAns
The maximum value that can be stored in a cell including sign-bit	
C The maximum value that can be stored in a cell excluding sign-bit	
C Address of that cell	Made k

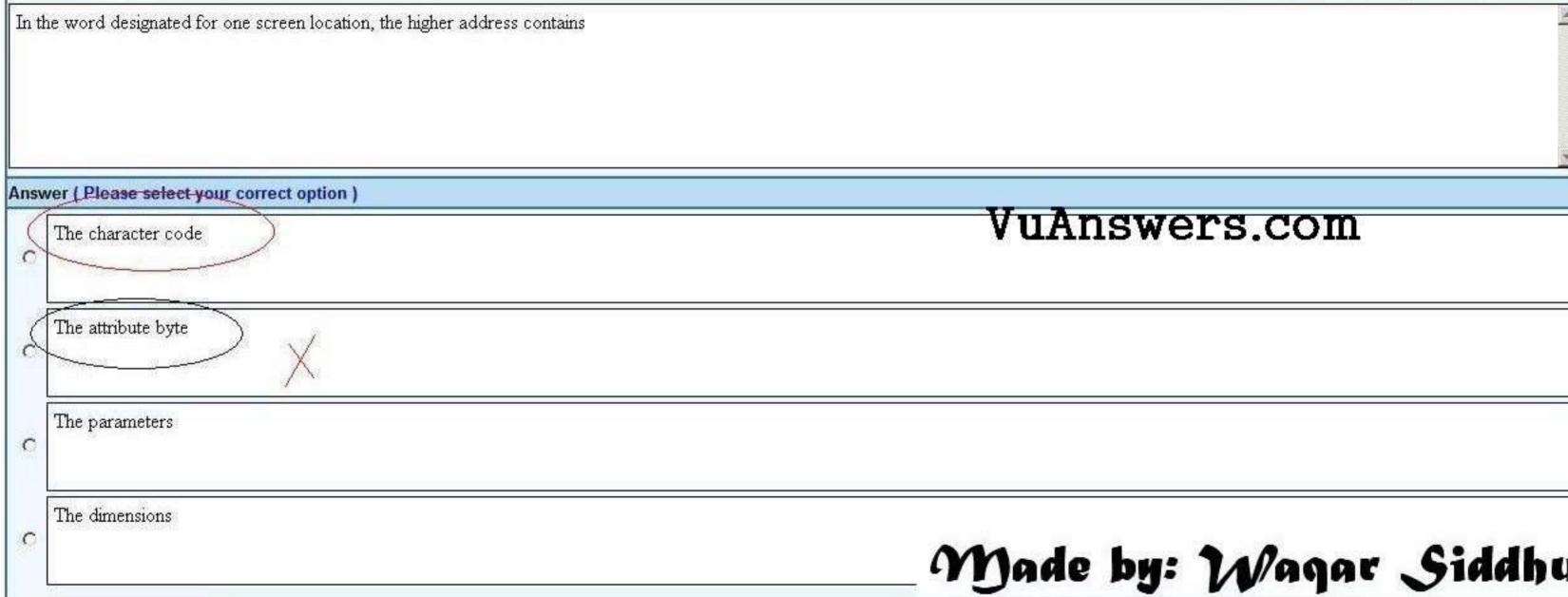




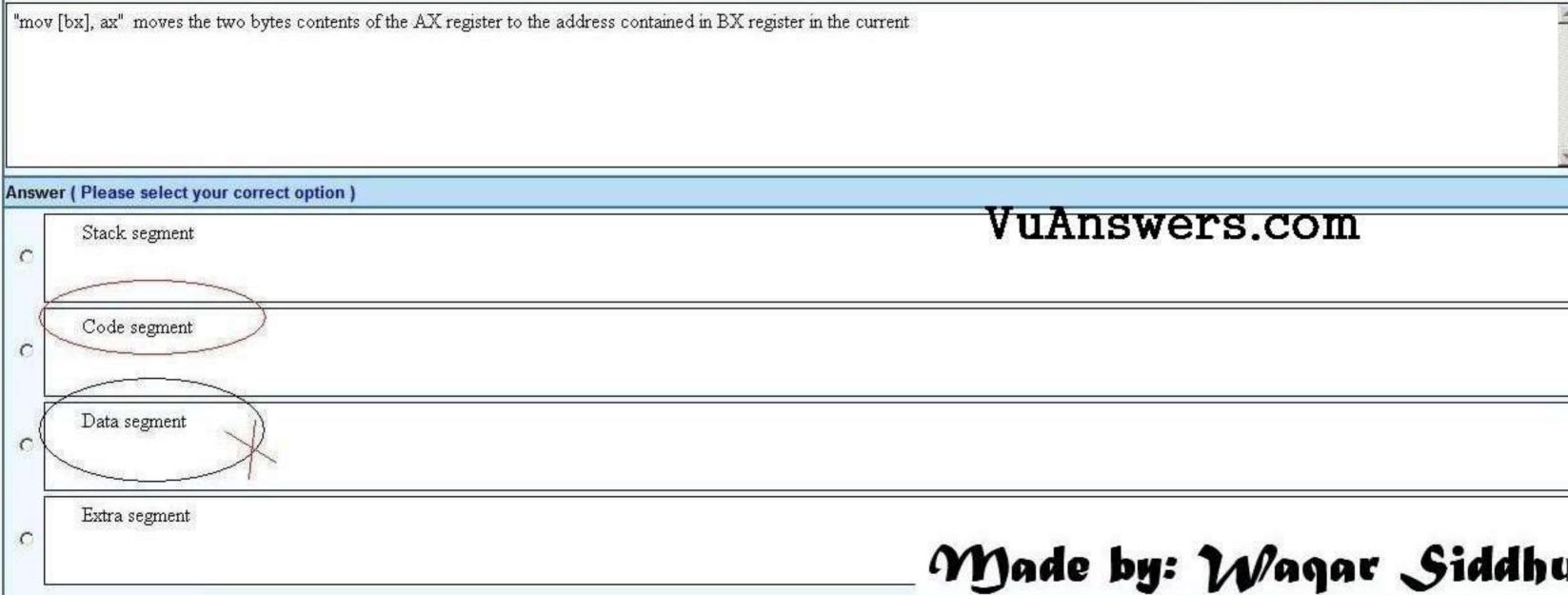
In MUL instruction if the source operand is a byte then it is multip	blied by register
Answer (Please select your correct option)	VuAns
AL	
CX	
C	Made k



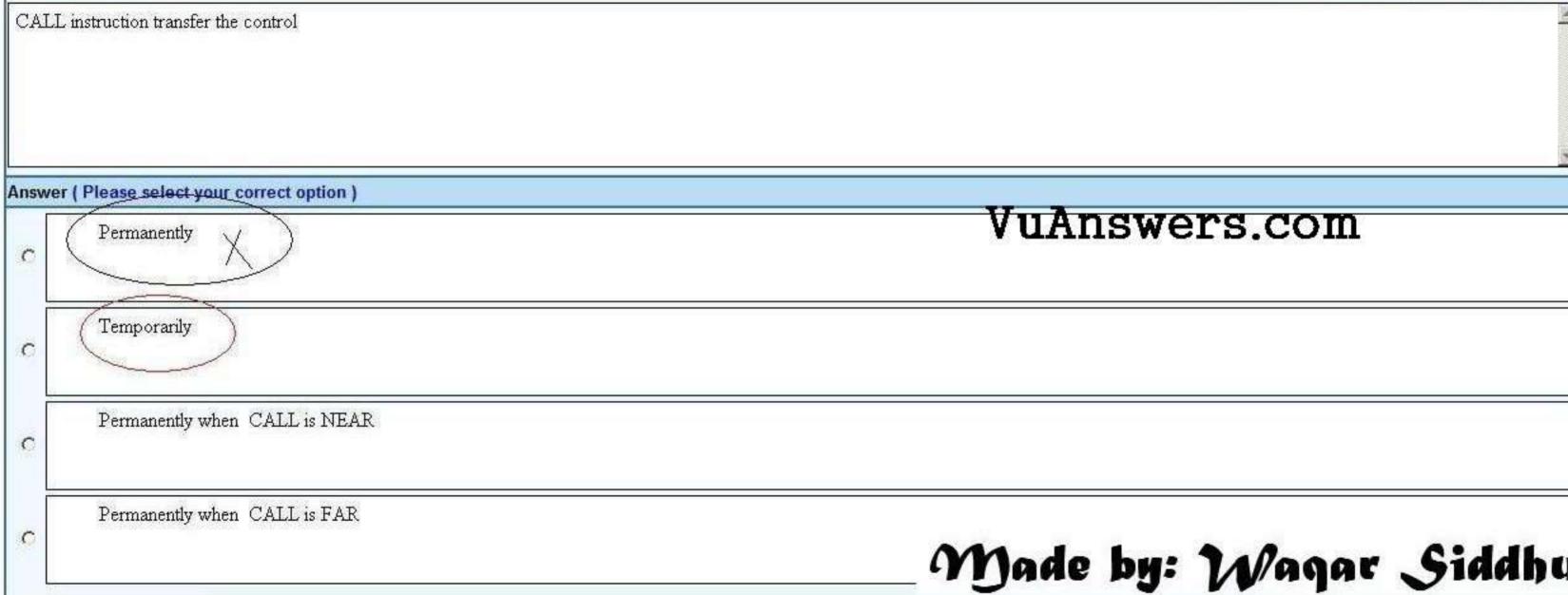














A 32 bit pi	rocessor has an accumulator of	
Answer (P	lease select your correct option)	
C 8 bits		VuAns
C 16 bit	S	
32 bit	s	
C 64 bit	S	Made k

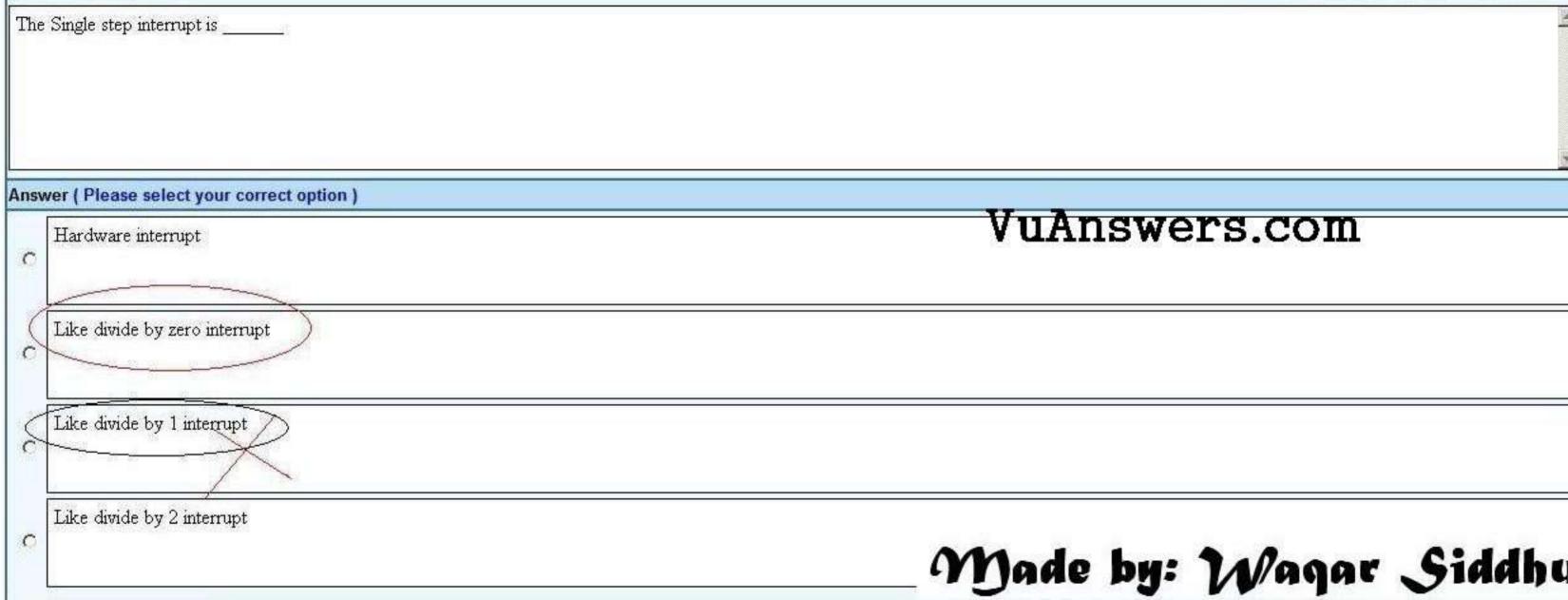




The parallel port connector is called	
Answer (Please select your correct option)	
DB-25	VuAns
c BD-25	
C DB-24	
c BD-24	Made k
2X	









are not used in mathematical operations.	
Answer (Please select your correct option)	VuAns
Carry, Interrupt and Trap flag Direction, Interrupt and Trap flag	
Direction, Overflow and Trap flag	
C Direction, Interrupt and Sign flag	Made k





In graphics mode a location in video memory corresponds to a on the screen	
Answer (Please select your correct option)	VuAns
dot	
c	
c	Made k





Answ C	ver (Please select your correct option) third fourth	VuAns
c	first	
0	second	Made b





VESA VBE 2.0 is a standard for	
Answer (Please select your correct option)	
C Ultra resolution mode	VuAns
C High resolution mode	
C Medium resolution mode	
C Low resolution mode	Made k





VBE stands for	, as proposed by VESA (an organization).	
Answer (Please selec Video BIOS Ext	t your correct option) ensions	VuAns
C Video BIOS Em	ergence	
C Video Binary Ex	tensions	
C Video Blocked H	Extensions	Made k





IDTR is a register.	
Answer (Please select your correct option)	
c 16 bit	VuAns
c 20 bit	
C 32 bit	
c 48 bit	Made k





In Computer Architecture, the Protected mode is different from	
Answer (Please select your correct option)	
C Programming mode	VuAns
Real time mode	
Visualization mode	
C Memory mode	Made k









In NA	.SM, an imported symbol is declared with the	while and exported symbol is declared with the	
Answe	r (Please select your correct option)		
c	Global directive, External directive		VuAns
C C	External directive, Global directive		
c	Iome Directive, Foreign Directive		
C	oreign Directive, Home Directive		Made k





In Computer Architecture, RISC stands for	
Answer (Please select your correct option)	
C Reversed Instruction Set Computer	VuAns
Reversed Instruction System Computer	
Reduced Instruction Set Computer	
C Reduced Instruction System Computer	Made b





In Computer Architecture, CISC stands for	
Answer (Please select your correct option) (Complex Instruction Set Computer)	VuAns
C Complex Instruction System Computer	
C Correct Instruction Set Computer	
C Correct Instruction System Computer	Made k





Threads can have function calls, parameters and which type of variables?		
Answer (Please select your correct option)	VuAns	
local		
Clegal		
C	Made k	





"INT 10 Video - Scroll Up Window" is a type of video services used	in which mode?
Answer (Please select your correct option)	
c Numeric	VuAns
Graphics	
c Text	
Audio	Made k





VuAns
Made k





BIOS sees the disks as	
Answer (Please select your correct option) Iogical storage device	VuAns
raw storage device	
in the form of sectors only	
c in the form of tracks only	Made k





In d	device attribute word, which of the following bit decides whether it is a charac	ter device or a block device ?
Answ	wer (Please select your correct option)	
c	Bit 12	VuAns
с	Bit 13	
с	Bit 14	
P	Bit 15	Made k





In 9 pin DB-9, which pin number is assigned to DSR (Data Set)	Ready) ?
Answer (Please select your correct option)	VuAns
с с 5	
6	
c 7	Made I





In 9 pin DB-9, which pin number is assigned to RTS (Request To :	Send) ?
Answer (Please select your correct option)	VuAns
c 6	
c 8	Made k





In 9 pin DB-9, which pin number is assigned to RD (Received Data) ?	
Answer (Please select your correct option)	VuAns
c 3	
c 4	Made k





In 9 pin DB-9, which pin number is assigned to TD (Transmitted	Data) ?
Answer (Please select your correct option)	VuAns
o ¹	VUAIIS
c 2	
3	
c 4	Made k





Which of the following BIOS INT provides serial port services	
Answer (Please select your correct option)	VuAns
c INT11	
c INT12 c INT13	
	Made

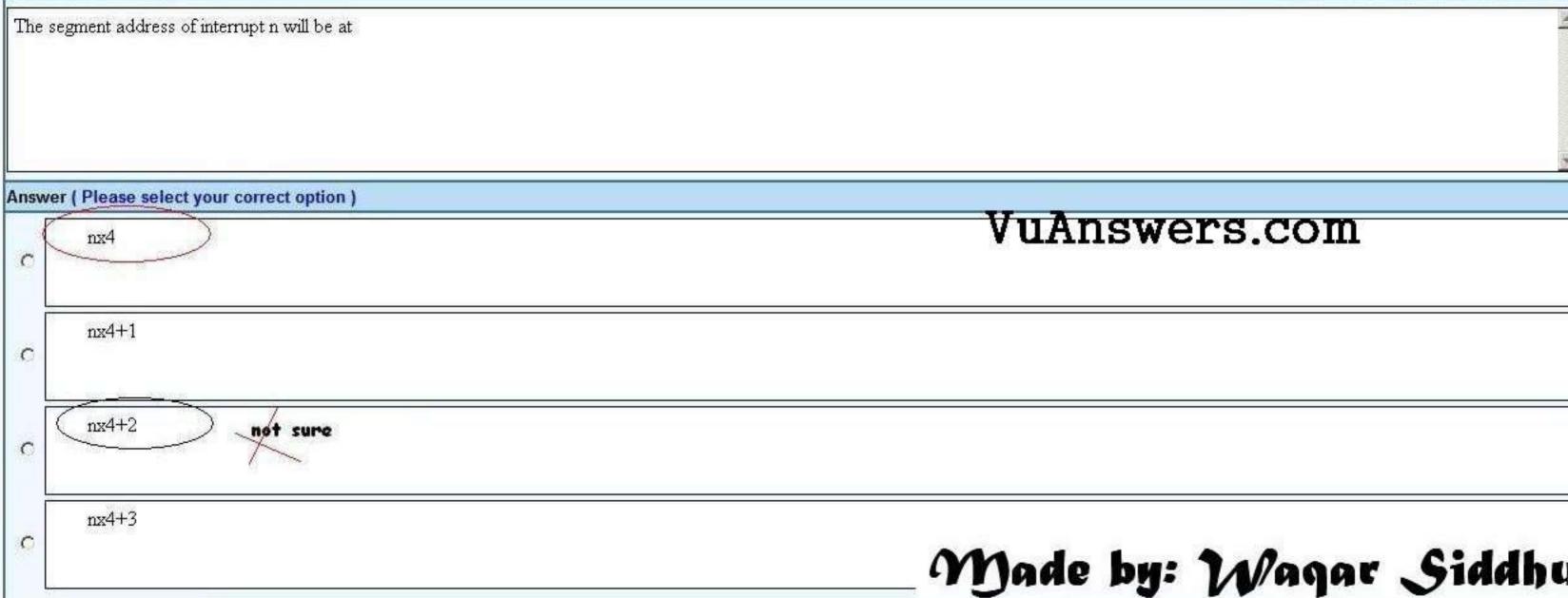




A 32 bit address register can access up to of memory, so	memory access has increased a lot.
Answer (Please select your correct option)	VuAns
(4GB)	
6GB	
o 8GB	Made k









An 8 x 16 font is stored in	bytes.
Answer (Please select your correct option)	
c ²	VuAns
c 4	
c 8	
	Made k





Which of the following interrupts is Non Maskable Interrupt (NMI).	
Answer (Please select your correct option)	VuAns
C INT 2 C INT 3 C INT 3	
c INT 0	
c INT 1	Made b





Which of the following is a special type of interrupt that returns to the same instruction instead of the next instruction?		uction instead of the next instruction?
	Please select your correct option) Divide overflow interrupt	VuAns
c	Debug interrupt	
c	Arithmetic overflow interrupt	
c	Change of sign interrupt	Made k





In Programmable Interrupt Controller, which one of the follow	ving ports is referred as interrupt mask register?
Answer (Please select your correct option)	
c ¹⁹	VuAns
c 20	
21)	
c 22	Made k





Which one of the following interrupts is used for arithmetic overflow?	
Answer (Please select your correct option)	VuAns
c INT 1	
c INT 2	
c INT 3	Made k





Question No : 1 of 52		
The instruction mov ax, 0		
Answer (Please select your correct option)	VuAns	
C will set the ZF		
c will not set the ZF		
c is a mathematical instruction		
C is a logical instruction	Made	

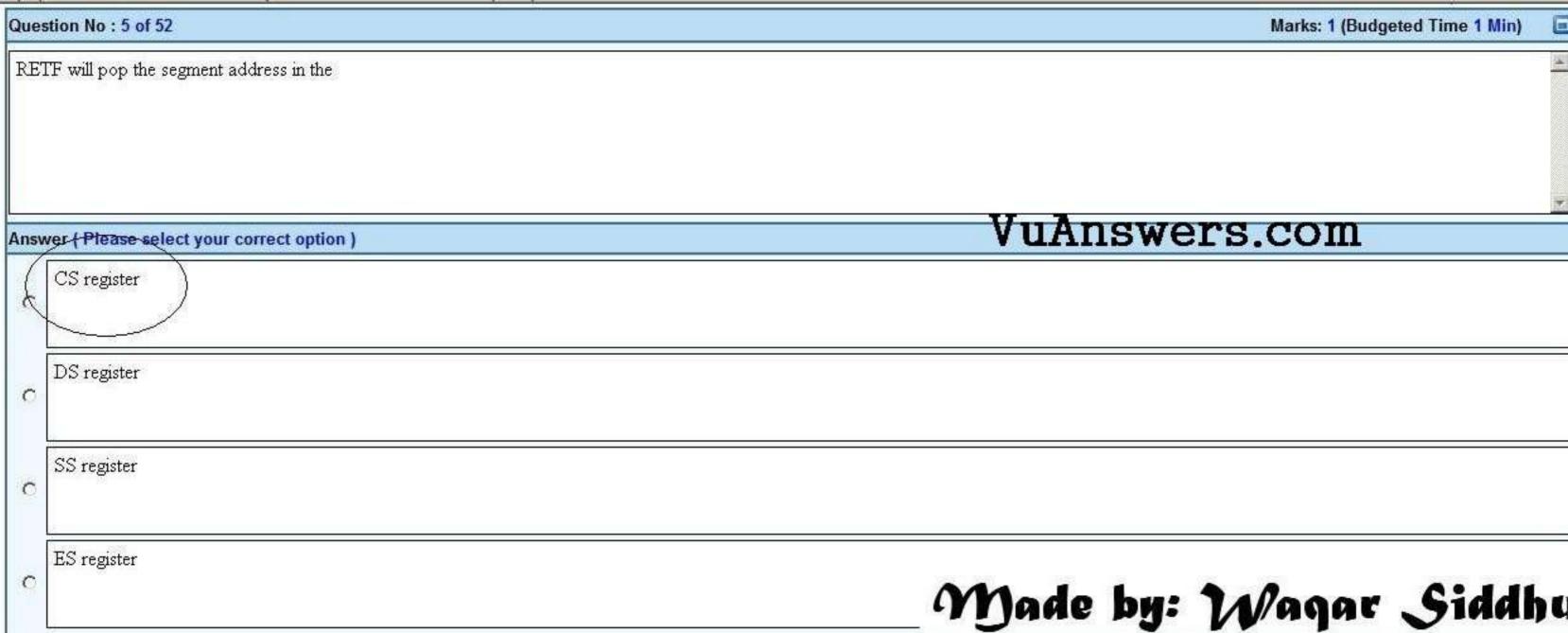


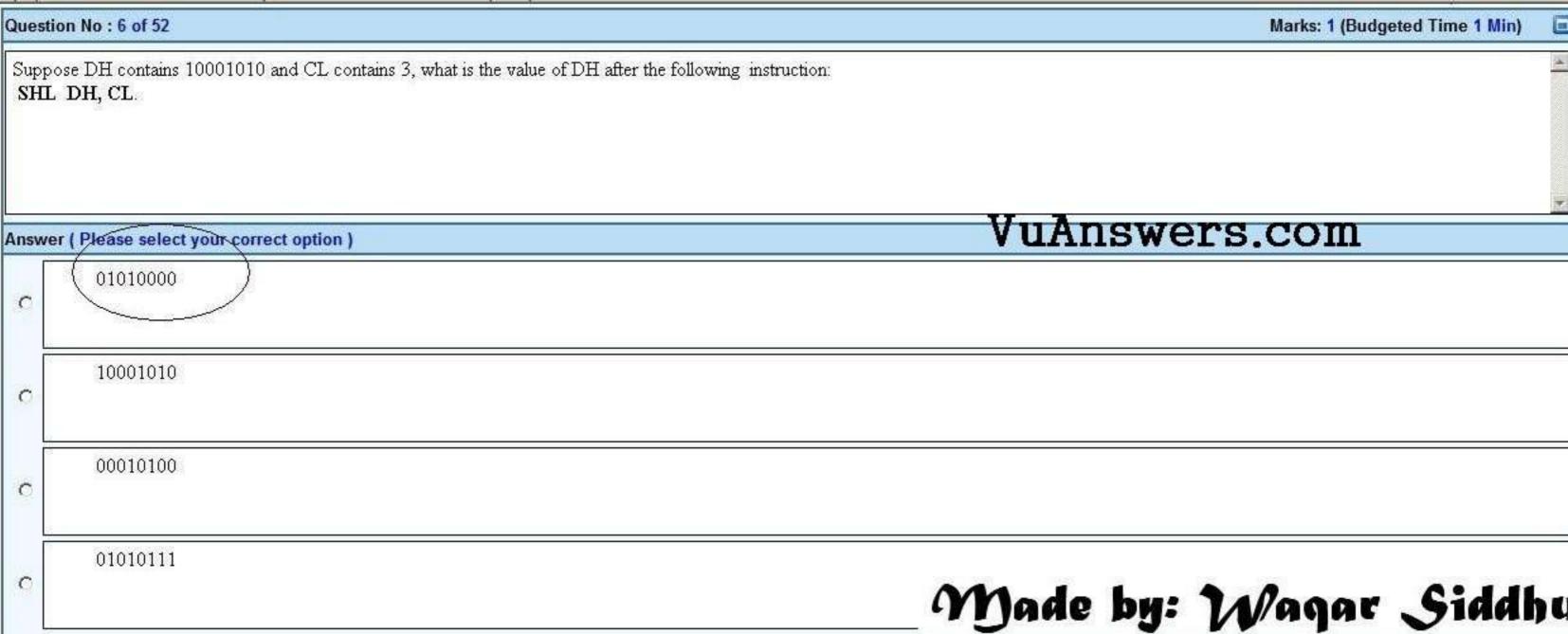


estion No : 3 of 52		
The data on which we want to perform some operations is called		
Answer (Please select your correct option)	VuAn	
C Opcode		
c Operand		
C Operator		
C	Made	









luestion No : 7 of 52		
In STOSB instruction, SI is decremented or incremented by		
Answer (Please select your correct option)	VuAns	
с ³		
c 2		
c 4	Made	



uestion No : 8 of 52		
The OUT instruction uses as source register.		
Answer (<u>Please</u> select your correct option)	VuAn	
C AL or AX		
C BL or BX		
C CL or CX		
C DL or DX	Made	





E Question No : 10 of 52 Marks: 1 (Budgeted Time 1 Min) Which bit of the Attribute Byte (counting from right to left, starting from 0) represents the RED component of background color ? VuAnswers.com Answer (Please select your correct option) 3 C 4 C 5 C 6



Question No : 11 of 52		
Sign flag is set to for positive numbers		
Answer (Please select your correct option)	VuAns	
с <mark>+1</mark>		
0 or 1		
c ¹	Made	



Que	Question No : 12 of 52		
Co	mputer Architecture and Assembly Language Programming is a course that is designed to study		
Ansv	wer (Please select your correct option)	VuAns	
с	Hardware only		
c	Software only		
c	Computer system more closely		
o	Computer Graphics	Made	



Que	Question No : 13 of 52				
[B]	M PC has separate memory address space and peripheral address space. Peripheral address space is selected when	instructions is giv			
Ans	wer (Please select your correct option)	VuAn			
0	IN				
c	ADD				
ç	MOV				
0	DEC	5 NW			



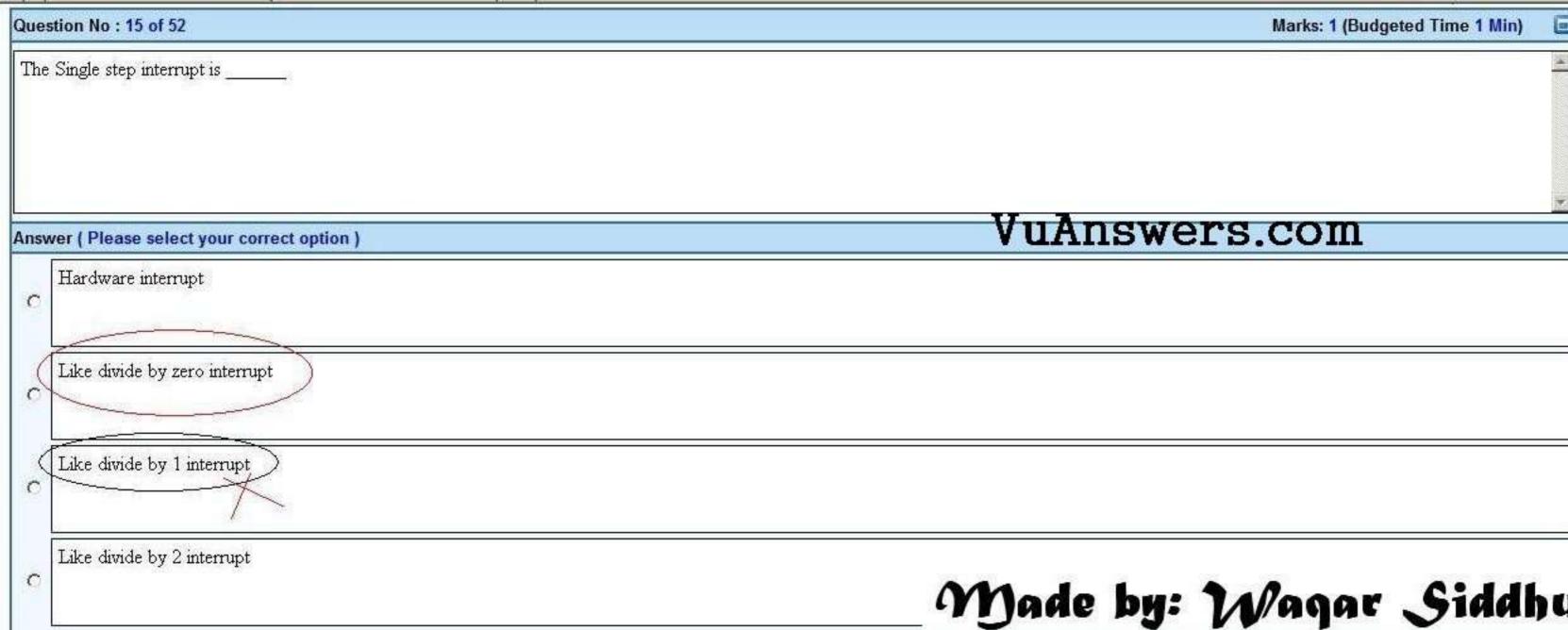
ven to the processor.





uestion No : 14 of 52		
The parallel port connector is called		
Answer (Please select your correct option)	VuAns	
DB-25		
c BD-25		
C DB-24		
c BD-24	Made	







Que	Question No : 17 of 52				
In g	graphics mode a location in video memory corresponds to a	on the screen.			
Ansv	wer (Please select your correct option)		VuAns		
С	line				
6	dot				
С	circle				
c	rectangle		Made		



Question No : 18 of 52			
In I	OOS input buffer, the number of characters actually read on return is stored in	byte.	
Ansv	wer (Please select your correct option)		VuAns
с	third		
С	fourth		
с	first		
¢	second		Made



uestion No : 19 of 52		
The value of AH register in the write Graphics pixel service is		
Answer (Please select your correct option)	VuAns	
OCh		
C 0Bh		
c 1 ^{Ch}		
c 2Ch	Made	



6 Question No : 20 of 52 Marks: 1 (Budgeted Time 1 Min) INT 21 service 01H is used to read character from standard input with echo. It returns the result in register. VuAnswers.com Answer (Please select your correct option) а b1 C cl C bh Made by: Waqar Siddhu



Question No : 21 of 52		
VESA (an organization) organizes 16 color bits for every pixel in	format.	
Answer (Please select your correct option)	∀u≱	Ans
5:5:5		
5:6:5		
C 5:5:6		
C 5:6:6	Mad	le



Question No : 22 of 52	
VESA stands for	
Answer (Please select your correct option)	VuAn
C Variable Electronics Standards Association	
Video Electronics Standards Association	
Video Energy Standards Association	
C Vega Energy Standards Association	Made



Que	estion No : 23 of 52	
DI	TR is a register.	
Ansv	wer (Please select your correct option)	VuAns
с	16 bit	
0	20 bit	
С	32 bit	
°(48 bit	Madel



Question No : 24 of 52		
In Motorola 68K processors, registers can hol	d addresses in indirect memory accesses.	
Answer (-Please select your correct option)	VuAn	
C (A0-A7		
с В0-В7		
с С0-С7		
C D0-D7	Made	



Que	Question No : 25 of 52		
Motorola 68K processors have		32 bit general purpose registers.	
Ansv	wer (Please select your correct o	ption)	VuAn
с	32		
c	64		
с	8		
c(16		Made



Que	stion No : 26 of 52	
In C	Computer Architecture, RISC stands for	
Ansv	ver (Please select your correct option)	VuAns
с	Reversed Instruction Set Computer	
0	Reversed Instruction System Computer	
°	Reduced Instruction Set Computer	
c	Reduced Instruction System Computer	Made

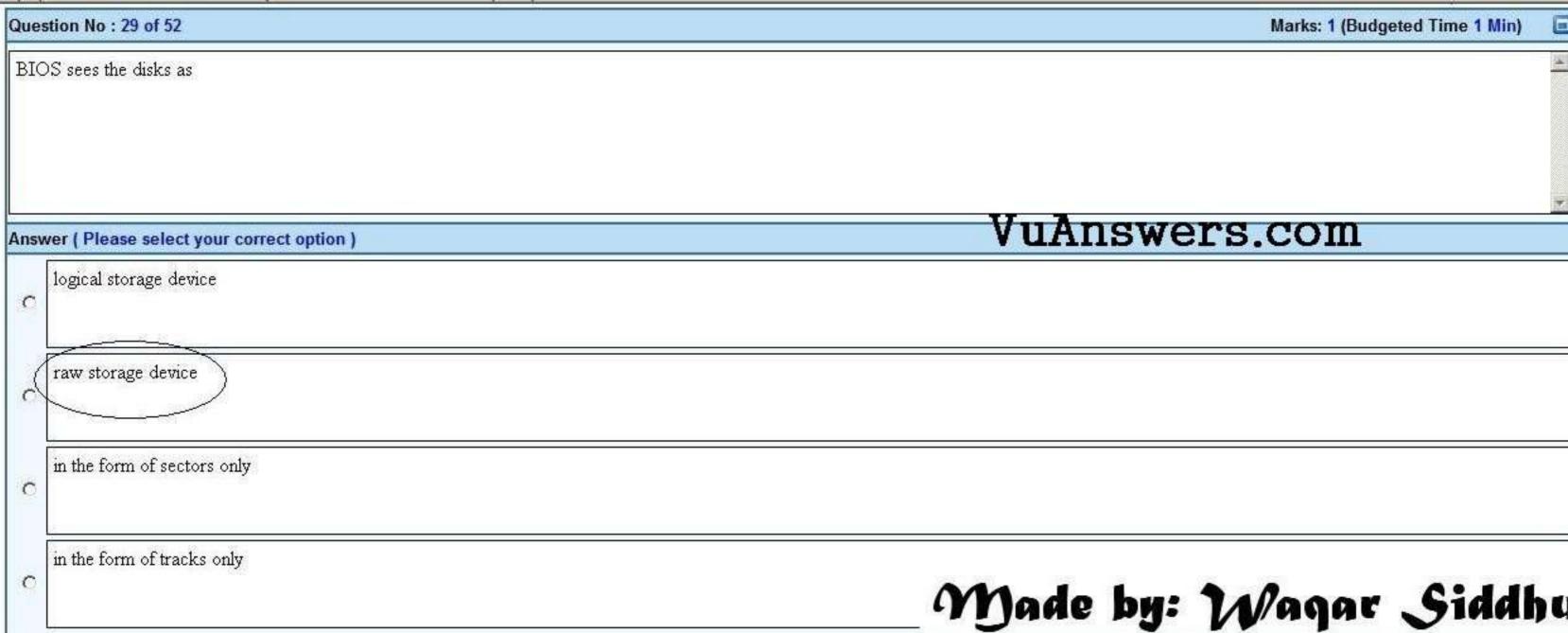


Question No : 27 of 52	
In Computer Architecture, CISC stands for	
Answer (Please select your correct option)	VuAns
Complex Instruction Set Computer Complex Instruction System Computer	
Correct Instruction Set Computer	
C Correct Instruction System Computer	Made



Question No : 28 of 52	
Video services are exported via	
Answer (Please select your correct option)	VuAns
C INT 7	
INT 10	
c INT 9	
c INT 8	Made





Question No : 30 of 52	
Which of the following gives more logical view of the storage medium ?	
Answer (Please select your correct option)	VuAn
DOS	
CRAM	
c ROM	
c BIOS	Made



Que	stion No : 31 of 52	
In S	pin DB-9, which pin number is assigned to DSR (Data Set Ready) ?	
Anst	ver (Please select your correct option)	VuAn
с	4	
o	5	
6	6	
c	7	Made



Que	tion No : 32 of 52	
In 9	pin DB-9, which pin number is assigned to RTS (Request To Send) ?	
Ansv	er (Please select your correct option)	VuAna
с	5	
0	6	
4	7)	
с	8	Made



Que	stion No : 33 of 52	
In S	pin DB-9, which pin number is assigned to CTS (Clear To Send) ?	
Ansv	wer (Please select your correct option)	VuAn
с	6	
c	7	
R	8	
c	9	Made





Question No : 35 of 52	
In 9 pin DB-9, which pin number is assigned to TD (Transn	utted Data) ?
Answer (Please select your correct option)	VuAn
c 1	
c 2	
3	
c 4	Made



Question No : 36 of 52	
In 9 pin DB-9, RI (Ring Indicator) is assigned on pin number	
Answer (Please select your correct option)	VuAns
c 6	
c 7	
c 8	
9	Made



Question No : 37 of 52	
The table index (TI) is set to to access the GDT (Global Descriptor Table).	
Answer (Please select your correct option)	VuAns
c 1	
c -1	
c -2	Made



Ques	stion No : 38 of 52	
Whi	ich of the following interrupt is used in debugging with the help of trap flag?	
Answ	ver (Please select your correct option)	VuAns
С	INT 0	
S		
c	INT 2	
0	INT 3	Made



E Question No : 39 of 52 Marks: 1 (Budgeted Time 1 Min) In Programmable Interrupt Controller, which one of the following ports is referred as interrupt mask register? VuAnswers.com Answer (Please select your correct option) 19 C 20 C 21 22 0



Question No : 40 of 52	
Which one of the following interrupts is used for arithmetic overflow?	
Answer (Please select your correct option)	VuAns
c INT 1	
c INT 2	
c INT 3	Made



uesti	tion No : 1 of 52	
The p	process of sending signals back and forth is called	
nswe	er (Please select your correct option)	VuAn
c	Activity	
c	Hand-shaking	
0	Interruption	
0	Time clicking	Made







Question no . 2 of 52	Question	No : 2	of 52
-----------------------	----------	--------	-------

Which of the following IRQs is connected to serial port COM 1?	
Answer (<u>Please</u> select your correct option)	VuAn
c IRQ 5	
c IRQ 6	
c IRQ 7	Made





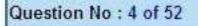


estion No : 3 of 52	
Which of the following interrupt is of highest priority interrupt?	
swer (Please select your correct option)	VuAn
Key board interrupt	
Timer interrupt	
- INT 2	
O INT 3	Made









C

C

Regarding assembler, which statement is true:

Answer (Please select your correct option)

Assembler converts mnemonics to the corresponding OPCODE

Assembler converts OPCODE to the corresponding mnemonics

Assembler executes the assembly code all at once

Assembler executes the assembly code step by step

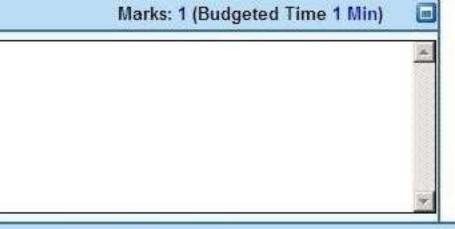








Question No : 5 of 52	
CMPS instruction subtracts the source location to the destination location	. Source location always lies in
nswer (Please select your correct option)	VuAn
ESD	
DS:SI DS:DI	
c	Made







Quest	ion N	lo:6	of 52

For the execution of the instruction "DIV BL", the implied dividend will be sto

Answer (Please select your correct option)	VuAn
AX	
CBX	
c	
DX	Made







Ques	stion No : 7 of 52	
Whe	en two devices in the system want to use the same IRQ line then what will happen	?
Answ	ver (Please select your correct option)	VuAn
0	An IRQ Collision	
С	An IRQ Conflict	
c	An IRQ Crash	
0	An IRQ Blockage	Made







Question No : 8 of 52		
The memory address always move from		
Answer (Please select your correct option)	VuAn	
processor to memory		
memory to processor		
memory to peripheral		
c peripheral to processor	Made	







Question No : 9 of 52		
After the execution of following two instructions, what will happen ? PUSH AX POP DX		
Answer (Please select your correct option)	VuAn	
C The value of DX will be at the top of stack		
C The value of AX will be at the top of stack		
C The value of DX will be copied into AX		
The value of AX will be copied into DX	Made	







Question No :	10	of 52
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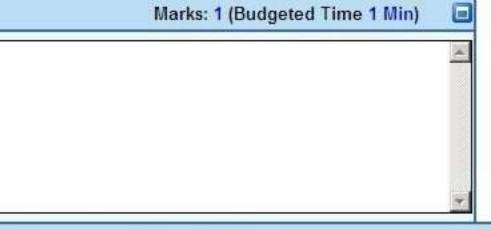
The second byte of the word designated for one screen location holds		
Answ	ver (Please select your correct option) The dimensions of the screen	VuAn
0	Character position on the screen	
0	Character color on the screen	
c	ASCII code of the character	Made







Question No : 11 of 52		
In Sh	ift instructions the destination can be	
Answe	er (Please select your correct option)	VuAn
o	An 8-bit register only	
c	A register only	
c	A memory location only	
0	A register or memory location	Made



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Question No : 12 of 52		
INT 10 is used for services.		
Answer (Please select your correct option)	VuAn	
BIOS video		
C DOS video		
c RAM		
C Disk	Made	







Question No : 13 of 52	
In graphics mode a location in video memory corresponds to a on the sc	reen.
Answer (Please select your correct option)	VuAn
Cline	
c dot	
c	
rectangle	Made







Question No : 14 of 52	
A thread can not be created	
Answer (Please select your correct option)	VuAn
in static way	
c in dynamic way	
c in static and dynamic way at the same time	
C manually	Made







Question No : 15 of 52	
The space where the current state of a task is stored is called	
Answer (Please select your correct option)	VuAn
c control block	
C Process control block	
c stack	
C hard disk	Made







Question No : 16 of 52	
VESA (an organization) organizes 16 color bits for every pixel in format.	
Answer (Please select your correct option)	VuAn
5:5:5	
5:6:5	
5:5:6	
5:6:6	Made







Question No : 17 of 52		
VESA stands for		
Answer (Please select your correct option)	VuAn	
Variable Electronics Standards Association Video Electronics Standards Association		
C Video Energy Standards Association		
C Vega Energy Standards Association	Made	







uestion No : 18 of 52		
The	Physical address of Interrupt Descriptor Table (IVT) is stored in	
nsw	ver (Please select your correct option)	VuAn
0	GDTR	
c	LGTD	
0	IDTR	
0	IVT	Made







Question No : 19 of 52		
IDTR is a	register.	
Answer (Please se	elect your correct option)	VuAn
C 16 bit		
C 20 bit		
c 32 bit		
48 bit)	Made







Question No : 20 of 52			
In Motorola 68K processors, _	registers can hold addresses in indirect memory accesses.		
Answer (Please select your corr	ect option)	VuAn	
A0-A7			
с В0-В7			
с C0-C7			
C D0-D7		Made	







Que	Question No : 21 of 52			
M	Motorola 68K processors have 32 bit general purpose registers.			
Ans	wer (Please select your correct o	ption)	VuAn	
Q	32			
С	64			
с	8			
4	16		Made	







n.			- AL		2 4	6 5 3
a	les	lioi	1 140); Z	20	f 52

In Computer Architecture, RISC stands for ____

Answer (Please select your correct option)



Reversed Instruction System Computer Reduced Instruction Set Computer	Q		Made
Reversed Instruction System Computer		Reduced Instruction System Computer	
C Reversed Instruction System Computer	0	Reduced Instruction Set Computer	
	C	· 이상에 제품 · · · · · · · · · · · · · · · · · ·	
		Description description Communities	
	c	Reversed Instruction Set Computer	





Q	11 22	. 6 63
Question	NO:23	2010

In multi-tasking environment, which is called scheduler for saving and restoring th	e registers?
Answer (Please select your correct option)	VuAn
c INT 10	
C [10]	
C INT 08	
C INT 07	Made







uestion No : 24 of 52		
Threads can have function calls, parameters and which type of variables?		
nswer (Please select your correct option)	VuAn	
global		
C legal		
C illegal	Made	







Question No : 25 of 52	
In multi-tasking programming, all threads are	
Answer (Please select your correct option) Independent to each other	Vu
Dependent on each other	
C Not executed	
Not scheduled	







Question No : 26 of 52		
"Far" jump makes intra	control possible	
Answer (Please select your co	prrect option)	VuAr
C		
Segment		
C Memory C Code		
C C		Made







Que	luestion No : 27 of 52		
All	possible types of Jumps in assembly language are:		
Ansv	ver (Please select your correct option)	VuAn	
с	short, far		
с	short, near		
c	short, near, far		
с	short, near, far, long	Made	







Question No : 28 of 52	
BIOS sees the disks as	
Answer (Please select your correct option)	VuAn
C logical storage device (raw storage device)	
in the form of sectors only	
in the form of tracks only	Made







Que	stion No : 29 of 52	
Wł	nich of the following gives more logical view of the storage medium ?	
Ans	wer (Please select your correct option)	VuAn
K	DOS	
0	RAM	
с	ROM	
c	BIOS	Made







Questio	n No :	30 of	52
	a a constant a s		

In 9 pin DB-9, which pin number is assigned to TD (Transmitted Data) ?	
Answer (Please select your correct option)	VuAn
o ¹	
c 2	
3	
c 4	Made







Question	No : 31 of 52	
the second se		

In 9 pin DB-9, which pin number is assigned to DTR (Data Terminal Ready) ?

nswer (Please select your correct option)	VuAn
c 2	
c 3	
4	Made







uestion No : 32 of 52	
n 9 pin DB-9, RI (Ring Indicator) is assigned on pin number	
nswer (Please select your correct option)	VuAn
c ⁷	
c 8	
9	
6	Made







Question No : 33 of 52	
8088 is a	
Answer (Please solect your correct option)	VuAn
16 bit processor	
c 32 bit processor	
c 64 bit processor	
c 128 bit processor	Made

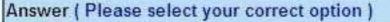






0	uest	ion	N	0:	34	of	52
-	aoos			w	~	~.	me.

A 32 bit address register can access up to of memory, so memory access has increased a lot.



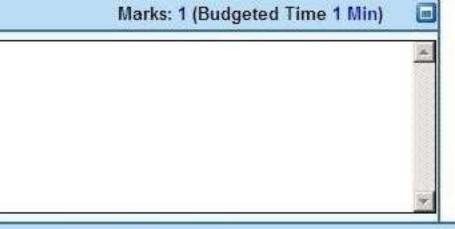
0	8GB	Made
с	6GB	
6	4GB	
с	2GB	





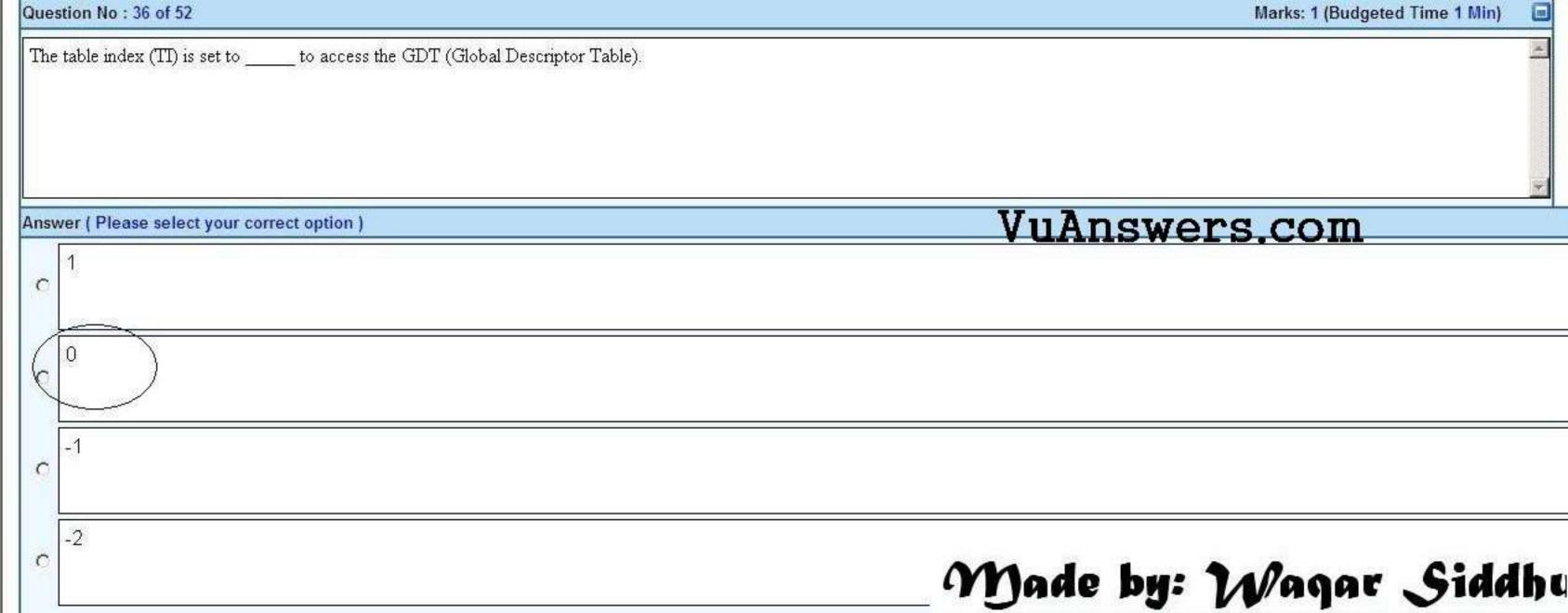


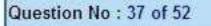
uestion No : 35 of 52	
AX register in extended 32 bit register is renamed as	
nswer (Please select your correct option)	VuAn
c AXE	
EAX	
AAX	
c XAX	Made



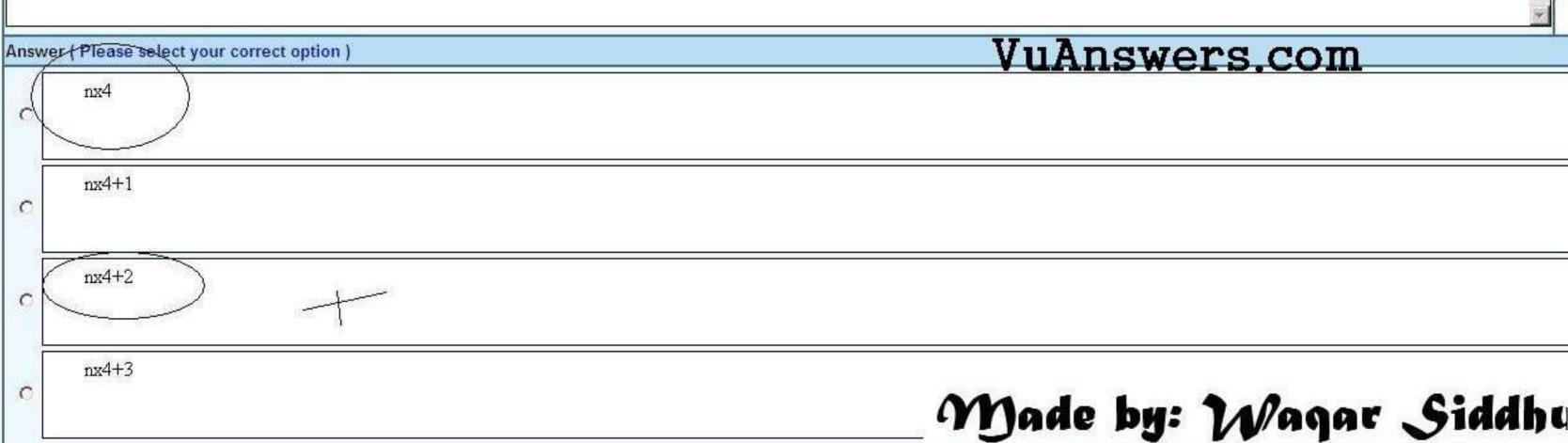


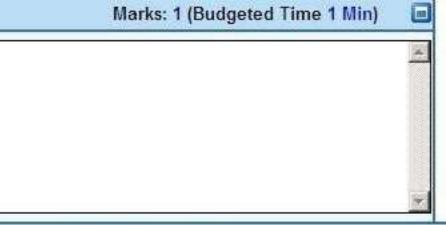


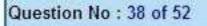




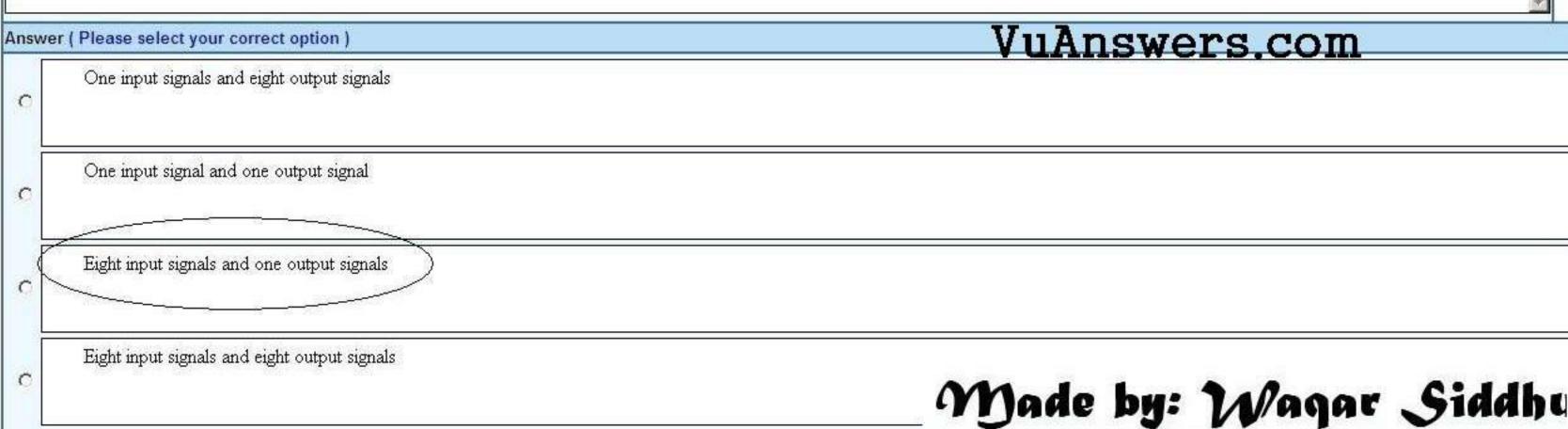
The segment address of interrupt n will be at





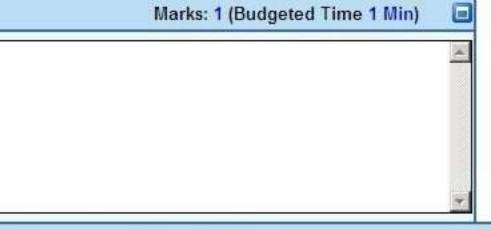


Programmable Interrupt Controller (PIC) has



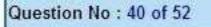


Que	estion No : 39 of 52	
	number of pin(s) of a processor is/are used by the external hardware to generate an interrupt.	
Ans	wer (Please select your correct option)	VuAn
С	1	
c	2	
0	3	
c	4	Made



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In Programmable Interrupt Controller, Port _____ is referred as a Control port.





stion No : 1 of 52	
TOSW instruction, when DF is clear, S	SI is
ver (Please select your correct option	1)
Incremented by 1	
Incremented by 2	
	correct
Decremented by 1	
Decremented by 2	







Question	No :	2 of	52

Wh	ich of the following IRQ	es is connected to serial port COM 1?	
Ansv	ver (Please select you	r correct option)	VuAn
С	IRQ 4	correct	
0	IRQ 5		
0	IRQ 6		
0	IRQ 7		Made







lues	uestion No : 3 of 52		
Whi	ich of the following IRQs is used by the parallel port?		
nsv	ver (Please select your correct option)	VuAn	
С	IRQ 4		
0	IRQ 5		
0	IRQ 6		
0	IRQ 7 Correct	Made	







Question	No : 4	of 52

The programmable interval timer (PIT) has input frequency of			
Answ	ver (Please select your corr	ect option)	VuAn
С	1.193MHZ	correct	
0	2.193MHZ		
0	3.193MHZ		
0	4.193MHZ		Made







ue	stion No : 5 of 52	
Wh	nich of the following interrupt is of highest priority interrupt?	
nsv	wer (Please select your correct option)	VuA
C	Key board interrupt	
0	Timer interrupt	
0	INT 2	
0	INT 3	Mod







Question	No : 6 of 52	

By	v executing which of the following instruction, the value in AX register becomes the half of previous	sly stored value ?
Ansv	swer (Please select your correct option)	VuAn
С	ShIAX, 1	
C	Shr AX, 1 COFFect	
c	NOT AX	
0	AND AX, 0xFFFF	Made







Que atien	M.	. 7	- 5	53
Question	NO	: 1	01	37

In the	rotate right operation every bit moves one position to the right and the bit dropped	I from the right is inserted at the left and:
Answe	r (Please select your correct option)	VuAn
с	The dropped bit is also copied in CF	
0	The dropped bit is also copied in AF	
c	The dropped bit is also copied in ZF	
c	The dropped bit is not copied in any flag	Made







Juestic	uestion No : 8 of 52		
In string instructions, CX is always			
Inswer	r (Please select your correct option)	VuAr	
0	Decremented by 1		
0	Decremented by 2		
0	Incremented by 1		
0	Incremented by 2	Made	







Question No : 9 of 52	
The word size of the processor is defined by the size of	
Answer (Please select your correct option) Its accumulator register	VuAn
C Address bus attached	
C Memory attached	
C Control Bus	Made





Question No : 10 of 52	: 10 of 52	No	uestion	C
------------------------	------------	----	---------	---

cannot predict which element the	needs, at a particular instant of time.
----------------------------------	-----------------------------------------

Answer (Please select your correct option)

C		
	Control bus, Memory	
C M	Memory, Processor	
1 mm 7	Processor, Control bus Processor, Memory	
0	Control bus, Memory	







Que	Question No : 11 of 52			
The time interval between two timer ticks is				
Ansv	wer (Please select your correct option)	VuAn		
С	40ms			
0	45ms			
0	50ms			
0	55ms COFFect	Made		







Question No : 12 of 52			
In DOS input buffer, the number of characters actually read on return is stored in		byte.	
Ansv	ver (Please select your correct option) third		VuAr
0	fourth		
0	first		
c	second COFFECt		Made







Question No : 13 of 52		
At	hread can not be created	
Ansv	wer (Please select your correct option)	VuAn
C	in static way	
Wite:	correct	
c	in dynamic way	
o	in static and dynamic way at the same time	
o	manually	Made







Question No : 14 of 52		
The	e space where the current state of a task is stored is called	
Ansv	wer (Please select your correct option)	VuAn
С	control block	
C	Process control block	
0	stack	
0	hard disk	Made







Que	estion No : 15 of 52	
VE	ESA stands for	
Ansv	wer (Please select your correct option)	VuAn
С	Variable Electronics Standards Association	
~	Video Electronics Standards Association	
0		orrect
c	Video Energy Standards Association	
o	Vega Energy Standards Association	Made







Que	Question No : 16 of 52			
VB	BE stands for, as proposed by VESA (an organization).			
Ans	swer (Please select your correct option)	VuAr		
o	Video BIOS Extensions			
c	Video BIOS Emergence			
0	Video Binary Extensions			
o	Video Blocked Extensions	Made		







ue	stion No : 17 of 52	
n(Computer Architecture, the Protected mode is different from	
nsv	wer (Please select your correct option)	VuAr
C	Programming mode	
0	Real time mode	
0	Visualization mode	
0	Memory mode	Made







Que	stion No : 18 of 52	
In A	Assembly language programming, how many prevalent Calling Conventions do	exist ?
Ansv	ver (Please select your correct option)	VuAn
0	2 <u>Correct</u>	
c	3	
o	4	Made







Question No : 19 of 52			
In 1	Aotorola 68K processors,	registers can hold addresses in indirect memory accesses.	
Ansv	wer (Please select your correct	option)	VuAn
С	A0-A7	correct	
c	В0-В7		
0	C0-C7		
C	D0-D7		Made







Que	Question No : 20 of 52			
Motorola 68K processors have 32 bit general purpose registers.				
Ansv	ver (Please select your correct o	ption)	VuAn	
С	32			
c	64			
c	8			
c	16	correct	Made	







Question	No	:21	of	52
Contraction of the second second				

Which byte of the DOS input buffer holds the number of characters actually read on return?

Answer (Please select your correct option)

C	First Byte	
C	Second Byte	
0	Third Byte	
0	Last Byte	Made







)ue:	estion No : 22 of 52	
Thr	reads can have function calls, parameters and which type of variables?	
Insv	wer (Please select your correct option)	VuAr
С	global	
0	local <u>COFFECt</u>	
0	legal	
0	illegal	Made







Question	No : 23 of	52

In multi-tasking programming, all threads are

Answer (Please select your correct option)

0		Made
*	Not scheduled	
0	Not executed	
0	Dependent on each other	
C	COITE	
	Independent to each other	







Que	stion No : 24 of 52		
Vid	eo services are exported via		
Ansv	ver (Please select your correct option)		VuAn
С	INT 7		
C	INT 10	correct	
c	INT 9		
0	INT 8		Made







Desanting	M.	25	- 5	53
Question	I NO :	23	01	37

TTH	TADTZOO	· · · · · · · · · · · · · · · · · · ·
The maximum memory	LAPXXX ca	10 20000 1C
The maximum memory	, 111 2200 00	ar access 15

Answer (Please select your correct option)

C 1 KB	
64 KB	
C 1 MB	ct
C 64 MB	Made







Jue	stion No : 26 of 52		
In <	direct addressing the memory address is	and is given in the instruction.	
Ansv	wer (Please select your correct option)		VuAn
С	Fixed	<u>correct</u>	
0	Empty		
0	A variable		
0	A register		Made







Que	stion No : 27 of 52	
Inst	truction "mov ax, bl" has a problem.	
Ansv	wer (Please select your correct option)	VuAr
С	size mismatch	
c	value mismatch	
c	operand mismatch	
c	memory mismatch	Made







Que	estion No : 28 of 52	
The	ie jump "JC" is taken when	
Ansv	swer (Please select your correct option)	VuAn
C	CF=1	
12	correct	
0	CF=0	
o	ZF=0	
0	ZF=1	
		Made







uestion	No : 29 of 52	
Which of	f the following gives more logical view of the storage medium ?	
nswer	Please select your correct option)	VuAn
DO		
CRA	M	
c RO	M	
c BIC	DS	Made

Q







Question	No:	30	of 52
----------	-----	----	-------

The serial port connection is a ----- connector

Answer (Please select your correct option)

	-		1	A	[
--	---	--	---	----------	---

0	9 pin DB-5	C	Made
C	3 pin DB-9		
С	8 pin DB-9		
0	9 pin DB-9		







Que	estion No : 31 of 52	
In S	9 pin DB-9, which pin number is assigned to RTS (Request To Send) ?	
Ans	wer (Please select your correct option)	VuAr
c	5	
0	6	
o	7 <u>correct</u>	
0	8	Made







Question No : 32 of 52	
In 9 pin DB-9, which pin r	number is assigned to CTS (Clear To Send) ?

Answer (Please select your correct option)

0	9	Made
0	8 <u>correct</u>	
0	7	
c	6	







ue	ion No : 33 of 52		
n 9	9 pin DB-9, RI (Ring Indicator) is assigned on pin number		
ısv	wer (Please select your correct option)	VuAr	
0	6		
0	T		
0	8		
0	9 <u>correct</u>	Made	

Q

Ŀ







Question No : 34 of 52	
8088 is a	
Answer (Please select your correct option)	VııAn
16 bit processor	
c 32 bit processor	
c 64 bit processor	
c 128 bit processor	Made







Question No : 35 of 52	
AX register in extended 32 bit register is renamed as	
Answer (Please select your correct option)	VıjAr
CAXE	
(EAX)	
C LAN	
CAAX	
CXAX	Made







Question No : 36 of 52	
The table index (TI) is set to to access the GDT (Global Descriptor Table).	
Answer (Please select your correct option)	VuAr
-1 c -1	
c -2	Made







Question No : 37 of 52

In 6	58K processors there is a program counter (PC) that holds the address of currentl	y executing instruction.
\nsv	wer (Please select your correct option)	VuAn
C	8 bit	
C	16 bit	
((32 bit	
c	64 bit	Made







uestion No : 38 of 52		
An 8 x 16 font is stored in b	ytes.	
Answer (Please select your correct option)	VuAr	
c 2		
с ⁴		
c 8		
16	Made	
	Mad	







estion No : 39 of 52	
'hich of the following interrupt is used in debugging with the help of trap flag?	
swer (Please select your correct option)	VuAn
INT 0 INT 1	
INT 2	
INT 3	Made







Question	No: 40) of 52
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In Programmable Interrupt Controller, which one of the following ports is referred as interrupt mask register?

Answer (Please select your correct option)

20 21 22 22	ade
20	
19	288 U.S. 20204 No.







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