



Grand Quiz Spring 2021

Subject Code CS401 lecture 1 to 22

Solved by Rizwan Qadeer

Dear Viewers:

I'm providing 100% correct quiz solution. You can visit my YouTube channel and get more information about all other subjects' quizzes and final year project (CS619).



Sialkot
Punjab, Pakistan



Rizwanqadeer848@gmail.com



<https://www.facebook.com/groups/923887914750307>



<https://www.youtube.com/channel/UCINsFwDiB62SValCcPDZbRQ/playlists>

RIZ MUGHAL (SQA ENGINEER)

Question # 1 of 30 (**Start time: 09:20:47 AM, 21 June 2021**)

In which of the following forms, a character is stored in memory while writing assembly language programs?

Select the correct option

- | | |
|----------------------------------|-------------|
| <input type="radio"/> | Octal code |
| <input checked="" type="radio"/> | ASCII code |
| <input type="radio"/> | Parity code |
| <input type="radio"/> | Scan code |

Click to Save

s

RIZ MUGHAL (SQA ENGINEER)

Question # 2 of 30 (Start time: 09:21:46 AM, 21 June 2021)

Which of following is an illegal assembly instruction?

Select the correct option

- | | |
|----------------------------------|--------------------|
| <input type="radio"/> | mov [num1], al |
| <input type="radio"/> | mov ax, bx |
| <input type="radio"/> | mov al, bl |
| <input checked="" type="radio"/> | mov [num1], [num2] |

Click to Save

Question # 3 of 30 (Start time: 09:22:09 AM, 21 June 2021)

In assembly language, the first executable instruction of the program should be placed at the offset _____.

Select the correct option

- | | |
|----------------------------------|--------|
| <input type="radio"/> | 0x1100 |
| <input type="radio"/> | 0x0010 |
| <input type="radio"/> | 0x0000 |
| <input checked="" type="radio"/> | 0x0100 |

Click to Save

Question # 4 of 30 (Start time: 09:22:29 AM, 21 June 2021)

CS and IP are both _____ bit registers.

Select the correct option

- | | |
|----------------------------------|----|
| <input checked="" type="radio"/> | 16 |
| <input type="radio"/> | 4 |
| <input type="radio"/> | 8 |
| <input type="radio"/> | 32 |

Click to Save

Question # 5 of 30 (Start time: 09:22:49 AM, 21 June 2021)

ASCII stands for _____.

Select the correct option

- American Standard Code for Information Interchange
- American Scientific Communication for Integer Interaction
- American Standard Communication for Integer Interchange
- American Standard Code for Information Interaction

Click to Save

Question # 6 of 30 (Start time: 09:23:10 AM, 21 June 2021)

The unit of stack operations in iAPX88 is a _____.

Select the correct option

- | | |
|----------------------------------|-------------|
| <input checked="" type="radio"/> | word |
| <input type="radio"/> | double word |
| <input type="radio"/> | byte |
| <input type="radio"/> | bit |

Click to Save

Question # 7 of 30 (Start time: 09:23:33 AM, 21 June 2021)

With the execution of CALL instruction, the value of _____ is decremented by 2.

Select the correct option

<input checked="" type="radio"/>	SP
<input type="radio"/>	BX
<input type="radio"/>	CX
<input type="radio"/>	DS

Click to Save

Question # 8 of 30 (Start time: 09:23:58 AM, 21 June 2021)

Which of the following instructions causes the size mismatch error?

Select the correct option

- | | |
|----------------------------------|--------------------|
| <input type="radio"/> | mov word[num1], bx |
| <input type="radio"/> | mov byte[num1], al |
| <input checked="" type="radio"/> | mov al, ax |
| <input type="radio"/> | mov ax, bx |

Click to Save

Question # 9 of 30 (Start time: 09:24:20 AM, 21 June 2021)

The mechanism used to drop the carry for making the computed address valid is known as _____.

Select the correct option

- | | |
|----------------------------------|---------------------|
| <input checked="" type="radio"/> | address wraparound |
| <input type="radio"/> | segment overlapping |
| <input type="radio"/> | carry overload |
| <input type="radio"/> | overflow |

Click to Save

Question # 10 of 30 (Start time: 09:24:42 AM, 21 June 2021)

BH register is a (an) _____ bit register.

Select the correct option

- | | |
|----------------------------------|----|
| <input type="radio"/> | 16 |
| <input type="radio"/> | 24 |
| <input type="radio"/> | 32 |
| <input checked="" type="radio"/> | 8 |

Click to Save

Question # 11 of 30 (Start time: 09:25:03 AM, 21 June 2021)

REP allows the instruction to be repeated _____ times.

Select the correct option

- | | |
|----------------------------------|----|
| <input type="radio"/> | DX |
| <input type="radio"/> | AX |
| <input type="radio"/> | BX |
| <input checked="" type="radio"/> | CX |

Click to Save

Question # 12 of 30 (Start time: 09:25:27 AM, 21 June 2021)

The most convenient place to store local variables is _____.

Select the correct option

- | | |
|----------------------------------|--------------|
| <input type="radio"/> | registers |
| <input type="radio"/> | main memory |
| <input type="radio"/> | cache memory |
| <input checked="" type="radio"/> | stack |

Click to Save

Question # 13 of 30 (Start time: 09:25:49 AM, 21 June 2021)

The routine which executes as a result of INT instruction is called _____.

Select the correct option

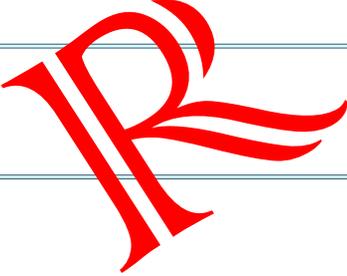
- | | |
|----------------------------------|---------------------------|
| <input type="radio"/> | Internal System Routine |
| <input type="radio"/> | Interrupt System Routine |
| <input type="radio"/> | Interrupt Vector Routine |
| <input checked="" type="radio"/> | Interrupt Service Routine |

Click to Save

Question # 14 of 30 (Start time: 09:26:10 AM, 21 June 2021)

In _____, every bit moves one position to the right, and the bit dropped from the right is inserted at the left. The carry flag.

Select the correct option

- | | |
|----------------------------------|----------------------------------|
| <input checked="" type="radio"/> | Rotate Right (ROR) |
| <input type="radio"/> | Rotate through Carry Left (RCL) |
| <input type="radio"/> | Rotate Left (ROL) |
| <input type="radio"/> | Rotate through Carry Right (RCR) |
- 

Click to Save

Question # 15 of 30 (Start time: 09:26:31 AM, 21 June 2021)

Which of the following has magnitude and sign?

Select the correct option

- | | |
|----------------------------------|--------------------|
| <input type="radio"/> | unsigned number |
| <input type="radio"/> | hexadecimal number |
| <input checked="" type="radio"/> | signed number |
| <input type="radio"/> | octal number |

Click to Save

Question # 16 of 30 (Start time: 09:26:55 AM, 21 June 2021)

Which of the following is a special instruction?

Select the correct option

- | | |
|----------------------------------|--------------|
| <input type="radio"/> | add bx, 0534 |
| <input checked="" type="radio"/> | cli |
| <input type="radio"/> | mov ax, bx |
| <input type="radio"/> | cmp ax, 0 |

Click to Save

Question # 17 of 30 (Start time: 09:27:17 AM, 21 June 2021)

The segment:offset pair is called a/an _____ address.

Select the correct option

- | | |
|----------------------------------|-----------|
| <input type="radio"/> | virtual |
| <input checked="" type="radio"/> | logical |
| <input type="radio"/> | effective |
| <input type="radio"/> | physical |

Click to Save

Question # 18 of 30 (Start time: 09:27:41 AM, 21 June 2021)

```
MOV AX, 0X4C00  
INT 0X21
```

Which of the following is the purpose of the given instructions?

Select the correct option

- | | |
|----------------------------------|---------------------|
| <input type="radio"/> | Variable definition |
| <input type="radio"/> | Move number into ax |
| <input checked="" type="radio"/> | Terminate a program |
| <input type="radio"/> | Move ax into 0X4C00 |

Click to Save

Question # 19 of 30 (Start time: 09:28:03 AM, 21 June 2021)

Extended Shifting Algorithm consists of:

Select the correct option

- 3 instructions
- 1 instruction
- 2 instructions
- 4 instructions

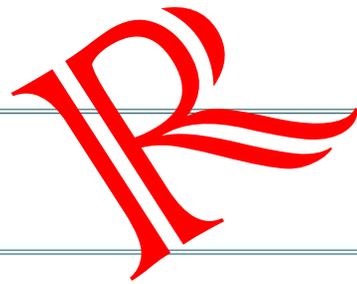
Click to Save

Question # 20 of 30 (Start time: 09:28:23 AM, 21 June 2021)

After the execution of STOSB and STOSW, the CX register will be _____ by _____.

Select the correct option

- | | |
|----------------------------------|----------------|
| <input type="radio"/> | incremented, 2 |
| <input type="radio"/> | incremented, 1 |
| <input checked="" type="radio"/> | decremented, 1 |
| <input type="radio"/> | decremented, 2 |



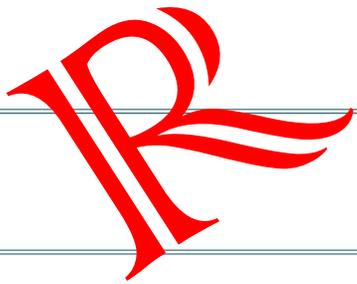
Click to Save

Question # 21 of 30 (Start time: 09:28:42 AM, 21 June 2021)

Registers are storage cells _____.

Select the correct option

- | | |
|----------------------------------|---------------------------------------|
| <input type="radio"/> | both inside and outside the processor |
| <input type="radio"/> | outside the processor |
| <input checked="" type="radio"/> | inside the processor |
| <input type="radio"/> | inside the memory |



Click to Save

Question # 22 of 30 (Start time: 09:29:06 AM, 21 June 2021)

Which of the following jump does not depend on flags register?

Select the correct option

<input type="radio"/>	JNE
<input type="radio"/>	JO
<input type="radio"/>	JP
<input checked="" type="radio"/>	JCXZ

Click to Save

Question # 23 of 30 (Start time: 09:29:28 AM, 21 June 2021)

In the instruction "mov word [es:0], 0x1230", 12 refers to _____ color on _____ background.

Select the correct option

- | | |
|----------------------------------|--------------|
| <input type="radio"/> | black, green |
| <input type="radio"/> | green, black |
| <input type="radio"/> | blue, green |
| <input checked="" type="radio"/> | green, blue |

Click to Save

Question # 24 of 30 (Start time: 09:29:53 AM, 21 June 2021)

sub sp, 2

The above instruction is used to perform a _____ operation on stack.

Select the correct option

- | | |
|----------------------------------|-------|
| <input type="radio"/> | POP |
| <input checked="" type="radio"/> | PUSH |
| <input type="radio"/> | RET n |
| <input type="radio"/> | RET |

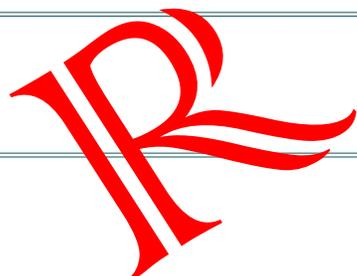
Click to Save

Question # 25 of 30 (Start time: 09:30:17 AM, 21 June 2021)

Which of the following addressing scheme has been used in the instruction MOV [BX], AX?

Select the correct option

- | | |
|----------------------------------|------------------------|
| <input checked="" type="radio"/> | Base Register Indirect |
| <input type="radio"/> | Base + Offset |
| <input type="radio"/> | Base + Index |
| <input type="radio"/> | Base Register Direct |



Click to Save

Question # 26 of 30 (Start time: 09:30:37 AM, 21 June 2021)

Which of the following is the interrupt number for Non-Maskable Interrupt (NMI)?

Select the correct option

- | | |
|----------------------------------|-------|
| <input type="radio"/> | INT 3 |
| <input checked="" type="radio"/> | INT 2 |
| <input type="radio"/> | INT 0 |
| <input type="radio"/> | INT 1 |

R

Click to Save

Question # 28 of 30 (Start time: 09:31:17 AM, 21 June 2021)

A symbol associated to an instruction in the program is known as:

Select the correct option

- | | |
|----------------------------------|------------|
| <input type="radio"/> | Definition |
| <input checked="" type="radio"/> | Label |
| <input type="radio"/> | Constant |
| <input type="radio"/> | Operand |

Click to Save

Question # 29 of 30 (Start time: 09:31:39 AM, 21 June 2021)

There are _____ registers in Intel 8088 that can hold the address of data.

Select the correct option

- | | |
|----------------------------------|---|
| <input type="radio"/> | 2 |
| <input type="radio"/> | 5 |
| <input type="radio"/> | 3 |
| <input checked="" type="radio"/> | 4 |

R

Click to Save

Question # 30 of 30 (Start time: 09:32:12 AM, 21 June 2021)

The IDTR is a _____ bit register similar in structure to the GDTR.

Select the correct option

- | | |
|----------------------------------|-----|
| <input type="radio"/> | 32 |
| <input type="radio"/> | 64 |
| <input type="radio"/> | 128 |
| <input checked="" type="radio"/> | 48 |

Click to Save