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Assignment, QUIZ, GDBZ and
GRAND QUIZ SERVICE are
available

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CS609 GRAND QUIZ

Sr. No.	MCQS	ANSWERS
1.	_____ is used to program various common parameters of transfer for all the channels.	DMA Command Register
2.	In _____ each byte is needed to be encapsulated in start and end.	Asynchronous communication
3.	_____ No. of bytes are used to store the character in the key board buffer.	2
4.	If NTFS volume is accessed in DOS then what will happen?	It will fire error of invalid media.
5.	There are two types of communication; synchronous and anti-synchronous	False
6.	Which of the following register is used, if you want to block access of any Hardware.	ISR
7.	LSN of FS Info block is available at _____.	FAT
8.	Counter register can be used to divide clock signal.	True
9.	The BIOS interrupt _____ can be used to configure RTC.	1AH
10.	The amount of memory above conventional memory (extended memory) can be determined using the service _____.	15H/88H
11.	_____ is a collection of contagious blocks.	Cluster
12.	If we want to send printing on the printer then we have to perform following steps.	All of the given
13.	NTFS volume can be accessed directly in DOS.	False
14.	DSR stands for _____.	Data set ready

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15.	DTE is _____.	Data terminal equipment
16.	In FAT12, to calculate the address or offset from index, we need to multiply it with _____.	3/2
17.	The _____ function initializes COM port whose number is passed as a parameter using BIOS services.	initialize()
18.	What will be the value of the word located at 1Fh in DPB when number of free clusters on drive is not known?	FFFFH
19.	DMA may not support memory to memory transfer	False
20.	The _____ service # is not used in any interrupt.	FF
21.	In order to produce the sound from PC internal Speaker we have to load the _____ bit divisor value at the _____ port.	16, 0x42
22.	There are two main types of interrupts, namely _____	Hardware interrupts and Software interrupts
23.	_____ is used to identify the cause of interrupt.	Interrupt ID register
24.	DOS has practically limit of _____ blocks per cluster.	64
25.	The Function of I/O controller is to provide _____.	All of given
26.	IVT is a table containing _____ byte entries each of which is a far address of an interrupt service routine.	4
27.	In NTFS, FAT and root directory has been replaced by _____.	MFT
28.	If we want to produce the shrill voice from the speaker phone then we have to load the _____ divisor value at Port _____.	high, 0x42
29.	Boot sector is the _____ logical sector of NTFS partition.	First
30.	A cluster is same as block.	Logically
31.	_____ whenever received indicates the start of communication and _____ whenever received indicates a temporary pause in the communication.	XON & XOFF
32.	_____ store the base address for LPT1.	40:08H

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33.	Each partition information chunk is 16 bytes long and the last two bytes at the end of the partition table data part is the partition table signature whose value should be _____ indicating that the code part contains valid executable code.	AA55
34.	To set the interrupt vector means is to change the double word sized interrupt vector within the IVT.	True
35.	DOS has a built in limit of _____ blocks per cluster.	128
36.	Total number of clusters of FAT12 are _____.	FEE H
37.	Standard PC operates in two modes in terms of memory which are	Real mode and protected mode
38.	BPB stands for _____.	BIOS parameter block
39.	Int _____ service 0 can be used to set the line parameter of the URT or COM port.	14H
40.	When LSN is equal to zero (0), it means _____.	LSN is relative address with respect to the start of Logical
41.	DCE is _____.	Data communication equipment
42.	Drive paramrter block is derived from _____.	BPB
43.	Control information about files is maintained using _____.	FCB
44.	We can access the contents of File by using the root directory entry and _____.	?
45.	The _____ function uses the COM port number to receive a byte from the COM port using BIOS services.	recievechar()
46.	When we mark a file as deleted by placing 0xE5 then the chain of clusters in FAT is also replaced by _____.	0
47.	n FAT32, lower _____ bits are used.	28 bits
48.	The service _____ is called the keyboard hook service.	15H/FFH

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49.	To store a cluster in FAT32 _____ is/are needed.	4 Bytes
50.	XON whenever received indicates the start of communication and XOFF whenever received indicates a temporary pause in the communication.	True
51.	_____ structure is a tree like structure.	DOS Directory
52.	The output on the monitor is controlled by a controller called _____ within the PC.	Video controller
53.	In case of synchronous communication a timing signal is required to identify the start and end of a bit.	True
54.	File can be _____ viewed as organization of data.	Logically
55.	In keyboard status byte bit no. 2 and 3 are used for ctrl and alt keys respectively. which of the following condition is used to check that Ctrl + Alt keys are pressed. Where: unsigned char far * scr = (unsigned char far*)(0x00400017);	if ((*scr)&12)==12)
56.	If the following function, keep(1, 1000); is called in the TSR program, it means that _____ No. of bytes are reserved in the memory.	16000
57.	Int 14H _____ can be used to set the line parameter of the UART or COM port.	Service # 0
58.	If printer is _____ then printer sends back the ACK signal to the printer interface.	Idle
59.	The keyboard makes use of interrupt number _____ for its input operations.	9
60.	The service number is usually placed in the _____ register.	AH
61.	Timer interrupt approximately occurs _____ times every second.	18.2
62.	Interrupt 9 is used to read scan code from _____.	Keyboard
63.	UART is a device used for _____ serial communication.	Asynchronous
64.	Int86() has _____ parameters.	3
65.	In standard PC _____ DMA's can be cascaded.	2
66.	Control word controls the _____ rate of keyboard.	Type matrix
67.	If we want to send printing command on the printer then we have to perform following steps.	Initialize , read status and checking
68.	If a device driver needs to change of CAPS LOCK on keyboard then, _____ should be written on the keyboard.	CAPS LOCK Byte
69.	The keyboard hook service is always called by interrupted _____ after acquiring the scan code from keyboard port .	9H

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70.	Size of FS Info block is _____.	512 bytes
71.	Software based flow control make use of _____ control characters	?
72.	The accumulative time that is required to access the selected block is called _____.	Access time
73.	At IRQ7 Interrupt # _____ is used.	x0F
74.	_____ is the first block on disk	LBA=0 block
75.	Only _____ ports are important from programming point of view.	70 and 71H
76.	The _____ function simply enables the self test facility within the modem control register.	SelfTestOn()
77.	_____ is used to read a block against its LSN.	absread()
78.	Int 14H _____ can be used to send a byte.	Service # 1
79.	When LSN is equal to zero (0), it means _____.	LSN is relative address with respect to the start of Logical
80.	The memory addresses of COM ports remain same for all computers.	False
81.	If FAT entry is between FFF0H to FFF6H in FAT16 then _____.	It is a Reserved cluster
82.	BIOS support _____ UARTS as COM ports.	4
83.	Here are _____ kinds of serial communication.	2
84.	_____ is a device incorporated into the PC to update time even if the computer is off.	Real time clock
85.	Int _____ is used to control the printer via the BIOS.	17H
86.	The size of each entry in FCB of FAT12 and FAT16 file systems is:	2 Bytes
87.	Total number of bytes that can be stored in Keyboard Buffer is _____	32
88.	The bit _____ of Line control register in UART, if set indicates that the DLL and DLM will act as the divisor register.	7
89.	Bit number _____ of port 64H of keyboard status register	7

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	indicate status of time-out error during input.	
90.	Suppose TX has current timer count stored in it and TIME is a pointer to timer count. Now this while loop will produce a delay of_____.	Quarter of a second
91.	_____stores the base address for LPT1.	40:08 H
92.	In interrupt 17H , if DX register contains zero, then I/O will be performed on_____	LPT1
93.	The speed of microprocessor depends on_____	Frequency of clock signal
94.	Interrupt _____is empty; we can use its vector as a flag.	65 H
95.	The interrupt no. 65 H is used _____	Neither by DOS nor by BIOS
96.	Timer interrupt is invoked by means of_____	Hardware
97.	In Real time clock, the status _____ is used to specify the date time formula.	Register B
98.	The UART devices uses _____ for error detection.	Parity Bit
99.	BIOS data area starts from _____.	40:00H
100.	The registers flags_____ are pushed on stack after execution of INT instruction and execution branches to the interrupt procedure.	CS and IP
101.	What is the output of the following: #include<DOS.H> Union REGS regs; Void main(void) { Regs.h.al=0x99; }	99
102.	Parameter in interrupts are passed through _____.	Registers
103.	The printer interface uses the_____.	IRQ 7
104.	While performing I/O on the printer using BIOS services, we must check_____ of status byte to see if printer is busy or not.	Most significant bit
105.	REGS is a _____.	Union
106.	Every ISR(interrupt service routine) should check service number in _____register and then perform the function accordingly.	AH
107.	The register which is not available in the UART device is_____	Interrupt focus register
108.	In the counter register of interval timer, bit number 3 changes its value between 0 and 1 within_____clock cycle	16

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109.	Operating system information is in a special block on disk which is called_____	Boot block
110.	The IPP acts as an interface between the CPU and a parallel_____	I/O device
111.	All interval timer channels are _____ bits wide.	16
112.	In interrupt software interrupts, the original interrupt can be invoked.	Anywhere in ISR
113.	_____is used to set date.	1A/05H
114.	Frequency of clock signal measures the speed of the _____	CPU
115.	To store each character in keyboard buffer _____bytes are required.	2
116.	In the context of COM ports. AH register is treated as a_____.	Line status register
117.	How many interrupts can be addressed via interrupt vector table?	256
118.	File is _____ viewed as an organization of data physically it can be collection of clusters or blocks.	Logically
119.	The output on the monitor is controlled by a controller called _____ within the PC.	Video controller
120.	There are _____ kinds of serial communication.	2
121.	Cluster number can also be referred as block number.	False
122.	In DOS we have limit of _____ blocks per cluster.	128
123.	_____ is used to read time from RTC.	1A/02H
124.	A cluster is same as block.	False
125.	_____ is Disk interrupt.	13H
126.	Total No. of bytes that can be stored in Keyboard Buffer is_____.	32
127.	The baud rate is set in accordance with the divisor value loaded within the UART internal registers base +0 and base +1.	True
128.	If we know the cluster number, we can access the blocks within the cluster using BIOS services directly.	False
129.	In FAT32, lower _____ bits are used.	28 bits

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130.	_____ register is used to store interrupt service number.	AL
131.	In case of asynchronous communication a timing, signal is not required to identify_____	Both start and end of the bit
132.	_____ is used to control the printer via BIOS.	INT 17H
133.	The keyboard buffer is a memory area reserved in the_____.	BIOS data area
134.	_____ notifies the end of interrupt(EOI) to the programmable interrupt controller?	Microprocessor
135.	The scan code for a key pressed would be available at port number_____.	Port 60
136.	The distinguishing factor between intel coprocessor 80287 and 80387 is that only _____ allow to reserve the sign of infinity.	80387
137.	In real time clock, when status register D indicates 0 then it shows that the batter is_____	Dead
138.	Bit number _____ of port 64H of keyboard status register is used for parity error checking.	7
139.	In self test mode of UART, the output of UART is_____ its input.	Different from
140.	"I/O devices are directly connected to the CPU"	NO