

Grand Quiz Spring 2021

Subject Code CS609 lecture 1 to 22

Solved By Riz Mughal

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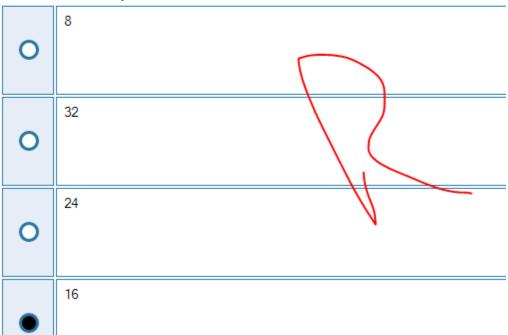
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RIZ MUGHAL (SQA ENGINEER)

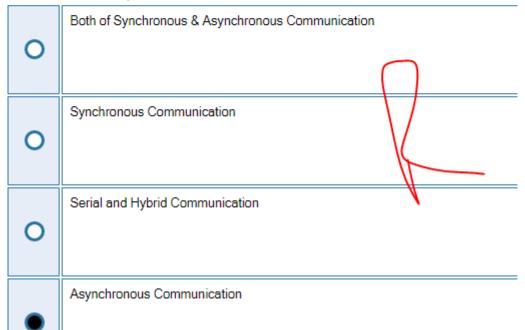
Question # 1 of 30 (Start time: 09:57:43 AM, 02 July 2021)

There are _____ number of hardware interrupts in interrupt vector table.



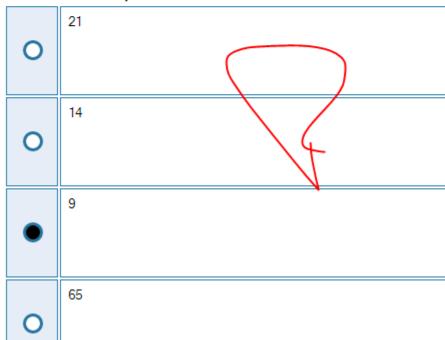
Question # 2 of 30 (Start time: 09:58:12 AM, 02 July 2021)

In ______ each byte is needed to be encapsulated in start and end.



Question # 3 of 30 (Start time: 09:58:36 AM, 02 July 2021)

Interrupt _____ is used for keyboard input operations.



CS609:Grand Quiz Question # 4 of 30 (Start time: 09:58:52 AM, 02 July 2021)

Keyboard makes use of interrupt _____ for its input operations.

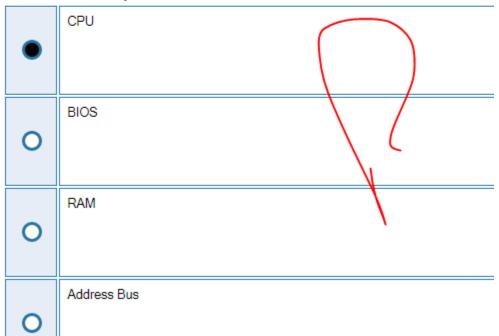


CS609:Grand Quiz Question # 5 of 30 (Start time: 09:59:08 AM, 02 July 2021) The service _____ is called the keyboard hook service. Select the correct option 15H/FFH 0 15H/4FH 15H/2FH 0 15H/2BH

CS609:Grand Quiz Quiz Start T Question # 6 of 30 (Start time: 09:59:22 AM, 02 July 2021) A code 0xFA on the port 60H (Keyboard Interface) indicates that the byte has been received _ Select the correct option With Errors 0 With acknowledgment 0 Properly Duplicate values

Question # 7 of 30 (Start time: 09:59:59 AM, 02 July 2021)

Frequency of clock signal measures the speed of the___



CS609:Grand Quiz Question # 8 of 30 (Start time: 10:00:33 AM, 02 July 2021) Logical view of organized data is called _____. Select the correct option Information 0 Code 0 Structure 0 File

CS609:Grand Quiz Question # 9 of 30 (Start time: 10:00:48 AM, 02 July 2021) In Self-Test mode of UART, the output of UART is _____ its input. Select the correct option Disconnected from 0 Interchanged with 0 Different from 0 Routed back to

Question # 10 of 30 (Start time: 10:01:03 AM, 02 July 2021) Display device (Monitor) performs ______. Select the correct option Both Memory mapped I/O and Isolated I/O 0 Isolated I/O 0 Event-driven I/O 0 Memory mapped I/O

CS609:Grand Quiz

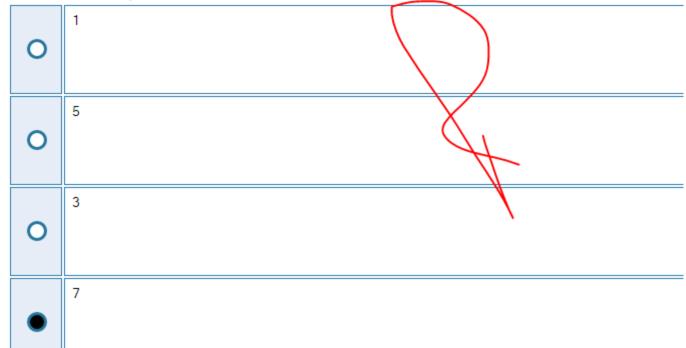
CS609:Grand Quiz Question # 11 of 30 (Start time: 10:01:19 AM, 02 July 2021) The Segment Address of IVT is fixed at _____. Select the correct option 000AH 0 000FH 0 0001H 0 0000H

CS609:Grand Quiz Question # 12 of 30 (Start time: 10:01:34 AM, 02 July 2021) BPB stands for_____ Select the correct option BIOS partition block 0 BIOS processing block 0 Base processing block 0 BIOS parameter block

CS609:Grand Quiz Question # 13 of 30 (Start time: 10:01:49 AM, 02 July 2021) NMI Stand for _____ Select the correct option Non Maximum Interrupt 0 Non Maskable Interrupt Non Multiplexed Interrupt 0 Non Multitude Interrupt

Question # 14 of 30 (Start time: 10:02:09 AM, 02 July 2021)

The bit _____ of Line control register in UART, if set, indicates that the DLL and DLM will act as the divisor register.



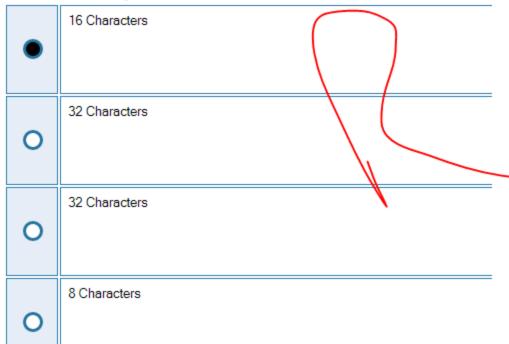
CS609:Grand Quiz Question # 15 of 30 (Start time: 10:02:25 AM, 02 July 2021) DMA is an abbreviation for ______. Select the correct option Data Managed Access 0 Distributed Memory Access 0 Direct Memory Access **Dual Memory Access**

CS609:Grand Quiz **Quiz Start Tin** Question # 16 of 30 (Start time: 10:03:01 AM, 02 July 2021) When getting system information through interrupt 11H, it places the returned information in _____ register. Select the correct option CX 0 AX DX 0 BX

CS609:Grand Quiz Question # 17 of 30 (Start time: 10:03:18 AM, 02 July 2021) Keyboard buffer works like a _____ data structure. Select the correct option Linked List 0 Circular Queue Stack 0 Queue

CS609:Grand Quiz Quiz St Question # 18 of 30 (Start time: 10:03:33 AM, 02 July 2021) In interrupt interception with software interrupts, the original interrupt can be invoked _____. Select the correct option In the middle of ISR 0 At the start of ISR Anywhere in ISR At the end of ISR

CS609:Grand Quiz Question # 19 of 30 (Start time: 10:03:47 AM, 02 July 2021) The capacity of keyboard buffer is _____.

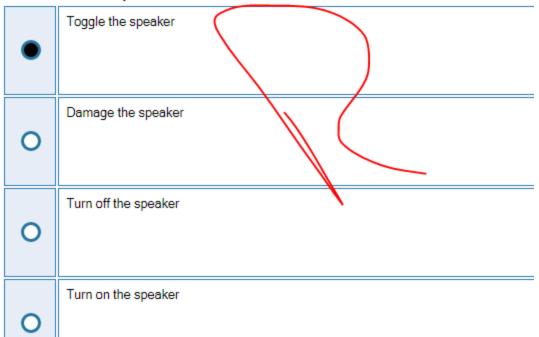


CS609:Grand Quiz Qι Question # 20 of 30 (Start time: 10:04:04 AM, 02 July 2021) In the context of Keyboard Interface, ____ means Typematic rate will be sent in next byte. Select the correct option 0xED 0 0xE5 0 0xF3 0xFF

CS609:Grand Quiz Question # 21 of 30 (Start time: 10:04:27 AM, 02 July 2021) IRQ1 is used by the _____interface. Select the correct option Scanner 0 Mouse 0 Keyboard Camera

Question # 22 of 30 (Start time: 10:04:44 AM, 02 July 2021)

The command "outport(0x61,inportb(0x61) ^ 3);" will ______.



CS609:Grand Quiz Quiz S Question # 23 of 30 (Start time: 10:05:01 AM, 02 July 2021) Communication between keyboard and keyboard controller is ___ Select the correct option Asynchronous serial O Parallel Communication 0 Synchronous serial Multiple serial

CS609:Grand Quiz Question # 24 of 30 (Start time: 10:05:17 AM, 02 July 2021) UART is the abbreviation for ______. Select the correct option Universal Asynchronous Receiver Transmitter Unity Asynchronous Receiver Transmitter 0 Uniform Asynchronous Receiver Transmitter 0 Used Asynchronous Receiver Transmitter

Question # 25 of 30 (Start time: 10:05:34 AM, 02 July 2021) In Keyboard Interface, _____ is LED control byte. Select the correct option 0xFF 0 0xED 0xF3 0 0xFA

CS609:Grand Quiz

CS609:Grand Quiz Question # 26 of 30 (Start time: 10:05:48 AM, 02 July 2021) Lower two bytes of any Far address contains _____ address. Select the correct option NULL 0 segment 0 offset both segment and offset

0

CS609:Grand Quiz Quiz Start Tim Question # 27 of 30 (Start time: 10:06:04 AM, 02 July 2021) All the modes of Interval Timer have different _____ wave pattern according to the need of the application. Select the correct option Sine 0 Haphazard 0 Trigonal 0 Square

CS609:Grand Quiz Quiz Start Time: 09:57 Question # 28 of 30 (Start time: 10:06:19 AM, 02 July 2021) If we want to produce the grave voice from speaker phone then we have to load the ____ divisor values at Port ___. Select the correct option low, 0x42 0 high, 0x22 0 high, 0x42 low, 0x22

Question # 29 of 30 (Start time: 10:06:35 AM, 02 July 2021) Graphic adapter is a(n) ______ Select the correct option I/O controller Memory 0 Software 0 Processor

CS609:Grand Quiz

CS609:Grand Quiz Quiz Start Time: Question # 30 of 30 (Start time: 10:06:49 AM, 02 July 2021) In order to produce the sound from PC internal Speaker we have to load the___bit divisor value at the ___port. Select the correct option 16, 0x42 64, 0x32 0 32, 0x22 0 8, 0x21

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